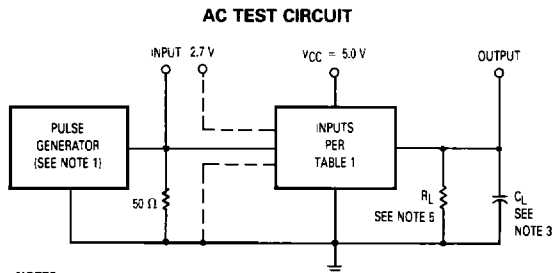
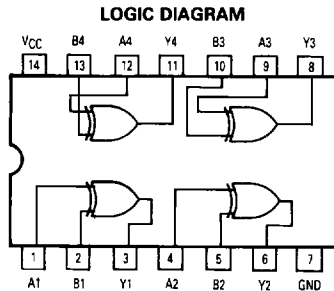


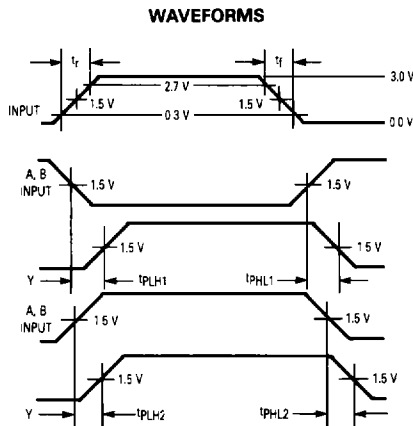


Quad 2-Input Exclusive OR Gate

ELECTRICALLY TESTED PER:
MIL-M-38510/34501



- NOTES:**
- V_{IN} = Input pulse generator has the following characteristics.
 $t_r = t_f \leq 2.5$ ns, PRR ≤ 1.0 MHz
 - Terminal conditions (pins not designated may be high ≈ 2.0 V, low ≈ 0.8 V, or open)
 - $C_L = 50$ pF $\pm 10\%$, including scope probe, wiring and stray capacitance, without package in test fixture
 - Voltage measurements are to be made with respect to network ground terminal.
 - $R_L = 499 \Omega \pm 5.0\%$.



Military 54F86



AVAILABLE AS:

- 1) JAN: JM38510/34501BXA
- 2) SMD: *
- 3) 883C: 54F86/BXAJC

X = CASE OUTLINE AS FOLLOWS:

- PACKAGE: CERDIP: C
CERFLAT: D
LCC: 2

*Call Factory for latest update

PIN ASSIGNMENTS

FUNCTION	DIL	FLATS	LCC	BURN-IN (CONDITION A)
A1	1	1	2	VCC
B1	2	2	3	VCC
Y1	3	3	4	OPEN
A2	4	4	6	VCC
B2	5	5	8	VCC
Y2	6	6	9	OPEN
GND	7	7	10	GND
Y3	8	8	12	OPEN
A3	9	9	13	VCC
B3	10	10	14	VCC
Y4	11	11	16	OPEN
A4	12	12	18	VCC
B4	13	13	19	VCC
VCC	14	14	20	VCC

BURN-IN CONDITIONS:
 $V_{CC} = 5.0$ V MIN/6.0 V MAX

TRUTH TABLE

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	0

5

54F86

Symbol	Parameter	Limits						Units	Test Condition (Unless Otherwise Specified)
		+25°C		+125°C		-55°C			
		Subgroup 1		Subgroup 2		Subgroup 3			
		Min	Max	Min	Max	Min	Max		
V _{OH}	Logical "1" Output Voltage	2.5		2.5		2.5		V	V _{CC} = 4.5 V, I _{OH} = -1.0 mA, V _{IL} = 0.8 V, V _{IN} = 2.0 V on other input.
V _{OL}	Logical "0" Output Voltage		0.5		0.5		0.5	V	V _{CC} = 4.5 V, I _{OL} = 20 mA, V _{IH} = 2.0 V on both inputs.
V _{IC}	Input Clamping Voltage		-1.2					V	V _{CC} = 4.5 V, I _{IN} = -18 mA, other input is open.
I _{IH1}	Logical "1" Input Current		20		20		20	μA	V _{CC} = 5.5 V, V _{IN} = 2.7 V, other input is open.
I _{IH2}	Logical "1" Input Current		100		100		100	μA	V _{CC} = 5.5 V, V _{IN} = 7.0 V, other input is open.
I _{IL}	Logical "0" Input Current	-0.03	-0.6	-0.03	-0.6	-0.03	-0.6	mA	V _{CC} = 5.5 V, V _{IN} = 0.5 V, other input is open.
I _{OD}	Diode Current	60		60		60		mA	V _{CC} = 4.5 V, V _{IN} = 5.5 V (both inputs), V _{OUT} = 2.5 V.
I _{OS}	Output Short Circuit Current	-60	-150	-60	-150	-60	-150	mA	V _{CC} = 5.5 V, V _{IN} = 5.5 V, other input = 0 V, V _{OUT} = 0 V.
I _{CCH}	Power Supply Current		18		18		18	mA	V _{CC} = 5.5 V, V _{IN} = 0 V, other input = 0 V.
I _{CC1}	Power Supply Current		23		23		23	mA	V _{CC} = 5.5 V, V _{IN} = 0 V (both inputs).
I _{CC2}	Power Supply Current		28		28		28		V _{CC} = 5.5 V, V _{IN} = 5.5 V (both inputs).
V _{IH}	Logical "1" Input Voltage	2.0		2.0		2.0		V	V _{CC} = 4.5 V.
V _{IL}	Logical "0" Input Voltage		0.8		0.8		0.8	V	V _{CC} = 4.5 V.
	Functional Tests	Subgroup 7		Subgroup 8A		Subgroup 8B			per Truth Table with V _{CC} = 4.5 V, (Repeat at) V _{CC} = 5.5 V, V _{INL} = 0.5 V, and V _{INH} = 2.5 V.

Symbol	Parameter	Limits						Units	Test Condition (Unless Otherwise Specified)
		+25°C		+125°C		-55°C			
		Subgroup 9		Subgroup 10		Subgroup 11			
		Min	Max	Min	Max	Min	Max		
t _{PHL1}	Propagation Delay Data-Output Output High-Low	1.5	6.5	1.0	8.0	1.0	8.0	ns	V _{CC} = 5.0 V, C _L = 50 pF, R _L = 499 Ω.
t _{PHL2}		1.5	5.5	1.0	7.0	1.0	7.0		
t _{PLH1}	Propagation Delay Data-Output Output Low-High	1.5	7.0	1.0	8.5	1.0	8.5	ns	V _{CC} = 5.0 V, C _L = 50 pF, R _L = 499 Ω.
t _{PLH2}		1.5	5.5	1.0	7.0	1.0	7.0		