

# MAXIM

## Dual/Quad Precision Op Amps

### General Description

The Maxim LT1013 is a precision dual op amp that upgrades the performance of popular devices such as the MC1458/MC1558, LM158 and OP221. The Maxim LT1014 is a precision quad op amp that directly upgrades designs in the industry-standard 14-pin DIP configuration and has specifications similar to the LT1013.

Precision specifications include: 40 $\mu$ V offset voltage, 0.3 $\mu$ V/ $^{\circ}$ C drift (TCV<sub>OS</sub>), 117dB CMRR, and 120dB PSRR. While supply current is typically only 350 $\mu$ A per amplifier, the outputs can source and sink more than 20mA.

Both the LT1013 and the LT1014 can be operated from a single +5V power supply. The input voltage range includes ground and the outputs swing to within a few millivolts of ground.

### Applications

Battery-Powered Precision Instrumentation  
 Strain-Gauge Signal Conditioners  
 Thermocouple Amplifiers  
 Instrumentation Amplifiers  
 4mA to 20mA Current-Loop Transmitters  
 Multiple-Limit Threshold Detection  
 Active Filters  
 Multiple Gain Blocks

### Features

- ◆ Single-Supply Operation  
 Input Voltage Range Extends to Ground  
 Output Swings to Ground while Sinking Current
- ◆ 150 $\mu$ V Max Offset Voltage
- ◆ Low Drift: 2 $\mu$ V/ $^{\circ}$ C Max
- ◆ 0.8nA Max Offset Current
- ◆ Guaranteed High Gain  
 5mA Load Current: 1.5 Million Min  
 17mA Load Current: 0.8 Million Min
- ◆ 500 $\mu$ A Max Supply Current per Amplifier
- ◆ Low Voltage Noise: 0.1Hz to 10Hz, 0.55 $\mu$ V<sub>p-p</sub>
- ◆ Lower Current Noise than OP07: 0.07 pA/ $\sqrt{Hz}$

### Ordering Information

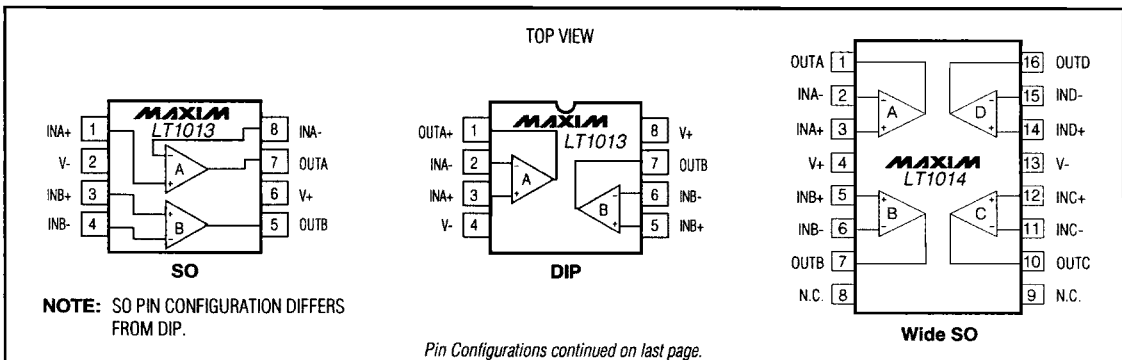
PART	TEMP. RANGE	PIN-PACKAGE
LT1013CN8	0 $^{\circ}$ C to +70 $^{\circ}$ C	8 Plastic DIP
LT1013DN8	0 $^{\circ}$ C to +70 $^{\circ}$ C	8 Plastic DIP
LT1013DS8	0 $^{\circ}$ C to +70 $^{\circ}$ C	8 SO
LT1013DC/D	0 $^{\circ}$ C to +70 $^{\circ}$ C	Dice*
LT1013IN8	-40 $^{\circ}$ C to +85 $^{\circ}$ C	8 Plastic DIP
LT1013IS8	-40 $^{\circ}$ C to +85 $^{\circ}$ C	8 SO
LT1013AMJ8	-55 $^{\circ}$ C to +125 $^{\circ}$ C	8 CERDIP**
LT1013MJ8	-55 $^{\circ}$ C to +125 $^{\circ}$ C	8 CERDIP**

Ordering Information continued on last page.

\* Dice are specified at  $T_A = +25^{\circ}$ C, D.C. parameters only.

\*\*Contact factory for availability and processing to MIL-STD-883.

### Pin Configurations



# Dual/Quad Precision Op Amps

## ABSOLUTE MAXIMUM RATINGS

Supply Voltage.....	±22V	14-Pin CERDIP (derate 9.09mW/°C above +70°C).....	727mW
Input Voltage.....	Equal to Positive Supply Voltage	16-Pin Wide SO (derate 9.52mW/°C above +70°C).....	762mW
	5V Below Negative Supply Voltage	Operating Temperature Ranges:	
Output Short-Circuit Duration.....	Continuous	LT1013/LT1014C.....	0°C to +70°C
Differential Input Voltage.....	±30V	LT1013/LT1014L.....	-40°C to +85°C
Continuous Power Dissipation (T <sub>A</sub> = +70°C)		LT1013/LT1014AM_M.....	-55°C to +125°C
8-Pin Plastic DIP (derate 9.09mW/°C above +70°C).....	727mW	Storage Temperature Range.....	-65°C to +150°C
8-Pin SO (derate 5.88mW/°C above +70°C).....	471mW	Lead Temperature (soldering, 10sec).....	+300°C
8-Pin CERDIP (derate 8.00mW/°C above +70°C).....	640mW		
14-Pin Plastic DIP (derate 10.00mW/°C above +70°C).....	800mW		

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

(V<sub>S</sub> = ±15V, V<sub>CM</sub> = 0V, T<sub>A</sub> = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	LT1013AM LT1014AM			LT1013C/D/WM LT1014C/D/WM			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
Input Offset Voltage	V <sub>OS</sub>	LT1013		40	150		60	300	μV
		LT1014		50	180		60	300	
		LT1013D/I, LT1014D/I					200	800	
Long-Term Input Offset Voltage Stability				0.4			0.5	μV/Mo.	
Input Offset Current	I <sub>OS</sub>			0.15	0.80		0.2	1.5	nA
Input Bias Current	I <sub>B</sub>			12	20		15	30	nA
Input Noise Voltage	e <sub>n</sub>	0.1Hz to 10Hz		0.55			0.55		μV <sub>p-p</sub>
Input Noise-Voltage Density	e <sub>n</sub>	f <sub>O</sub> = 10Hz		24			24		nV/√Hz
		f <sub>O</sub> = 1000Hz		22			22		
Input Noise-Current Density	i <sub>n</sub>	f <sub>O</sub> = 10Hz		0.07			0.07		pA/√Hz
Input Resistance (Note 1)		Differential	100	400		70	300		MΩ
		Common mode		5			4		GΩ
Large-Signal Voltage Gain	A <sub>VOL</sub>	V <sub>O</sub> = ±10V, R <sub>L</sub> = 2kΩ	1.5	8.0		1.2	7.0		V/μV
		V <sub>O</sub> = ±10V, R <sub>L</sub> = 600Ω	0.8	2.5		0.5	2.0		
Input Voltage Range			+13.5	+13.8		+13.5	+13.8		V
			-15.0	-15.3		-15.0	-15.3		
Common-Mode Rejection Ratio	CMRR	V <sub>CM</sub> = +13.5V, -15.0V	100	117		97	114		dB

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LT1013/LT1014

## ELECTRICAL CHARACTERISTICS (continued)

( $V_S = \pm 15V$ ,  $V_{CM} = 0V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	LT1013AM LT1014AM			LT1013C/D/I/M LT1014C/D/I/M			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
Power-Supply Rejection Ratio	PSRR	$V_S = \pm 2V$ to $\pm 18V$	103	120		100	117		dB
Channel Separation		$V_O = \pm 10V$ , $R_L = 2k\Omega$	123	140		120	137		dB
Output Voltage Swing	$V_{OUT}$	$R_L = 2k\Omega$	$\pm 13$	$\pm 14$		$\pm 12.5$	$\pm 14$		V
Slew Rate			0.2	0.4		0.2	0.4		V/ $\mu s$
Supply Current	$I_S$	Per amplifier		0.35	0.50		0.35	0.55	mA

**Note 1:** Guaranteed by design.

## ELECTRICAL CHARACTERISTICS

( $V_{S+} = +5V$ ,  $V_{S-} = 0V$ ,  $V_{OUT} = +1.4V$ ,  $V_{CM} = 0V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	LT1013AM LT1014AM			LT1013C/D/I/M LT1014C/D/I/M			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
Input Offset Voltage	$V_{OS}$	LT1013		60	250		90	450	$\mu V$
		LT1014		70	280		90	450	
		LT1013D/I, LT1014D/I					250	950	
Input Offset Current	$I_{OS}$			0.2	1.3		0.3	2.0	nA
Input Bias Current	$I_B$			15	35		18	50	nA
Large-Signal Voltage Gain	$A_{VOL}$	$V_O = 5mV$ to $4V$ , $R_L = 500\Omega$		1.0			1.0		V/ $\mu V$
Input Voltage Range			+3.5	+3.8		+3.5	+3.8	V	
			0	-0.3		0	-0.3		
Output Voltage Swing	$V_{OUT}$	Output low, no load		15	25		15	25	mV
		Output low, $600\Omega$ to ground		5	10		5	10	
		Output low, $I_{SINK} = 1mA$		220	350		220	350	
		Output high, no load	4.0	4.4		4.0	4.4	V	
		Output high, $600\Omega$ to ground	3.4	4.0		3.4	4.0		
Supply Current	$I_S$	Per amplifier		0.31	0.45		0.32	0.50	mA

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# Dual/Quad Precision Op Amps

## ELECTRICAL CHARACTERISTICS

( $V_S = \pm 15V$ ,  $V_{CM} = 0V$ ,  $T_A = -40^\circ C$  to  $+85^\circ C$  for LT1013I and LT1014I,  $T_A = 0^\circ C$  to  $+70^\circ C$  for LT1013C/D and LT1014C/D, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	LT1013C/D/ LT1014C/D/I			UNITS	
			MIN	TYP	MAX		
Input Offset Voltage	$V_{OS}$	LT1013C, LT1014C		80	400	$\mu V$	
		LT1013D/I, LT1014D/I		230	1000		
		LT1013C, LT1014C: $V_S = 5V, 0V, V_O = 1.4V$		110	570		
		LT1013D/I, LT1014D/I: $V_S = 5V, 0V, V_O = 1.4V$		280	1200		
Input Offset-Voltage Drift	$TCV_{OS}$	(Note 1)		0.4	2.5	$\mu V/^\circ C$	
		LT1013D/I, LT1014D/I (Note 1)		0.7	5.0		
Input Offset Current	$I_{OS}$			0.3	2.8	nA	
		$V_S = 5V, 0V; V_O = 1.4V$		0.5	6.0		
Input Bias Current	$I_B$			16	38	nA	
		$V_S = 5V, 0V; V_O = 1.4V$		24	90		
Large-Signal Voltage Gain	$A_{VOL}$	$V_O = \pm 10V, R_L = 2k\Omega$	0.7	4.0		V/ $\mu V$	
Common-Mode Rejection Ratio	CMRR	$V_{CM} = +13.0V, -15.0V$	94	113		dB	
Power-Supply Rejection Ratio	PSRR	$V_S = \pm 2V$ to $\pm 18V$	97	116		dB	
Output Voltage Swing	$V_{OUT}$	$R_L = 2k\Omega$	$\pm 12.0$	$\pm 13.9$		V	
		$V_S = 5V, 0V,$ $R_L = 600\Omega$	Output low	6	13		mV
			Output high	3.2	3.9		V
Supply Current per Amplifier	$I_S$			0.37	0.60	mA	
		$V_S = 5V, 0V, V_O = 1.4V$		0.34	0.55		

# Dual/Quad Precision Op Amps

LT1013/LT1014

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## ELECTRICAL CHARACTERISTICS

( $V_S = \pm 15V$ ,  $V_{CM} = 0V$ ,  $T_A = -55^\circ C$  to  $+125^\circ C$ , unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	LT1013AM			LT1014AM			LT1013M LT1014M			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
Input Offset Voltage	$V_{OS}$	$V_S = 5V, 0V, V_O = 1.4V$			80	300	90	350	110	550	$\mu V$	
			$T_A = -55^\circ C$ to $+100^\circ C$ (Note 2)		80	450	90	480	100	750		
			$T_A = +125^\circ C, V_{CM} = 0.1V$		120	450	150	480	200	750		
			$T_A = +125^\circ C, V_{CM} = 0V$		250	900	300	960	400	1500		
Input Offset-Voltage Drift	$TCV_{OS}$	(Note 1)	0.4	2.0	0.4	2.0	0.5	2.5	$\mu V/^\circ C$			
Input Offset Current	$I_{OS}$		0.3	2.5	0.3	2.8	0.4	5.0	nA			
		$V_S = 5V, 0V; V_O = 1.4V$	0.6	6.0	0.7	7.0	0.9	10.0				
Input Bias Current	$I_B$		15	30	15	30	18	45	nA			
		$V_S = 5V, 0V; V_O = 1.4V$	20	80	25	90	28	120				
Large-Signal Voltage Gain	$A_{VOL}$	$V_O = \pm 10V, R_L = 2k\Omega$	0.5	2.0	0.4	2.0	0.25	2.0	$V/\mu V$			
Common-Mode Rejection Ratio	CMRR	$V_{CM} = +13.0V, -14.9V$	97	114	96	114	94	113	dB			
Power-Supply Rejection Ratio	PSRR	$V_S = \pm 2V$ to $\pm 18V$	100	117	100	117	97	116	dB			
Output Voltage Swing	$V_{OUT}$	$R_L = 2k\Omega$	$\pm 12.0$	$\pm 13.8$	$\pm 12.0$	$\pm 13.8$	$\pm 11.5$	$\pm 13.8$	V			
		$V_S = 5V, 0V, R_L = 600\Omega$ to ground	Output low	6	15	6	15	6	18	mV		
			Output high	3.2	3.8	3.2	3.8	3.1	3.8	V		
Supply Current per Amplifier	$I_S$		0.38	0.60	0.38	0.60	0.38	0.70	mA			
		$V_S = 5V, 0V; V_O = 1.4V$	0.34	0.55	0.34	0.55	0.34	0.65				

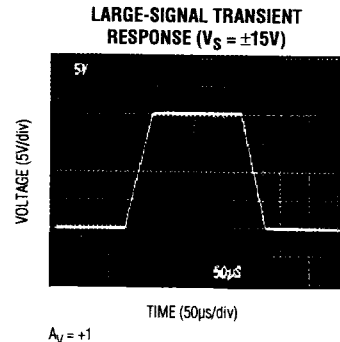
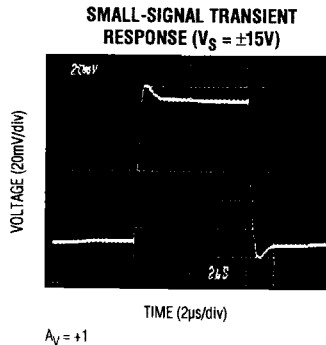
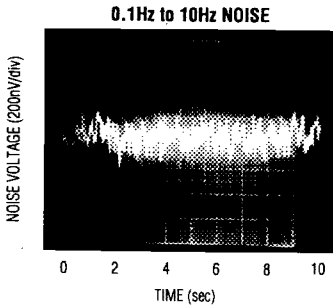
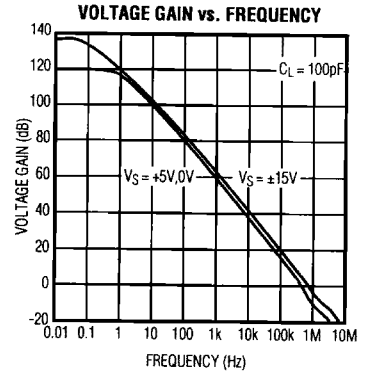
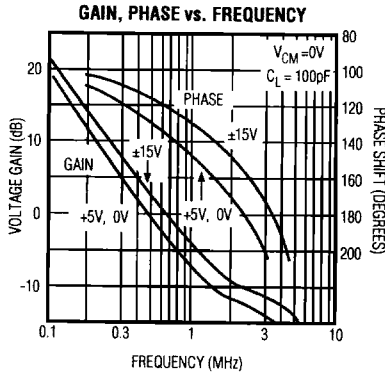
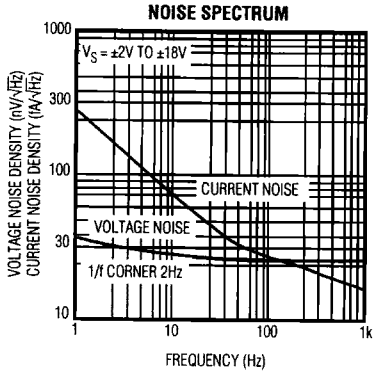
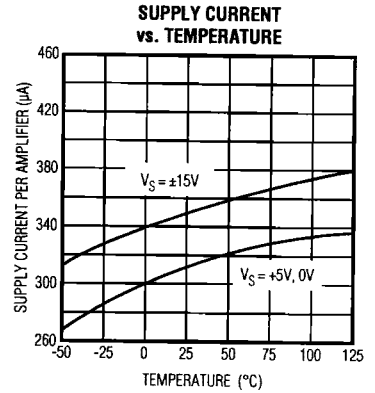
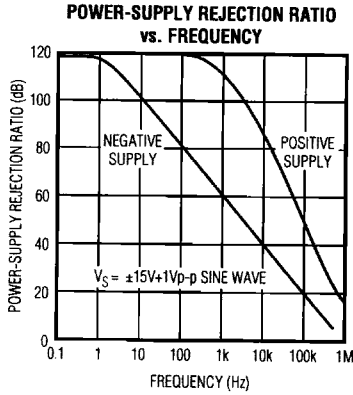
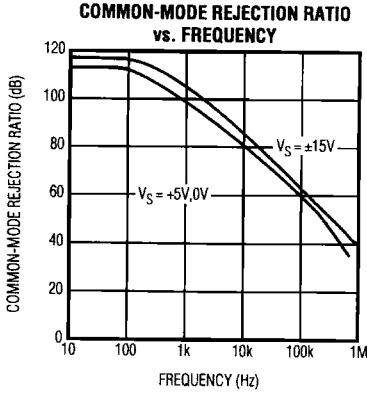
**Note 1:** Guaranteed by design.

**Note 2:** This parameter is guaranteed by design and is not tested.

# Dual/Quad Precision Op Amps

## Typical Operating Characteristics

( $T_A = +25^\circ\text{C}$ , unless otherwise noted.)

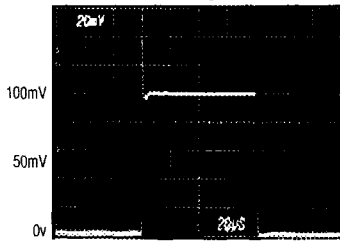


# Dual/Quad Precision Op Amps

## Typical Operating Characteristics (continued)

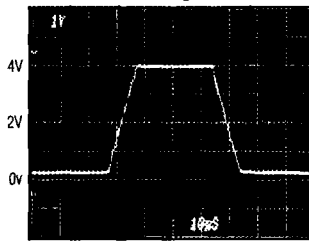
LT1013/LT1014

**SMALL-SIGNAL TRANSIENT RESPONSE ( $V_S = 5V, 0V$ )**



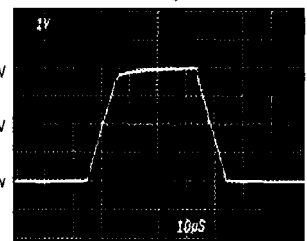
$A_V = +1$   
 $R_L = 600\Omega$  to GND

**LARGE-SIGNAL TRANSIENT RESPONSE ( $V_S = 5V, 0V$ )**



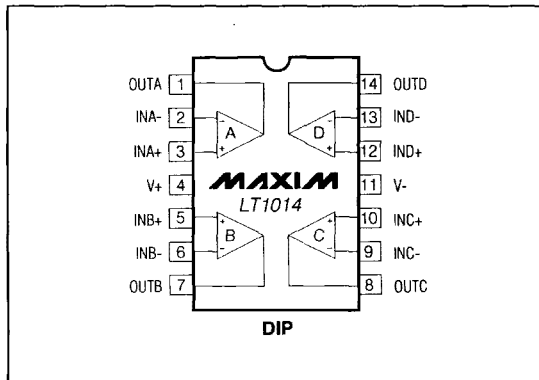
$A_V = +1$   
 $R_L = 4.7k\Omega$  to 5V

**LARGE-SIGNAL TRANSIENT RESPONSE ( $V_S = 5V, 0V$ )**

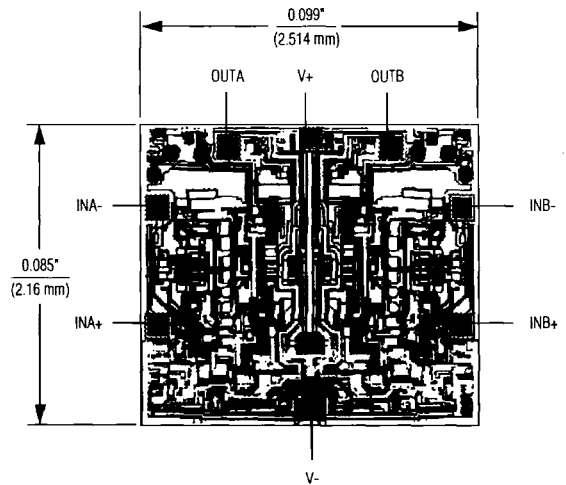


$A_V = +1$   
No load

## Pin Configurations (continued)



## Chip Topography



## Ordering Information (continued)

PART	TEMP. RANGE	PIN-PACKAGE
LT1014CN	0°C to +70°C	14 Plastic DIP
LT1014DS	0°C to +70°C	16 Wide SO
LT1014DN	0°C to +70°C	14 Plastic DIP
LT1014IN	-40°C to +85°C	14 Plastic DIP
LT1014IS	-40°C to +85°C	16 Wide SO
LT1014AMJ	-55°C to +125°C	14 CERDIP**
LT1014MJ	-55°C to +125°C	14 CERDIP**

\*\*Contact factory for availability and processing to MIL-STD-883.

