TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC257FN TC74AC258FN

TC74AC257FN 2-Channel Multiplexer (3-state) TC74AC258FN 2-Channel Multiplexer (3-state, inverting)

The TC74AC257 and TC74AC258 are advanced high speed CMOS MULTIPLEXERs fabricated with silicon gate and double-layer metal wiring C2MOS technology.

They achieve the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Each is composed of four independent 2-channel multiplexers with common SELECT and OUTPUT ENABLE (OE).

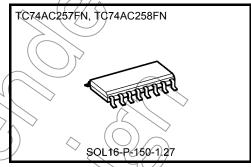
The TC74AC257 is a non-inverting multiplexer, while the TC74AC258 is an inverting.

If OE is set low, the outputs are held in a high-impedance state. When SELECT is set low, "A" data inputs are enabled.

Conversely, when SELECT is high, "B" data inputs are

enabled.

Note: xxxFN (JEDEC SOP) is not available in Japan:



Weight

SOL16-P-150-1.27

: 0.13 g (typ.)

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

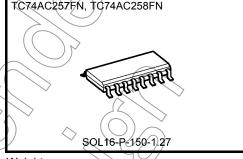
Features

- High speed: $t_{pd} = 3.6 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ N}$
- Low power dissipation: $I_{CC} = 8 \mu A \text{ (max) at Ta} = 25^{\circ}C$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Symmetrical output impedance: $|I_0H| = I_0L = 24 \text{ mA (min)}$

Capability of driving $50~\Omega$

transmission lines.

- Balanced propagation delays; toLH ~ tpHL
- Wide operating voltage range: V_{CC} (opr) = 2~5.5 V
- Pin and function compatible with 74F257/258



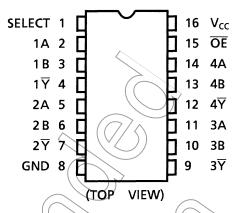
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Pin Assignment

TC74AC257

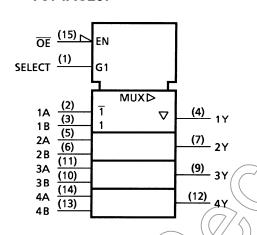
SELECT 1 16 V_{CC} 1A 2 15 <u>OE</u> 1B 3 14 4A 1Y 4 13 4B 2A 5 12 4Y 2B 6 3A 11 3B 2Y 7 10 GND 8 **3Y** (TOP VIEW)

TC74AC258

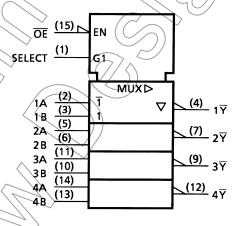


IEC Logic Symbol

TC74AC257



TC74AC258



Truth Table

			\ / /		
	Input	Outputs			
ŌĒ	SELECT	A	В	Y (257)	Y (258)
Н	x \	××	X	Z	z
L	L		/x	L _(Н
L	L((H	Х	Н	L
L	Ä	$ \angle x $	L_>	7	H
(L	1	, X	H	\wedge) L

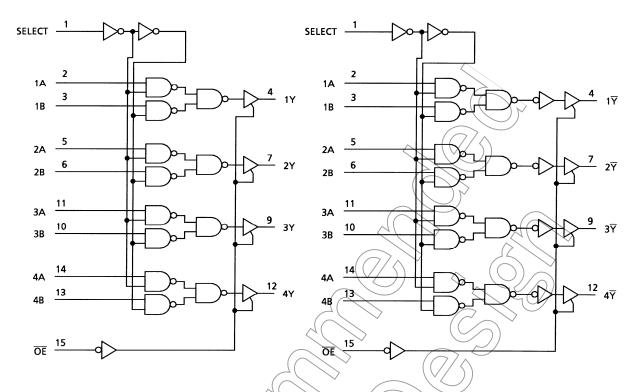
X: Don't care

Z: High impedance

System Diagram

TC74AC257

TC74AC258



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	ACC	-0.5~7.0	V
DC input voltage	VIN	-0.5~VCC + 0.5	V
DC output voltage	√ Vout	-0.5-V _{CC} + 0.5	V
Input diode current	<i>∭</i> κ	±20	mA
Output diode current	lok <	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±100	mA
Power dissipation	PD	180	mW
Storage temperature	Tstg	-65~150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0~5.5	V
Input voltage	V _{IN}	0~V _{CC}	V
Output voltage	V _{OUT}	0~V _{CC}	⟨ v
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dV	$0\sim100 \ (V_{CC}=3.3\pm0.3 \ V)$	ns/V
input rise and rail time	avav	$0\sim20 \ (V_{CC} = 5 \pm 0.5 \ V)$	

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

					/ / /					
					Ta = 25°C		Ta = -40~85°C			
Characteristics	Symbol	Test Condition		V(V)	Min	Тур.	Max	Min	Max	Unit
				2.0	1.50	-(7	1.50	_	
High-level input voltage	V_{IH}		</td <td>3.0</td> <td>2.10</td> <td>_(</td> <td>(\mathcal{I})</td> <td>2.10</td> <td>_</td> <td>V</td>	3.0	2.10	_((\mathcal{I})	2.10	_	V
				5.5	3.85	(7)	\leq	3.85	_	
I am land bank				2.0		(4)	0.50	_	0.50	
Low-level input voltage	V_{IL}			3.0/		\ <u> </u>	0.90	_	0.90	V
				5.5	_))—	1.65	_	1.65	
				2.0	1.9	2.0	_	1.9	_	
		V _{IN} = V _{IH} or V _{Ib}	1 _{OH} = -50 μA	3.0	2.9	3.0	_	2.9	_	V
High-level output	V _{OH}			4.5	4.4	4.5	_	4.4	_	
voltage			I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	
			I _{OH} = -24 mA	4.5	3.94	_	_	3.80	_	
			I _{OH} = -75 mA (Note)	5.5	_	_	_	3.85	_	
				2.0	_	0.0	0.1	_	0.1	
	√ Vol	V _{IN} = V _{IH} or	OL = 50 μA	3.0	_	0.0	0.1	_	0.1	V
Low-level output				4.5	_	0.0	0.1	_	0.1	
voltage		VIL	I _{OL} = 12 mA	3.0	_	_	0.36	_	0.44	V
			I _{OL} = 24 mA	4.5	_	_	0.36	_	0.44	
			I _{OL} = 75 mA (Note)	5.5	_	_	_	_	1.65	
3-state output off-state current	loz	$V_{IN} = V_{IF}$	or V _{IL} V _{CC} or GND	5.5	_	_	±0.5	_	±5.0	μΑ
Input leakage current	IN	V _{IN} = V _{CC} or GND			_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	$V_{IN} = V_C$	_C or GND	5.5	_	_	8.0	_	80.0	μΑ

Note: This spec indicates the capability of driving 50 Ω transmission lines.

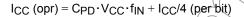
One output should be tested at a time for a 10 ms maximum duration.

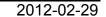
AC Characteristics (CL = 50 pF, RL = 500 Ω , input: t_r = t_f = 3 ns)

Characteristics	Symbol Test Condition -	Tost Condition		Ta = 25°C			Ta = -4	Unit	
Characteristics		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
Propagation delay time	t _{pLH}	_	3.3 ± 0.3	_	7.0	14.3	1.0	16.3	ns
(A, B-Y, \overline{Y})	t _{pHL}		5.0 ± 0.5	_	4.7	7.5	1.0	8.5	
Propagation delay time	t _{pLH}	_	3.3 ± 0.3	_	8.6	17.2	1.0	19.6	ns
(SELECT-Y, \overline{Y})	t _{pHL}		5.0 ± 0.5	_	5.5	9.1	7.0	10.4	110
Output enable time	t _{pZL}	_	3.3 ± 0.3	_ <	7.3	14.0	1.0	16.0	ns
Output chable time	t _{pHZ}	_	5.0 ± 0.5	_//	5.0	7.9	1.0	9.0	13
Output disable time	t_{pLZ}	_	3.3 ± 0.3	-(5.6	9.6	1.0	11.0	ns
Output disable time	t _{pHZ}	_	5.0 ± 0.5	_ \	5.1	7.9	1.0	9.0	113
Input capacitance	C _{IN}	_	,		5	10		10	pF
Output capacitance	C _{OUT}			1	10		4	\searrow	pF
Power dissipation	C _{PD}		(7)		28			> _	pF
capacitance	(Note)	_		ノナ	200	7)	ы

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

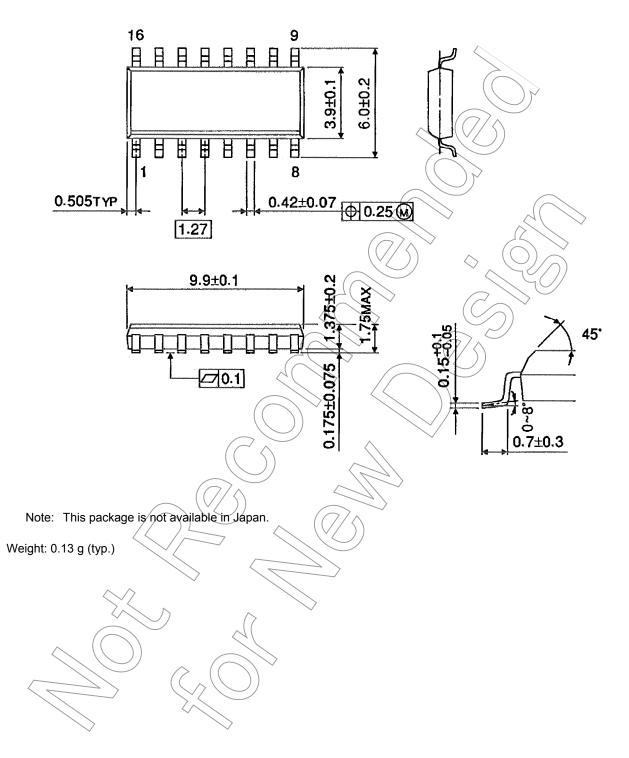
Average operating current can be obtained by the equation:





Package Dimensions (Note)

SOL16-P-150-1.27 Unit: mm



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