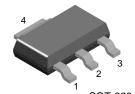


FZT790A

PNP Low Saturation Transistor

• These devices are designed with high current gain and low saturation voltage with collector currents up to 3A continuous.



1. Base 2.4. Collector 3. Emitter

Absolute Maximum Ratings * T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CEO}	Collector-Emitter Voltage	40	V
V _{CBO}	Collector-Base Voltage	50	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current - Continuous	3	Α
T _J , T _{STG}	Operating and Storage Junction Temperature Range	- 55 ~ + 150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Electrical Characteristics T_A=25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Characte	eristics	•		•		
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA, I _B = 0	60			V
BV _{CBO}	Collector-Emitter Breakdown Voltage	$I_C = 100 \mu A, I_E = 0$	80			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 100 \mu A, I_C = 0$	5			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 30V, I_E = 0$			100 10	nA
1	Emitter Cut off Current	$V_{CB} = 30V, I_E = 0, T_A = 100^{\circ}C$				μA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 4V, I_{C} = 0$			100	nA
On Characte	eristics *					
h _{FE}	DC Current Gain	$I_C = 100 \text{mA}, V_{CE} = 2 \text{V}$	70			
		$I_C = 500 \text{mA}, V_{CE} = 2V$	250		550	
		$I_C = 1A$, $V_{CE} = 2V$	80			
		$I_C = 2A$, $V_{CE} = 2V$	40			
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 1A, I _B = 100mA			300	mV
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 1A, I _B = 100mA			1.25	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = 1A, V _{CE} = 2V			1	V
Small Signal	Characteristics	•				
C _{obo}	Output Capacitance	V _{CB} = 10V, I _E = 0, f = 1MHz			45	pF
f _T	Transition Frequency	I _C = 10mA, V _{CE} = 5V, f = 100MHz	75			MHz

^{*} Pulse Test: Pulse Width ≤300µs, Duty Cycle ≤ 2.0%

NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics

Symbol	Parameter	Max.	Units
P _D	Total Device Dissipation	2	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		°C/W

These ratings are based on a maximum junction temperature of 150degrees C.
These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Typical Characteristics

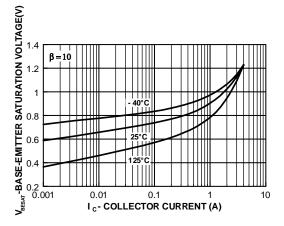


Figure 1. Base-Emitter Saturation Voltage vs Collector Current

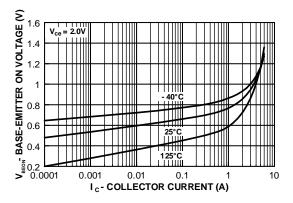


Figure 2. Base-Emitter On Voltage vs Collector Current

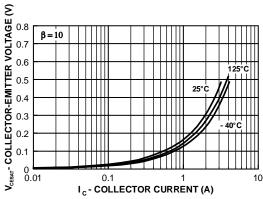


Figure 3. Collector-Emitter Saturation Voltage vs Collector Current

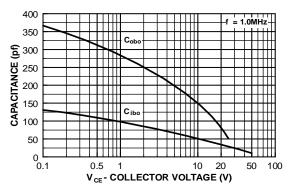


Figure 4. Input/Output Capacitance vs Reverse Bias Voltage

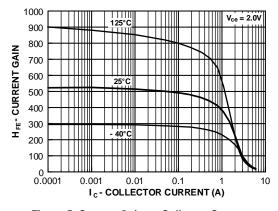


Figure 5. Current Gain vs Collector Current

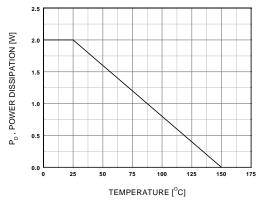
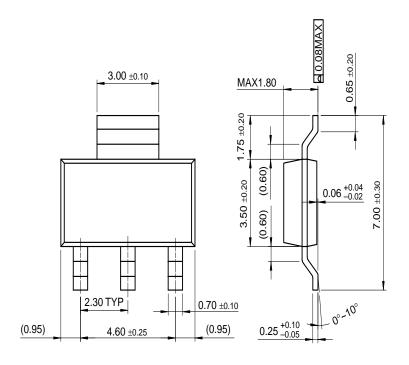


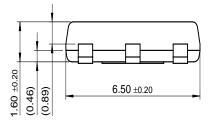
Figure 6. Power Dissipation vs Ambient Temperature

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Package Demensions

SOT-223





Dimensions in Millimeters

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