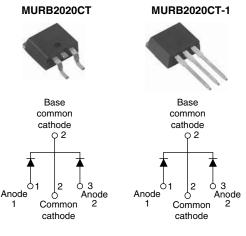


Vishay High Power Products

Ultrafast Rectifier, 2 x 10 A FRED Pt[™]



D²PAK

TO-262

PRODUCT SUMMARY				
t _{rr}	25 ns			
I _{F(AV)}	2 x 10 A			
V _R	200 V			

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL TEST CONDITIONS		MAX.	UNITS
Peak repetitive reverse voltage		V _{RRM}		200	V
Average rectified forward current	per leg	I _{F(AV)}		10	•
	total device		Rated V _R , T _C = 145 °C	20	
Non-repetitive peak surge current per leg		I _{FSM}		100	A
Peak repetitive forward current per leg		I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 145 °C	20	
Operating junction and storage temperatures		T _J , T _{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-	
		I _F = 8 A, T _J = 125 °C	-	-	0.85	V
Forward voltage V _F	I _F = 16 A	-	-	1.15		
		I _F = 16 A, T _J = 125 °C	-	-	1.05	
		$V_{R} = V_{R}$ rated	-	-	15	
Reverse leakage current I _R	I _R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μA
Junction capacitance	CT	V _R = 200 V	-	55	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	-	35	
Boyoroo roooyony timo	+	I _F = 0.5 A, I _R = 1.0 A, I _{REC} = 0.25 A		-	-	25	
neverse recovery lime	Reverse recovery time t _{rr}	T _J = 25 °C		-	- 21 -	ns	
		T _J = 125 °C		-	35	-	
Peak recovery current I _{RRM}	T _J = 25 °C	I _F = 10 A dI _F /dt = 200 A/μs V _B = 160 V	-	1.9	-	٨	
	T _J = 125 °C		-	4.8	-	A	
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	25	-	nC
	T _J = 125 °C		-	78	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}		-	-	2.5		
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-		
Waight			-	2.0	-	g	
Weight			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)	
Marking davias		Case style D ² PAK		MURB2020CT			
Marking device		Case style TO-262		MURB2020CT-1			

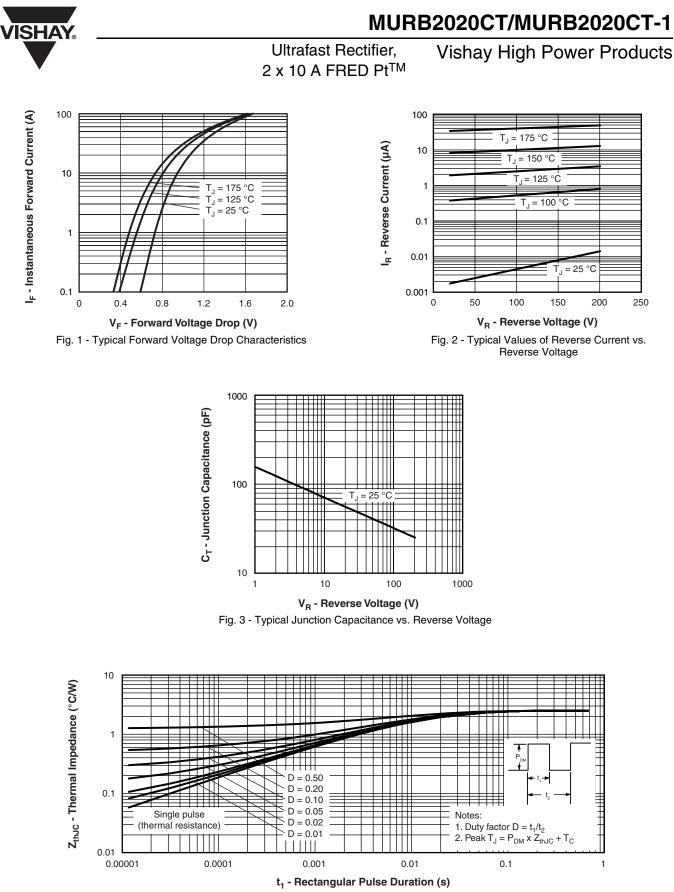
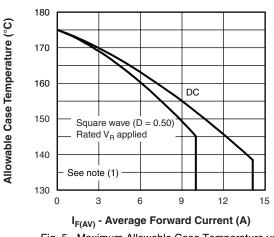
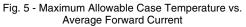


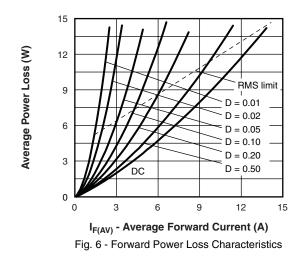
Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

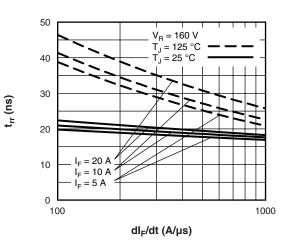
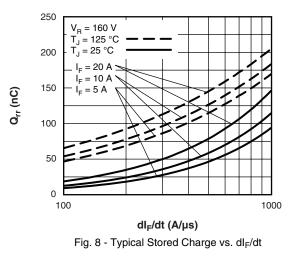


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt





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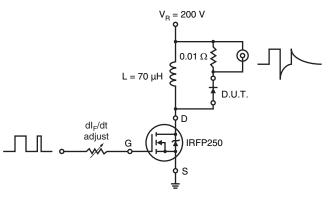


Fig. 9 - Reverse Recovery Parameter Test Circuit

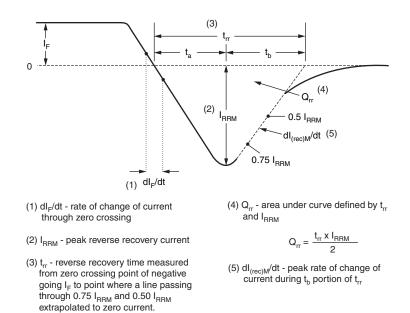


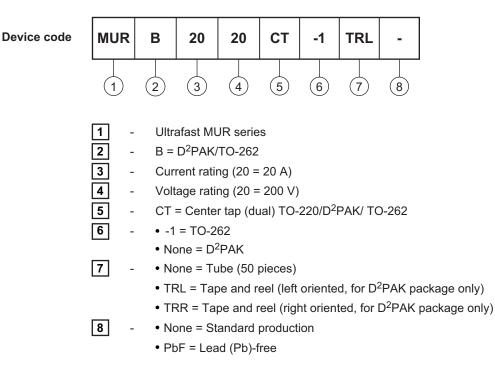
Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95014					
Part marking information	http://www.vishay.com/doc?95008				
Packaging information	http://www.vishay.com/doc?95032				



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