Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RQK0303MGDQA

Silicon N Channel MOS FET Power Switching

REJ03G1276-0400 Rev.4.00 Jun 15, 2006

Features

- Low on-resistance $R_{DS(on)} = 42 \ m\Omega \ typ \ (V_{GS} = 10 \ V, \ I_D = 1.8 \ A)$
- Low drive current
- High speed switching
- 4.5 V gate drive

Outline

Note:

RENESAS Package code: PLSP0003ZB-A (Package name: MPAK)

3
0
1. Source
2. Gate
3. Drain

Absolute Maximum Ratings

Marking is "MG".

 $(Ta = 25^{\circ}C)$

			(/
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	3.7	Α
Drain peak current	I _{D(Pulse)} Note1	5	Α
Body - drain diode reverse drain current	I _{DR}	3.7	А
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board (FR-4: $40 \times 40 \times 1$ mm)

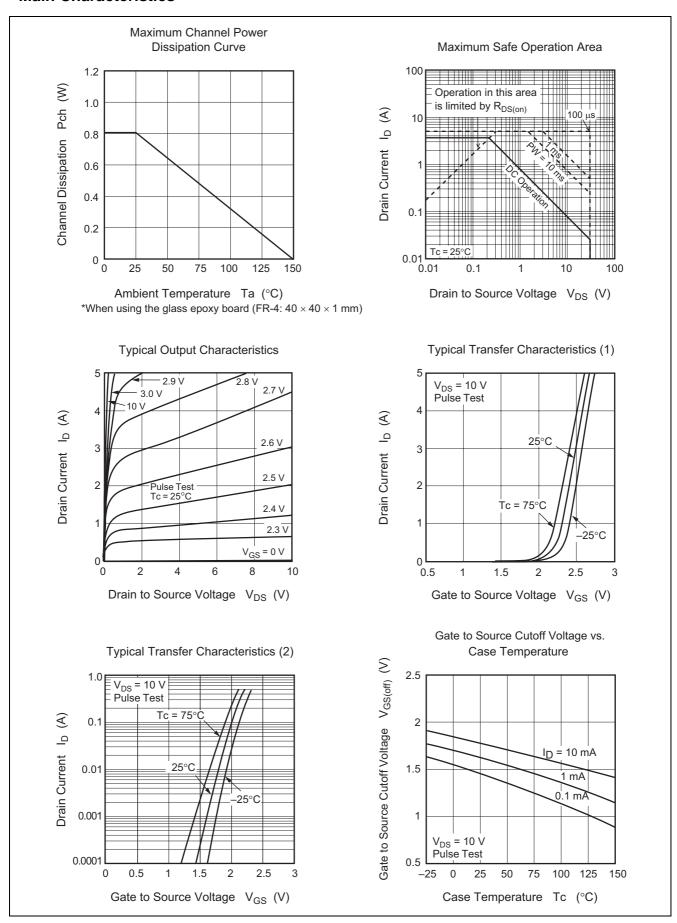
Electrical Characteristics

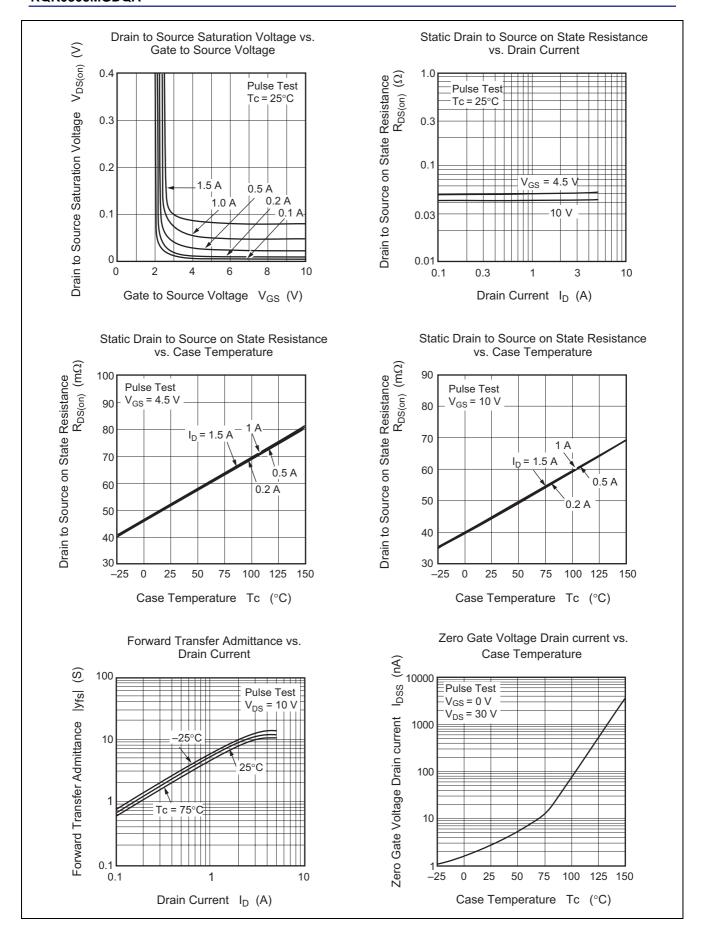
 $(Ta = 25^{\circ}C)$

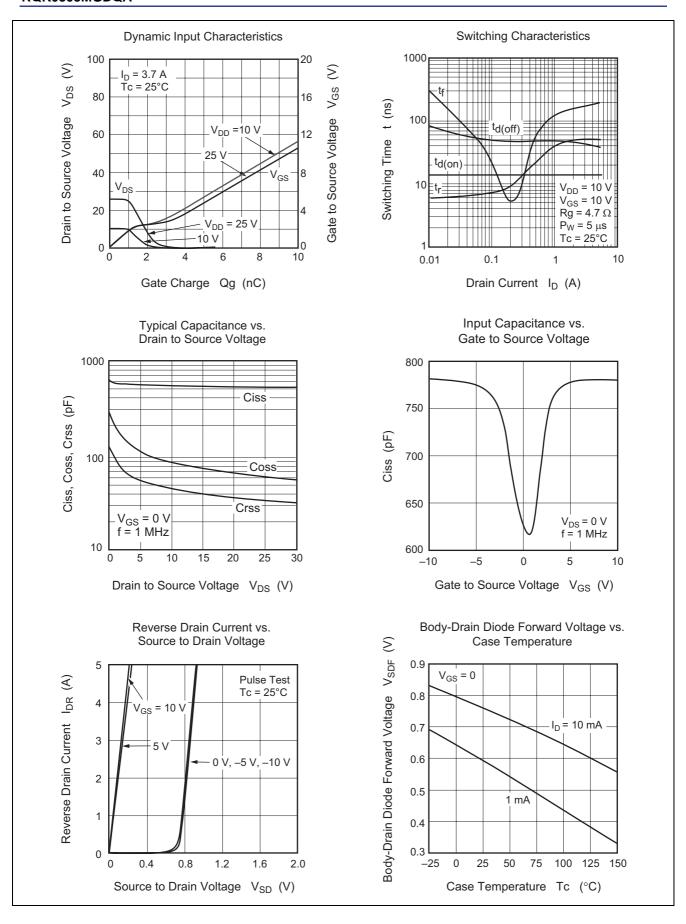
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \mu\text{A}, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Drain to source leak current	I _{DSS}		_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Drain to source on state resistance	R _{DS(on)}	_	42	53	mΩ	$I_D = 1.8 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note3}}$
	R _{DS(on)}	_	50	70	mΩ	$I_D = 1.8 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	3.9	6.5	_	S	$I_D = 1.8 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	550	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	87	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	42	_	pF	
Turn - on delay time	t _{d(on)}	_	13	_	ns	$I_D = 1 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time	t _r	_	39	_	ns	$R_L = 10 \Omega$, $Rg = 4.7 \Omega$
Turn - off delay time	t _{d(off)}	_	46	_	ns	
Fall time	t _f	_	114	_	ns	
Total gate charge	Qg	_	8.9	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 10 \text{ V},$
Gate to source charge	Qgs	_	1.0	_	nC	I _D = 3.7A
Gate to drain charge	Qgd	_	1.3	_	nC	
Body - drain diode forward voltage	V_{DF}	_	0.8	_	V	$I_F = 1.5 \text{ A}, V_{GS} = 0^{\text{Note3}}$

Notes: 3. Pulse test

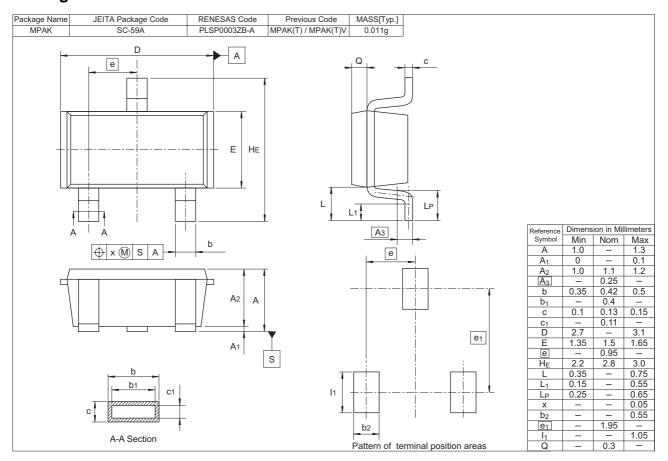
Main Characteristics







Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
RQK0303MGDQATL-E	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

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Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

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Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

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Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510