

# 2-Input AND Gate with Open Drain Output

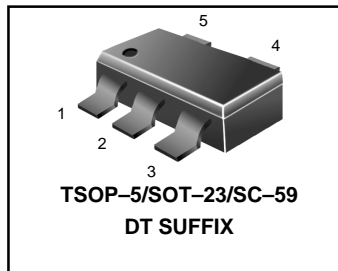
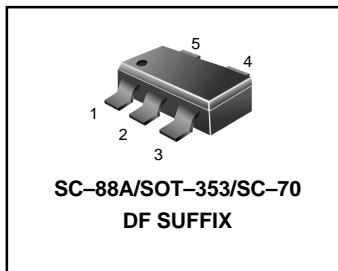
**L74VHC1G09**

The L74VHC1G09 is an advanced high speed CMOS 2-input AND gate with open drain output fabricated with silicon gate CMOS technology. It achieves high speed operation similar to equivalent Bipolar Schottky TTL while maintaining CMOS low power dissipation.

The internal circuit is composed of three stages, including an open drain output which provides the capability to set output switching level. This allows the LMC74VHC1G09 to be used to interface 5 V circuits to circuits of any voltage between  $V_{CC}$  and 7 V using an external resistor and power supply.

The L74VHC1G09 input structure provides protection when voltages up to 7 V are applied, regardless of the supply voltage.

- High Speed:  $t_{PD} = 4.3$  ns (Typ) at  $V_{CC} = 5$  V
- Low Internal Power Dissipation:  $I_{CC} = 2$  mA (Max) at  $T_A = 25^\circ\text{C}$
- Power Down Protection Provided on Inputs
- Pin and Function Compatible with Other Standard Logic Families
- Chip Complexity: FETs = 62; Equivalent Gates = 16



### MARKING DIAGRAMS

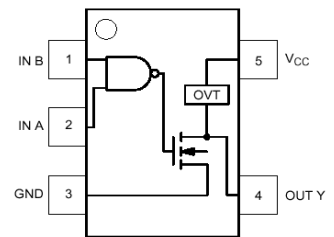
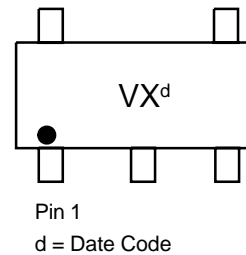
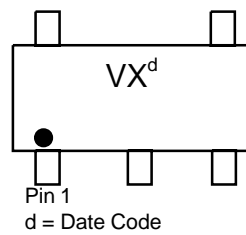


Figure 1. Pinout (Top View)

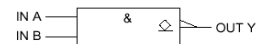


Figure 2. Logic Symbol

| PIN ASSIGNMENT |          |
|----------------|----------|
| 1              | IN B     |
| 2              | IN A     |
| 3              | GND      |
| 4              | OUT Y    |
| 5              | $V_{CC}$ |

| FUNCTION TABLE |   |        |
|----------------|---|--------|
| Inputs         |   | Output |
| A              | B | Y      |
| L              | L | L      |
| L              | H | L      |
| H              | L | L      |
| H              | H | Z      |

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

**L74VHC1G09**

**MAXIMUM RATINGS**

| Symbol                | Parameter                                  | Value  | Unit                  |
|-----------------------|--|--|-----------------------|
| V <sub>CC</sub>       | DC Supply Voltage                          | - 0.5 to + 7.0   | V                     |
| V <sub>IN</sub>       | DC Input Voltage                           | - 0.5 to +7.0  | V                     |
| V <sub>OUT</sub>      | DC Output Voltage                          | - 0.5 to +7.0  | V                     |
| I <sub>IK</sub>       | Input Diode Current                        | -20  | mA                    |
| I <sub>OK</sub>       | Output Diode Current                       | V <sub>OUT</sub> < GND; V <sub>OUT</sub> > V <sub>CC</sub>                           | +20 mA                |
| I <sub>OUT</sub>      | DC Output Current, per Pin                 | + 25   | mA                    |
| I <sub>CC</sub>       | DC Supply Current, V <sub>CC</sub> and GND | +50  | mA                    |
| P <sub>D</sub>        | Power dissipation in still air             | SC-88A, TSOP-5   | 200 mW                |
| θ <sub>JA</sub>       | Thermal resistance                         | SC-88A, TSOP-5   | 333 °C/W              |
| T <sub>L</sub>        | Lead Temperature, 1 mm from Case for 10 s  | 260  | °C                    |
| T <sub>J</sub>        | Junction Temperature Under Bias            | + 150  | °C                    |
| T <sub>stg</sub>      | Storage temperature                        | -65 to +150  | °C                    |
| V <sub>ESD</sub>      | ESD Withstand Voltage                      | Human Body Model (Note 2)<br>Machine Model (Note 3)<br>Charged Device Model (Note 4) | >2000<br>> 200<br>N/A |
| I <sub>LATCH-UP</sub> | Latch-Up Performance                       | Above V <sub>CC</sub> and Below GND at 125°C (Note 5)                                | ± 500 mA              |

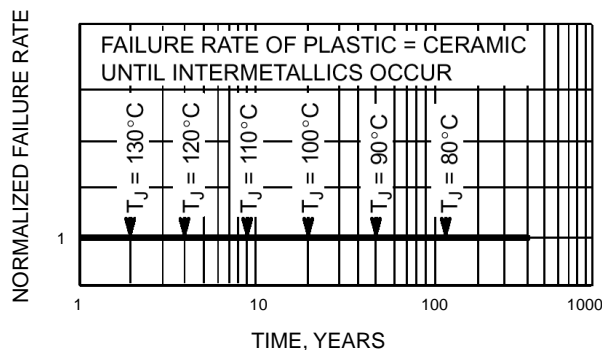
1. Maximum Ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute-maximum-rated conditions is not implied. Functional operation should be restricted to the Recommended Operating Conditions.
2. Tested to EIA/JESD22-A114-A
3. Tested to EIA/JESD22-A115-A
4. Tested to JESD22-C101-A
5. Tested to EIA/JESD78

**RECOMMENDED OPERATING CONDITIONS**

| Symbol                          | Parameter                   | Min  | Max      | Unit |
|---------------------------------|-----------------------------|--|----------|------|
| V <sub>CC</sub>                 | DC Supply Voltage           | 2.0  | 5.5      | V    |
| V <sub>IN</sub>                 | DC Input Voltage            | 0.0  | 5.5      | V    |
| V <sub>OUT</sub>                | DC Output Voltage           | 0.0  | 7.0      | V    |
| T <sub>A</sub>                  | Operating Temperature Range | - 55   | + 125    | °C   |
| t <sub>r</sub> , t <sub>f</sub> | Input Rise and Fall Time    | V <sub>CC</sub> = 3.3 ± 0.3 V<br>V <sub>CC</sub> = 5.0 ± 0.5 V | 0<br>100 | ns/V |
|                                 |                             |  | 20       |      |

**DEVICE JUNCTION TEMPERATURE VERSUS TIME TO 0.1% BOND FAILURES**

| Junction Temperature °C | Time, Hours | Time, Years |
|-------------------------|-------------|-------------|
| 80                      | 1,032,200   | 117.8       |
| 90                      | 419,300     | 47.9        |
| 100                     | 178,700     | 20.4        |
| 110                     | 79,600      | 9.4         |
| 120                     | 37,000      | 4.2         |
| 130                     | 17,800      | 2.0         |
| 140                     | 8,900       | 1.0         |



**Figure 3. Failure Rate vs. Time Junction Temperature**

**L74VHC1G09**
**DC ELECTRICAL CHARACTERISTICS**

| Symbol           | Parameter   | Test Conditions  | V <sub>CC</sub><br>(V) | T <sub>A</sub> = 25°C |     |      | T <sub>A</sub> ≤ 85°C |      | -55°C ≤ T <sub>A</sub> ≤ 125°C |      | Unit |
|------------------|---|--|------------------------|-----------------------|-----|------|-----------------------|------|--------------------------------|------|------|
|                  |   |  |                        | Min                   | Typ | Max  | Min                   | Max  | Min                            | Max  |      |
| V <sub>IH</sub>  | Minimum High-Level Input Voltage  |  | 2.0                    | 1.5                   |     |      | 1.5                   |      | 1.5                            |      | V    |
|                  |   |  | 3.0                    | 2.1                   |     | 2.1  |                       | 2.1  |                                |      |      |
|                  |   |  | 4.5                    | 3.15                  |     | 3.15 |                       | 3.15 |                                |      |      |
|                  |   |  | 5.5                    | 3.85                  |     | 3.85 |                       | 3.85 |                                |      |      |
| V <sub>IL</sub>  | Maximum Low-Level Input Voltage   |  | 2.0                    |                       |     | 0.5  |                       | 0.5  |                                | 0.5  | V    |
|                  |   |  | 3.0                    |                       |     | 0.9  |                       | 0.9  |                                | 0.9  |      |
|                  |   |  | 4.5                    |                       |     | 1.35 |                       | 1.35 |                                | 1.35 |      |
|                  |   |  | 5.5                    |                       |     | 1.65 |                       | 1.65 |                                | 1.65 |      |
| V <sub>OH</sub>  | Minimum High-Level Output Voltage<br>V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub> | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OH</sub> = -50 μA | 2.0                    | 1.9                   | 2.0 |      | 1.9                   |      | 1.9                            |      | V    |
|                  |   |  | 3.0                    | 2.9                   | 3.0 |      | 2.9                   |      | 2.9                            |      |      |
|                  |   | 4.5  | 4.4                    | 4.0                   |     | 4.4  |                       | 4.4  |                                | 4.4  |      |
|                  |   |  |                        |                       |     |      |                       |      |                                |      |      |
|                  |   | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OH</sub> = -4 mA  | 3.0                    | 2.58                  |     |      | 2.48                  |      | 2.34                           |      |      |
|                  |   | I <sub>OH</sub> = -8 mA  | 4.5                    | 3.94                  |     |      | 3.80                  |      | 3.66                           |      |      |
| V <sub>OL</sub>  | Maximum Low-Level Output Voltage<br>V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>  | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OL</sub> = 50 μA  | 2.0                    |                       | 0.0 | 0.1  |                       | 0.1  |                                | 0.1  | V    |
|                  |   |  | 3.0                    |                       | 0.0 | 0.1  |                       | 0.1  |                                | 0.1  |      |
|                  |   | 4.5  |                        | 0.0                   | 0.1 |      | 0.1                   |      | 0.1                            |      |      |
|                  |   |  |                        |                       |     |      |                       |      |                                |      |      |
|                  |   | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>I <sub>OL</sub> = 4 mA   | 3.0                    |                       |     | 0.36 |                       | 0.44 |                                | 0.52 |      |
|                  |   | I <sub>OL</sub> = 8 mA   | 4.5                    |                       |     | 0.36 |                       | 0.44 |                                | 0.52 |      |
| I <sub>IN</sub>  | Maximum Input Leakage Current   | V <sub>IN</sub> = 5.5 V or GND   | 0 to 5.5               |                       |     | ±0.1 |                       | ±1.0 |                                | ±1.0 | μA   |
| I <sub>CC</sub>  | Maximum Quiescent Supply Current  | V <sub>IN</sub> = V <sub>CC</sub> or GND   | 5.5                    |                       |     | 2.0  |                       | 20   |                                | 40   | μA   |
| I <sub>OPD</sub> | Maximum Off-state Leakage Current   | V <sub>OUT</sub> = 5.5 V   | 0                      |                       |     | 0.25 |                       | 2.5  |                                | 5.0  | μA   |

**AC ELECTRICAL CHARACTERISTICS** C<sub>load</sub> = 50 pF, Input t<sub>r</sub> = t<sub>f</sub> = 3.0 ns

| Symbol           | Parameter                                     | Test Conditions  | T <sub>A</sub> = 25°C |     |      | T <sub>A</sub> ≤ 85°C |      | -55°C to 125°C |      | Unit |
|------------------|---|--|-----------------------|-----|------|-----------------------|------|----------------|------|------|
|                  |   |  | Min                   | Typ | Max  | Min                   | Max  | Min            | Max  |      |
| t <sub>PZL</sub> | Maximum Output Enable Time, Input A or B to Y | V <sub>CC</sub> = 3.3 ± 0.3 V C <sub>L</sub> = 15 pF<br>R <sub>L</sub> = R <sub>I</sub> = 500 Ω C <sub>L</sub> = 50 pF |                       | 6.2 | 8.8  |                       | 10.5 |                | 12.5 | ns   |
|                  |   |  |                       | 8.7 | 12.3 |                       | 14.0 |                | 16.5 |      |
| t <sub>PLZ</sub> | Maximum Output Disable Time                   | V <sub>CC</sub> = 5.0 ± 0.5 V C <sub>L</sub> = 15 pF<br>R <sub>L</sub> = R <sub>I</sub> = 500 Ω C <sub>L</sub> = 50 pF |                       | 4.3 | 5.9  |                       | 7.0  |                | 9.0  | ns   |
|                  |   |  |                       | 5.8 | 7.9  |                       | 9.0  |                | 11.0 |      |
| t <sub>PLZ</sub> | Maximum Output Disable Time                   | V <sub>CC</sub> = 3.3 ± 0.3 V C <sub>L</sub> = 50 pF<br>R <sub>L</sub> = R <sub>I</sub> = 500 Ω                        |                       | 8.7 | 12.3 |                       | 14.0 |                | 16.5 | ns   |
|                  |   |  |                       | 5.8 | 7.9  |                       | 9.0  |                | 11.0 |      |
| C <sub>IN</sub>  | Maximum Input Capacitance                     |  |                       | 6.0 | 10   |                       | 10   |                | 10   | pF   |
|                  |   |  |                       |     |      |                       |      |                |      |      |

|                 |  |  | Typical @ 25°C, V <sub>CC</sub> = 5.0 V |  |  |  |    |
|-----------------|--|--|---|--|--|--|----|
| C <sub>PD</sub> | Power Dissipation Capacitance (Note 6) |  | 18                                      |  |  |  | pF |

6. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC(OPR)</sub> = C<sub>PD</sub> × V<sub>CC</sub> × f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no-load dynamic power consumption; P<sub>D</sub> = C<sub>PD</sub> × V<sub>CC</sub><sup>2</sup> × f<sub>in</sub> + I<sub>CC</sub> × V<sub>CC</sub>.

### L74VHC1G09

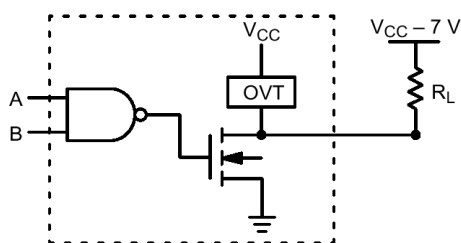


Figure 4. Output Voltage Mismatch Application

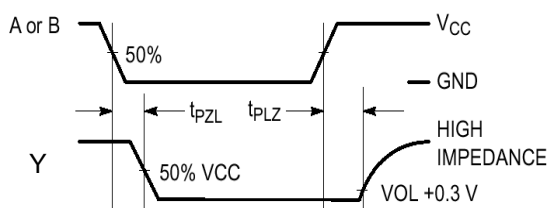
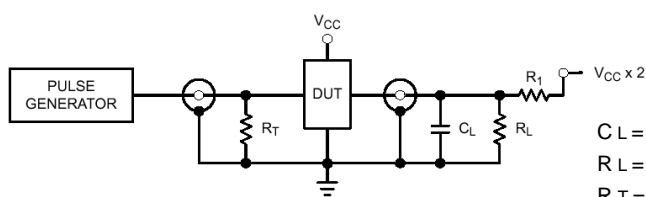


Figure 5. Switching Waveforms



$C_L = 50 \text{ pF}$  equivalent (Includes jig and probe capacitance)  
 $R_L = R_1 = 500 \Omega$  or equivalent  
 $R_T = Z_{OUT}$  of pulse generator (typically  $50 \Omega$ )

Figure 6. Test Circuit

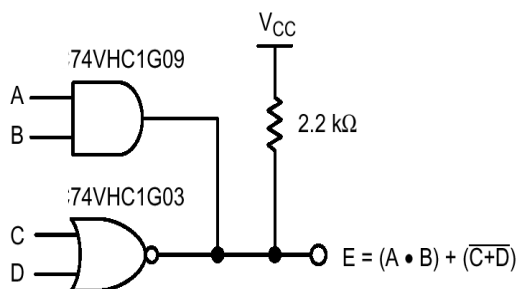


Figure 7. Complex Boolean Functions

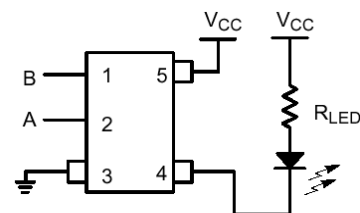


Figure 8. LED Driver

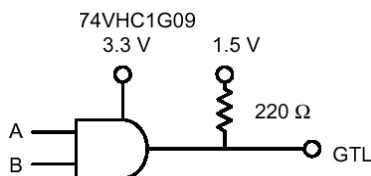


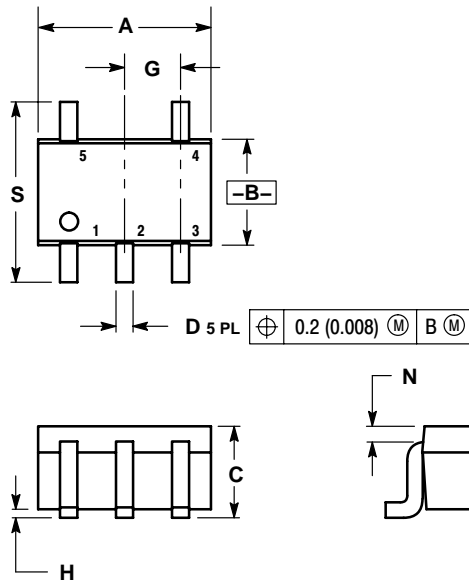
Figure 9. GTL Driver

#### DEVICE ORDERING INFORMATION

| Device Nomenclature |                         |                       |            |                 |                |                      |                                      |                               |
|---------------------|-------------------------|-----------------------|------------|-----------------|----------------|----------------------|--------------------------------------|-------------------------------|
| Device Order Number | Logic Circuit Indicator | Temp Range Identifier | Technology | Device Function | Package Suffix | Tape and Reel Suffix | Package Type (Name/SOT#/Common Name) | Tape and Reel Size            |
| L74VHC1G09DFT1      | L                       | 74                    | VHC1G      | 09              | DF             | T1                   | SC-70/SC-88A/<br>SOT-353             | 178 mm (7 in)<br>3000 Unit    |
| L74VHC1G09DFT2      | L                       | 74                    | VHC1G      | 09              | DF             | T2                   | SC-70/SC-88A/<br>SOT-353             | 178 mm (7 in)<br>3000 Unit    |
| L74VHC1G09DFT4      | L                       | 74                    | VHC1G      | 09              | DF             | T4                   | SC-70/SC-88A/<br>SOT-353             | 330 mm (13 in)<br>10,000 Unit |
| L74VHC1G09DTT1      | L                       | 74                    | VHC1G      | 09              | DT             | T1                   | SOT-23/TSOPS/<br>SC-59               | 178 mm (7 in)<br>3000 Unit    |
| L74VHC1G09DTT3      | L                       | 74                    | VHC1G      | 09              | DT             | T3                   | SOT-23/TSOPS/<br>SC-59               | 330 mm (13 in)<br>10,000 Unit |

### L74VHC1G09

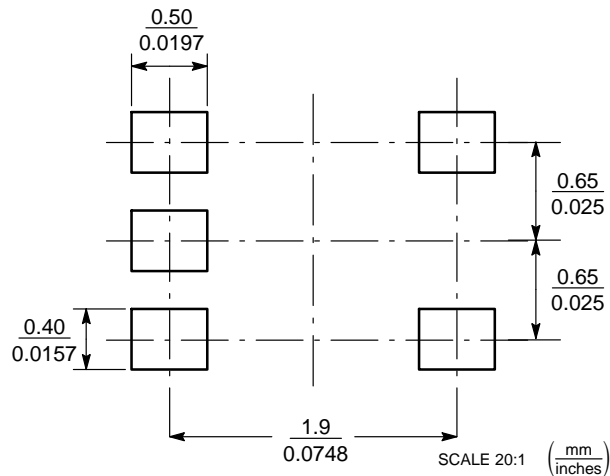
#### PACKAGE DIMENSIONS SC70-5/SC-88A/SOT-353 DF SUFFIX



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. 419A-01 OBSOLETE. NEW STANDARD 419A-02.
  4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

| DIM | INCHES    |       | MILLIMETERS |      |
|-----|-----------|-------|-------------|------|
|     | MIN       | MAX   | MIN         | MAX  |
| A   | 0.071     | 0.087 | 1.80        | 2.20 |
| B   | 0.045     | 0.053 | 1.15        | 1.35 |
| C   | 0.031     | 0.043 | 0.80        | 1.10 |
| D   | 0.004     | 0.012 | 0.10        | 0.30 |
| G   | 0.026 BSC |       | 0.65 BSC    |      |
| H   | ---       | 0.004 | ---         | 0.10 |
| J   | 0.004     | 0.010 | 0.10        | 0.25 |
| K   | 0.004     | 0.012 | 0.10        | 0.30 |
| N   | 0.008 REF |       | 0.20 REF    |      |
| S   | 0.079     | 0.087 | 2.00        | 2.20 |

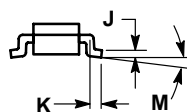
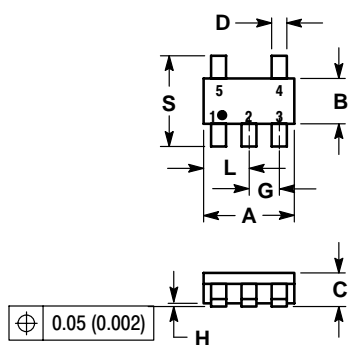
#### SOLDERING FOOTPRINT\*



## L74VHC1G09

### PACKAGE DIMENSIONS

SOT23-5/TSOP-5/SC59-5  
DT SUFFIX



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. A AND B DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

| DIM | MILLIMETERS |       | INCHES |        |
|-----|-------------|-------|--------|--------|
|     | MIN         | MAX   | MIN    | MAX    |
| A   | 2.90        | 3.10  | 0.1142 | 0.1220 |
| B   | 1.30        | 1.70  | 0.0512 | 0.0669 |
| C   | 0.90        | 1.10  | 0.0354 | 0.0433 |
| D   | 0.25        | 0.50  | 0.0098 | 0.0197 |
| G   | 0.85        | 1.05  | 0.0335 | 0.0413 |
| H   | 0.013       | 0.100 | 0.0005 | 0.0040 |
| J   | 0.10        | 0.26  | 0.0040 | 0.0102 |
| K   | 0.20        | 0.60  | 0.0079 | 0.0236 |
| L   | 1.25        | 1.55  | 0.0493 | 0.0610 |
| M   | 0           | 10    | 0      | 10     |
| S   | 2.50        | 3.00  | 0.0985 | 0.1181 |

### SOLDERING FOOTPRINT\*

