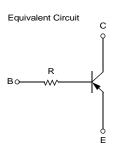


KSR2010

Switching Application (Bias Resistor Built In)

- Switching circuit, Inverter, Interface circuit, Driver Circuit
- Built in bias Resistor (R=10KΩ)
- Complement to KSR1010





PNP Epitaxial Silicon Transistor

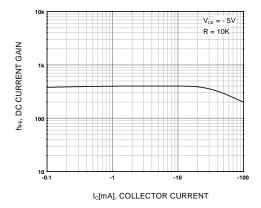
Absolute Maximum Ratings Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	-40	V
V_{CEO}	Collector-Emitter Voltage	-40	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current	-100	mA
P _C	Collector Power Dissipation	300	mW
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

Electrical Characteristics T_a =25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C = -100μA, I _E =0	-40			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I_E = -1mA, I_B =0	-40			V
I _{CBO}	Collector Cut-off Current	V _{CB} = -30V, I _E =0			-0.1	μΑ
h _{FE}	DC Current Gain	V_{CE} = -5V, I_{C} = -1mA	100		600	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -10mA, I _B = -1mA			-0.3	V
C _{ob}	Output Capacitance	V_{CB} = -10V, I_{E} =0 f=1MHz		5.5		pF
f _T	Current Gain Bandwidth Product	$V_{CE} = -10V, I_{C} = -5mA$		200		MHz
R	Input Resistor		7	10	13	ΚΩ

Typical Characteristics



-1000 (cef (sas) (mV); SATURATION VOLTAGE
R = 10k

Figure 1. DC current Gain

Figure 2. Collector-Emitter Saturation Voltage

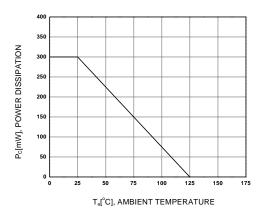
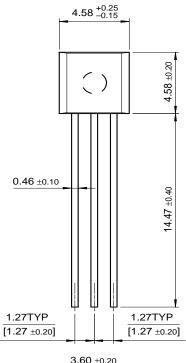
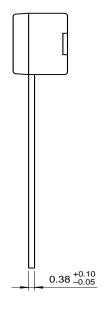


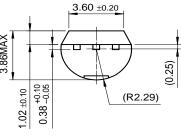
Figure 3. Power Derating

Package Dimensions

TO-92







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