

# **SRA2202UF**

**PNP Silicon Transistor** 

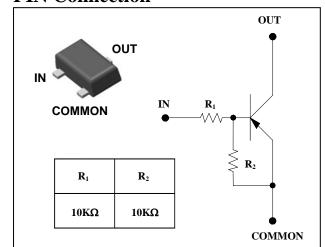
#### **Descriptions**

- Switching application
- Interface circuit and driver circuit application

#### **Features**

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

#### **PIN Connection**



#### **Ordering Information**

Type NO.	Marking	Package Code	
SRA2202UF	<u>2R</u> □ ① ②	SOT-323F	

①Device Code ② Year&Week Code

#### **Absolute Maximum Ratings**

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V <sub>I</sub>	-30, 10	V
Output current	I <sub>O</sub>	-100	mA
Power dissipation	$P_{D}$	200	mW
Junction temperature	TJ	150	°C
Storage temperature range	$T_{stg}$	-55 ~ 150	°C

#### **Electrical Characteristics**

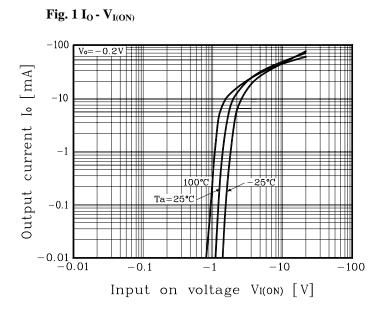
 $(Ta=25^{\circ}C)$ 

Characteristic	Symbol	<b>Test Condition</b>	Min.	Typ.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_{O} = -5V$ , $I_{O} = -10$ mA	50	80	-	-
Output voltage	$V_{O(ON)}$	$I_0 = -10 \text{mA}, I_1 = -0.5 \text{mA}$	-	-0.1	-0.3	V
Input voltage (ON)	V <sub>I(ON)</sub>	$V_0 = -0.2V$ , $I_0 = -5mA$	ı	-1.8	-2.4	V
Input voltage (OFF)	V <sub>I(OFF)</sub>	$V_{O} = -5V$ , $I_{O} = -0.1$ mA	-1.0	-1.2	-	V
Transition frequency	$f_{T}^{}^{*}}$	$V_O = -10V$ , $I_O = -5mA$ , $f = 1MHz$	-	200	-	MHz
Input current	$I_1$	$V_1 = -5V$ , $I_0 = 0$	-	-	-0.88	mA
Input resistor (Input to base)	R <sub>1</sub>	-	7	10	13	ΚΩ
Input resistor (Base to common)	R <sub>2</sub>	-	7	10	13	ΚΩ

<sup>\* :</sup> Characteristic of transistor only

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### **Electrical Characteristic Curves**



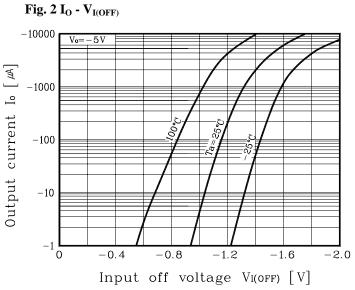
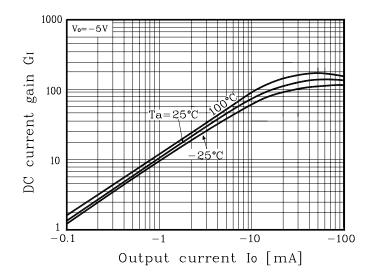
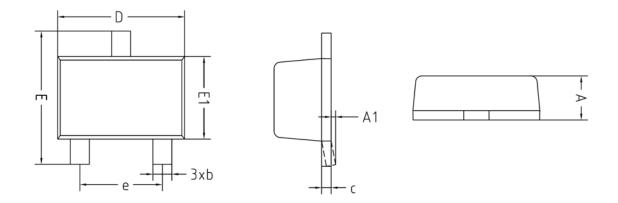


Fig. 3  $G_I$  -  $I_O$ 



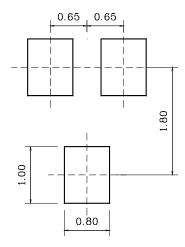
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## **Outline Dimension**



SYMBOL	MILLIMETERS			NOTE
STITIBOL	MINIMUM	NOMINAL	MAXIMUM	NOTE
Α	0.60	-	0.80	
A1	0.00	-	0.10	
Ь	0.30	-	0.40	
С	0.08	-	0.16	
D	1.90	2.00	2.10	
E	1.95	2.10	2.25	
E1	1.20	1.30	1.40	
е	1.30BSC			

#### \*Recommend PCB solder land [Unit: mm]



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