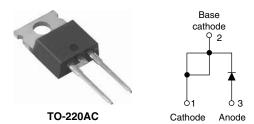


Vishay High Power Products

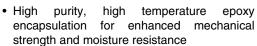
Schottky Rectifier, 6 A



PRODUCT SUMMARY				
I _{F(AV)}	6 A			
V_{R}	35 to 45 V			

FEATURES

- 175 °C T_J operation
- High frequency operation
- · Low forward voltage drop





RoHS'

- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial level

DESCRIPTION

The 6TQ...PbF Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS						
I _{F(AV)}	Rectangular waveform	6	Α					
V _{RRM}	Range	35 to 45	V					
I _{FSM}	$t_p = 5 \mu s sine$	690	Α					
V _F	6 Apk, T _J = 125 °C	0.53	V					
T _J	Range	- 55 to 175	°C					

VOLTAGE RATINGS						
PARAMETER SYMBOL 6TQ035PbF 6TQ040PbF 6TQ045PbF UNITS						
Maximum DC reverse voltage	V_R	35	40	45	V	
Maximum working peak reverse voltage	V_{RWM}	35	40	45	V	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 164 °C	6	А			
Maximum peak one cycle non-repetitive surge current	l-a	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	690	A		
See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	140			
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.10 mH		8	mJ		
Repetitive avalanche current	I _{AR}	Current decaying linearly to zer Frequency limited by T_J maxim	1.20	Α			

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

6TQ...PbF Series

Vishay High Power Products Schottky Rectifier, 6 A



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS			
		6 A	T 05.00	0.60	V		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	12 A	- T _J = 25 °C	0.73			
See fig. 1	V FM (1)	6 A	T.ı = 125 °C	0.53			
		12 A	- IJ = 125 °C	0.64			
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V Dataday	0.8	A		
See fig. 2	IRM ('')	T _J = 125 °C	V_R = Rated V_R	7	mA		
Threshold voltage	V _{F(TO)}	T T maximum		0.35	V		
Forward slope resistance	r _t	ij=ijmaximum	$T_J = T_J$ maximum		mΩ		
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	400	pF			
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 8			nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/μ			V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and stor temperature range	age	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistand junction to case	ce,	R _{thJC}	DC operation See fig. 4 2.2		°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	C/VV	
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque maximum				12 (10)	(lbf \cdot in)	
Marking device				6TC	035	
			Case style TO-220AC	6TQ040		
				6TC	045	



Schottky Rectifier, 6 A

Vishay High Power Products

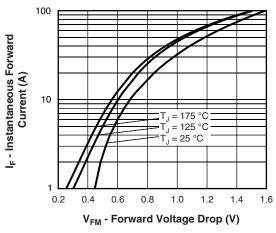


Fig. 1 - Maximum Forward Voltage Drop Characteristics

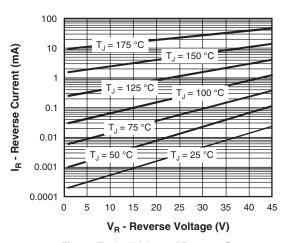


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

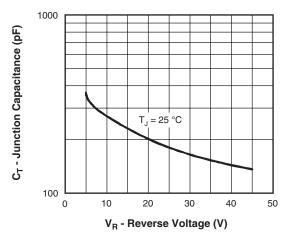


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

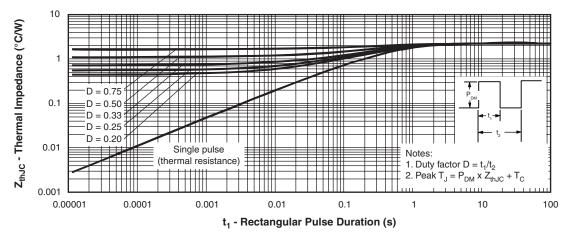


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

Vishay High Power Products Schottky Rectifier, 6 A



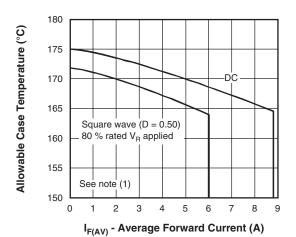


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

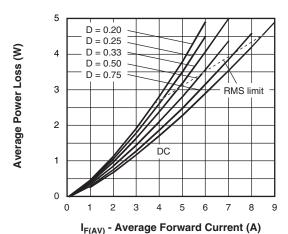


Fig. 6 - Forward Power Loss Characteristics

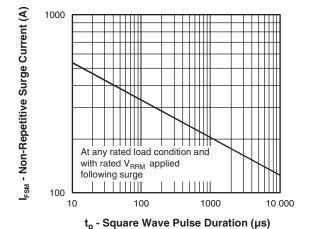


Fig. 7 - Maximum Non-Repetitive Surge Current

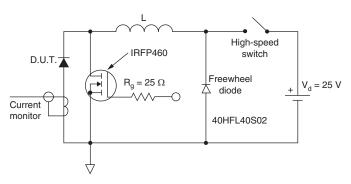


Fig. 8 - Unclamped Inductive Test Circuit

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80$ % rated V_R

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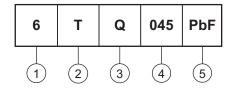


Schottky Rectifier, 6 A

Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



- 1 Current rating (6 = 6 A)
- 2 Package:

T = TO-220

3 - Schottky "Q" series

035 = 35 V

4 - Voltage ratings

040 = 40 V 045 = 45 V

- 5 • None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95221				
Part marking information	http://www.vishay.com/doc?95224			

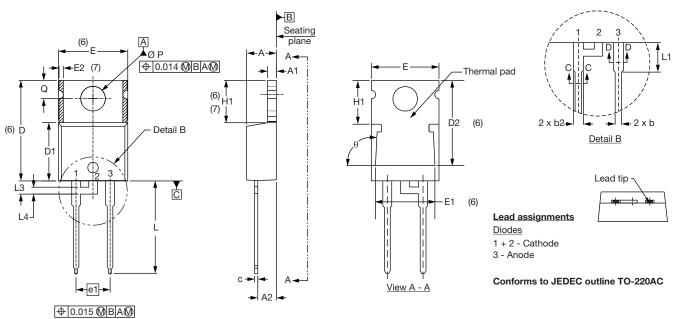
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Vishay Semiconductors

TO-220AC

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	NOTES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIM	IETERS	INCHES		NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline





Vishay

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