M3R Series 9x14 mm, 3.3 Volt, HCMOS/TTL, Clock Oscillator



00.0000

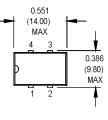
MHz

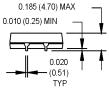
J





This product is not recommended for new designs





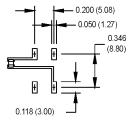


All dimensions

in inches (mm).



SUGGESTED SOLDER PAD LAYOUT



NOTE: A capacitor of value 0.01 μ F or greater between Vdd and Ground is recommended.

Pin	Connections	

PIN	FUNCTION				
1	N/C or Tristate				
2	Ground				
3	Output				
4	+Vdd				

ectrical Specifications ਨਿੰਹ ਨੂੰ ਕੀ ਕੀ ਛੇ ਸੋ ਨਿਰੀ	equency Range perating Temperature corage Temperature equency Stability ging 1st Year Thereafter (per year) put Voltage put Current	F TA TS ∆F/F	-55		80 ng information +125 ng information	°C			
ectrical Specifications	orage Temperature equency Stability ging 1st Year Thereafter (per year) put Voltage	Ts ∆F/F	-55 (S -5		+125	°C			
ectrical Specifications S T O dul dul dul	equency Stability ging 1st Year Thereafter (per year) put Voltage	∆F/F	-5	ee orderi		-			
ectrical Specifications	ging 1st Year Thereafter (per year) put Voltage		-5	ee orderi	ng information	- \			
ectrical Specifications	1st Year Thereafter (per year) put Voltage	Vdd			<u> </u>	1)			
ectrical Sol Sol	Thereafter (per year) put Voltage	Vdd				Í			
ectrical Sol	put Voltage	Vdd	-5		+5	ppm			
ectrical Sol	U U	Vdd			+5	ppm			
ectrical Sol Sol	put Current		3.0	3.3	3.6	V			
ectrical Sol Sol		ldd			15	mA	1.000 to 27.000 MHz		
ectrical Sol Sol					20	mA	27.001 to 50.000 MHz		
ectrical sol					40	mA	50.001 to 80.000 MHz		
sol Lo	utput Type						HCMOS/TTL		
Sy Sy	bad				15	pF	See Note 1		
-	ymmetry (Duty Cycle)		(See ordering information)				50% Vdd level		
ш Log	ogic "1" Level	Voh	90% Vdd			V	HCMOS Load		
Log	ogic "0" Level	Vol			10% Vdd	V	HCMOS Load		
Ou	utput Current				±4	mA			
Ris	se/Fall Time	Tr/Tf			8	ns	See Note 2		
Tris	istate Function		Input Logic "1" or floating: output active						
			Input Logic	"0": outp					
Sta	art up Time				10	ms			
Ra	andom Jitter	Rj		5	12	ps RMS	1-Sigma		
	echanical Shock	MIL-STD-202, Method 213, C (100 g's)							
Ĕ <u>Vik</u>	bration	MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)							
<u>e</u> Th	nermal Cycle	MIL-STD-883, Method 1010, B (-55°C to +125°C, 15 min dwell, 10 cycles)							
	ermeticity	MIL-STD-202, Method 112							
	olderability	Per EIAJ-STD-002							
	ax Soldering Conditions	See solder profile, Figure 1							
1. I 2.		Circuit Diag							

Ordering Information

Product Series

Stability -

Temperature Range 1: 0°C to +70°C

2: -40°C to +85°C **6:** -20°C to +70°C

3: ±100 ppm

6: ±25 ppm*

Output Type ·

J: J Lead

*0°C to 70°C only

F: Fixed

M₃R

4: ±50 ppm

Symmetry/Logic Compatibility

Package/Lead Configurations

Frequency (customer specified)

M2008Sxxx - Contact factory for datasheet.

A: 40/60 TTL/HCMOS

3 F A

1

T: Tristate

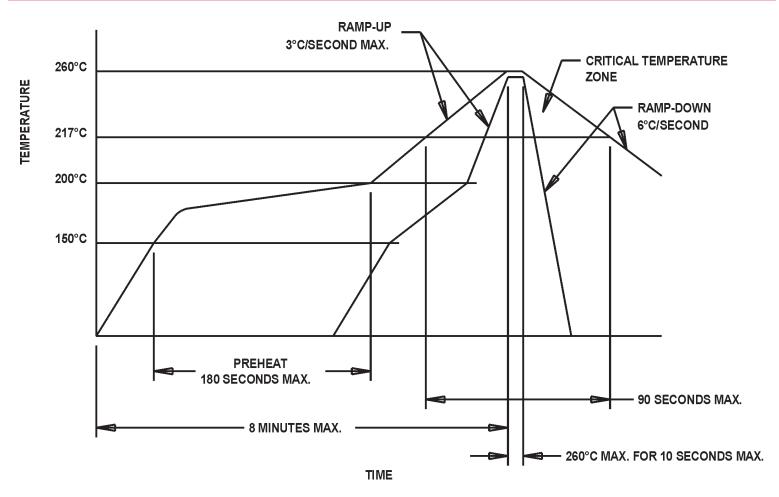
C: 45/55 HCMOS

Rise/Fall times are measured between 10% Vdd and 90% Vdd with HCN
TTL output drive capability is 2 TTL (10 LS-TTL)

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.

Please see www.mtronpti.com for our complete offering and detailed datasheets. Contact us for your application specific requirements: MtronPTI 1-800-762-8800.

MtronPTI Lead Free Solder Profile



MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.