RICOH

R5511x SERIES

LDO REGULATOR WITH RESET

NO.EA-111-140530

OUTLINE

The R5511x Series are CMOS-based voltage regulator (LDO) ICs equipped with a voltage detector (VD). LDO function of the R5511x has features of high ripple rejection, low dropout voltage, high output voltage accuracy, and low supply current. Each of these ICs consists of a voltage reference unit, an error amplifier, resistors for setting output voltage, a current limit circuit, a voltage detector, and a chip enable circuit. The output of built-in voltage detector is Nch open drain type. (With the mask option CMOS output type is also available.)

The output voltage and the detector threshold voltage are fixed in the IC. Low supply current by the merit of CMOS process and the built-in transistor with low ON-resistance make low dropout voltage. These regulators in the R5511x Series are remarkable improvement on the current regulators in terms of ripple rejection, input transient response, and load transient response. Furthermore, the R5511x series can supervise input voltage (the input voltage means the input level for V_{DD} or V_{SENSE} pin) with built-in detector. Thus, the R5511x series are suitable not only for cellular handsets but also for power supply for CD-drives, DVD-drives, and so forth.

Since the packages for these ICs are the SON-6, SOT-23-5, SOT-89-5 package, high density mounting of the ICs on boards is possible.

FEATURES

Supply Current	Typ. 50µA
Standby Current	
Ripple Rejection	
Output Current	
Output Voltage Range	
Output Voltage Accuracy	±1.5% (Vouт ≥ 2.0V)
Dropout Voltage	
Detector Threshold Range	1.2V to 5.0V
Detector threshold Accuracy	±1.5% (Vouт ≥ 2.0V)
Low Temperature-drift Coefficient of Output Voltage	Typ. ±100ppm/°C
Absolute Maximum Voltage	6.5V
Packages	SON-6, SOT-23-5, SOT-89-5
Output Delay Time (tdelay)	A: (tdelay=1ms, Hysteresis5%)
	B: (tdelay=20ms, Hysteresis5%)
	C: (tdelay=60ms, Hysteresis5%)
	D: (tdelay=240ms, no Hysteresis)
Output Delay Time (100mg / With an without hystoresis	an ha daalamatad with waaria ragwaat

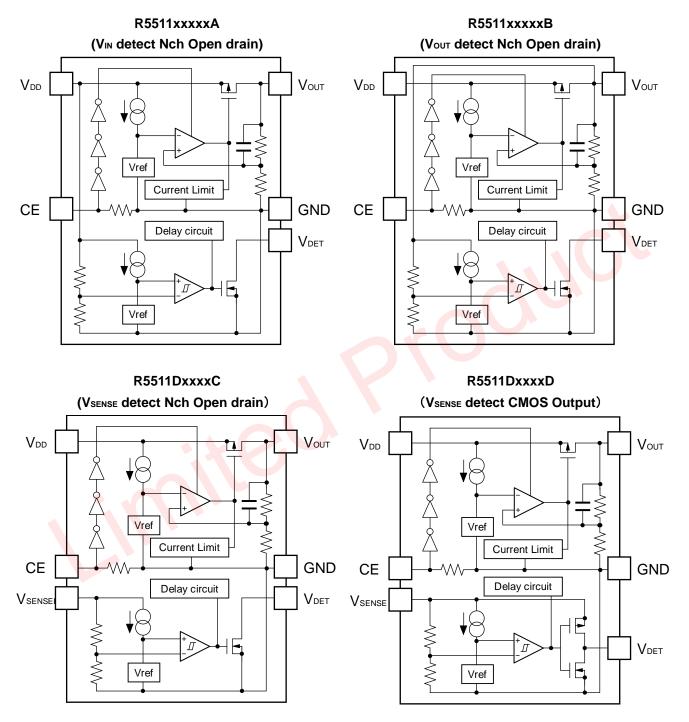
*Output Delay Time : 120ms / With or without hysteresis can be designated with user's request

- Ceramic capacitors are recommended to be used with this IC1 μF or more
- Built-in Fold-back Protection Circuit

APPLICATIONS

- CD-drives and DVD-drives
- Power source for Cellular Phone

BLOCK DIAGRAMS



R5511x

SELECTION GUIDE

The output voltage setting code number, hysteresis, output delay time, the detecting target, output type can be selected at the user's request.

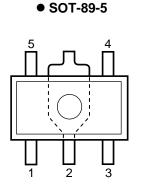
Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R5511Dxxx\$*-TR-FE	SON-6	3,000pcs	0	0
R5511Nxxx\$#-TR-FE	SOT-23-5	3,000pcs	0	0
R5511Hxxx\$#-T1-FE	SOT-89-5	1,000pcs	0	0
xxx : Serial number code of	output voltage and dete	ctor threshold setting,	with/without hyste	resis
 \$: Designation of output of (A) 1ms (B) 20ms (C) 60ms (D) 240ms * : Designation of the determination of the det		type		
(C) V _{SENSE} detect Nch (D) V _{SENSE} detect CM	•			
#: Designation of the dete (A) V _{IN} detect Nch Op (B) Vou⊤ detect Nch C	ecting target and output ben drain	type		

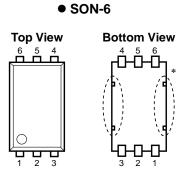
*With Hysteresis / No delay time version can be designated.

PIN CONFIGURATION

5 (mark side) 2 1 3

• SOT-23-5





PIN DESCRIPTIONS

• SON-6

Pin No	Symbol	Pin Description
1	CE	Chip Enable Pin
2	GND	Ground Pin
3	Vout	Voltage Regulator Output Pin
4	Vdd	Input and SENSE Pin of Voltage Detector
5	Vsense	SENSE Pin
6	Vdet	Voltage Detector Output Pin (Detect"L", Reset"H")
*) Tab suspensio	n leads are GND leve	I. (They are connected to the reverse side of this IC.)

*) Tab suspension leads are GND level. (They are connected to the reverse side of this IC.)

The tab suspension leads should be open and do not connect to other wires or land patterns.

• SOT-23-5

Pin No	Symbol	Pin Description		
1	CE	Chip Enable Pin		
2	GND	Ground Pin		
3	Vdet	Voltage Regulator Output Pin (Detect"L", Reset"H")		
4	Vdd	Input and SENSE Pin of Voltage Detector		
5	Vout	Voltage Detector Output Pin		

• SOT-89-5

Pin No	Symbol	Pin Description
1	Vdet	Voltage Detector Output Pin (Detect"L", Reset"H")
2	GND	Ground Pin
3	CE	Chip Enable Pin
4	Vout	Voltage Regulator Output Pin
5	Vdd	Input and SENSE Pin of Voltage Detector

ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
VIN	Input Voltage	6.5	V
Vce	Input Voltage (CE Input Pin)	6.5	V
Vsense	Input Voltage (SENSE Pin)	6.5	V
Vdet	VD Output Voltage (CMOS Output)	-0.3 to V _{IN} + 0.3	V
VDET	VD Output Voltage (Nch Open Drain)	-0.3 to 6.5	V
Vout	Output Voltage -0.3 to V _{IN} + 0.3		V
Ιουτ	Output Current	400	mA
	Power Dissipation (SON-6) *	500	
PD	Power Dissipation (SOT-23-5) *	420	mW
	Power Dissipation (SOT-89-5) *	900	
Topt	Operating Temperature -40 to +85		°C
Tstg	Storage Temperature	-55 to +125	°C

*) For Power Dissipation, please refer to PACKAGE INFORMATION.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field.

The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

ELECTRICAL CHARACTERISTICS

• R5511x

Topt=25°C

Symbol	ltem	Conditions	Min.	Тур.	Max.	Unit
Vin	Input Voltage				6.0	V
Iss1	Quiescent Current 1	VIN-VOUT=1.0V		50	80	μA
Iss2	Quiescent Current 2	VIN=-VDET-0.1V, VCE=0V		1.5	3.0	μA
lss3	Quiescent Current 3	VIN=-VDET+1.0V, VCE=0V		1.5	3.0	μA

• VR Part

Symbol	Item	Cond	ditions	Min.	Тур.	Max.	Unit
			2.0V ≤ V _{SET}	×0.985		×1.015	V
Vout	Output voltage	Vin–Vout=1.0V Iout=30mA	1.5V < VSET < 2.0V	-30	N	+30	mV
		1001 –00 111A	$1.2V \le V_{\text{SET}} \le 1.5V$	×0.980		×1.020	V
Іоит	Output Current	VIN-VOUT=1.0V		300			mA
ΔVουτ/ΔΙουτ	Load regulation	$V_{IN}-V_{OUT}=1.0V$ 1mA ≤ Iout ≤ 100mA ^{*1}			5	15	mV
Vdif	Dropout Voltage	Refer to the Electrical Characteristics by Output Voltage				V	
ΔVουτ/ΔVin	Line regulation	Iout=30mA Vout+0.5V ≤ ViN ≤ 6.0V			0.05	0.15	%/V
RR	Ripple Rejection	f=1kHz, Ripple 0.5Vp–p VIN–Vreg1=1.0V			75		dB
ΔVουτ/ΔTopt	Output Voltage Temperature Coefficient	Iou <mark>T=30mA</mark> -40°C ≤ Topt ≤ 85°C			±100		ppm ∕°C
lsc	Short Current Limit	Vout=0V			50		mA
Rpd	Pull-down resistance for CE pin			2	5	14	MΩ
Vсен	CE Input Voltage "H"			1.1		Vin	V
Vcel	CE Input Voltage "L"			0.0		0.3	V

*1) Guaranteed by Design.

R5511x

• VD Part

Topt=25°C

Symbol	Item	Conditions	Min.	Тур.	Max.	Unit
		2.0V ≤ V _{SET}	×0.985		×1.015	V
-Vdet	Detector Threshold	1.5V < VSET < 2.0V	-30		+30	mV
		1.2V ≤ V _{SET} ≤ 1.5V	×0.980		×1.020	V
V _{HYS}	Detector Threshold Hysteresis	Output Delay Time: 0ms, 20ms, 60ms	-V _{DET} ×0.03	-V _{DET} ×0.05	-V _{DET} ×0.07	V
lol	Output Current	Refer to Electrical Characteristics			mA	
Іон	Ouiput Current	by Detector Threshold				
Vddl	Minimum Operating Voltage	Ιουτ=10μΑ		0.65	0.80	V
$\Delta - V$ det/ Δ Topt	Detector Threshold Temperature Coefficient	–40°C ≤ Topt ≤ 85°C		±100		ppm ∕°C
		A Version	0.5	1.0	2.8	
tdelay	Output Dolou Time	B Version	16	20	24	
	Output Delay Time	C Version	50	60	70	ms
		D Version	200	240	280	

• Electrical Characteristics by Output Voltage

Output Voltage	Dropout Voltage Vы⊧ (V)			
Vout (V)	Condition	Тур.	Max.	
1.2V ≤ Vset < 1.5V		0.18	0.28	
1.5V ≤ Vset< 1.8V		0.16	0.22	
1.8V ≤ VSET< 2.2V	Ιουτ=100mA	0.14	0.20	
2.2V ≤ Vset< <mark>2.8</mark> V		0.12	0.17	
2.8V ≤ V _{SET} ≤ 4.0V		0.10	0.15	

• Electrical Characteristics by Detector Threshold

• Nch Open Drain Type

Detector Threshold		"L" Output Current Io∟ (mA)					
-Vdet (V)	Condit	Min.	Тур.	Max.			
1.2V ≤ Vdset < 1.6V	Vdd=1.1V		1.1	2.8	5.0		
1.6V ≤ Vdset < 3.1V	Vdd=1.5V	Vds=0.5V	3.0	6.0	10.0		
3.1V ≤ V _{DSET} ≤ 5.0V	VDD=3.0V		8.0	11.0	15.0		

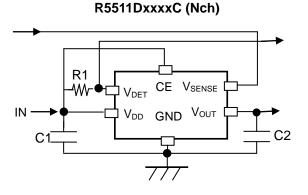
• CMOS Output Type

Detector Threshold	"H" Output Current Іон (mA)				
-Vdet (V)	Condition	Min.	Тур.	Max.	
1.2V ≤ Vdset < 1.6V, VS=1.7V	V _{DD} =VS V _{DS} =VS×0.8	0.10	0.20	0.35	
1.6V ≤ V _{DSET} < 3.1V, VS=3.3V		0.55	0.90	1.40	
3.1V ≤ V _{DSET} ≤ 5.0V, VS=5.4V		1.50	2.10	2.90	

IN

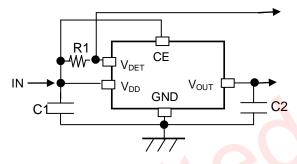
R5511x

TYPICAL APPLICATION



R1=470kΩ,C1=C2=Ceramic 1μF

R5511N/H Series



R1=470kΩ,C1=C2=Ceramic 1μF

TECHNICAL NOTES

When using these ICs, consider the following points:

Phase Compensation

In these ICs, phase compensation is made for securing stable operation even if the load current is varied. For this purpose, use a capacitor C2 with good frequency characteristics and ESR (Equivalent Series Resistance). (Note: If additional ceramic capacitors are connected with parallel to the output pin with an output capacitor for phase compensation, the operation might be unstable. Because of this, test these ICs with as same external components as ones to be used on the PCB.)

PCB Layout

Make V_{DD} and GND lines sufficient. If their impedance is high, noise pickup or unstable operation may result. Connect a capacitor C1 with a capacitance value as much as 1μ F or more between V_{DD} and GND pin, and as close as possible to the pins.

Set external components, especially the output capacitor C2, as close as possible to the ICs, and make wiring as short as possible.

VDFT



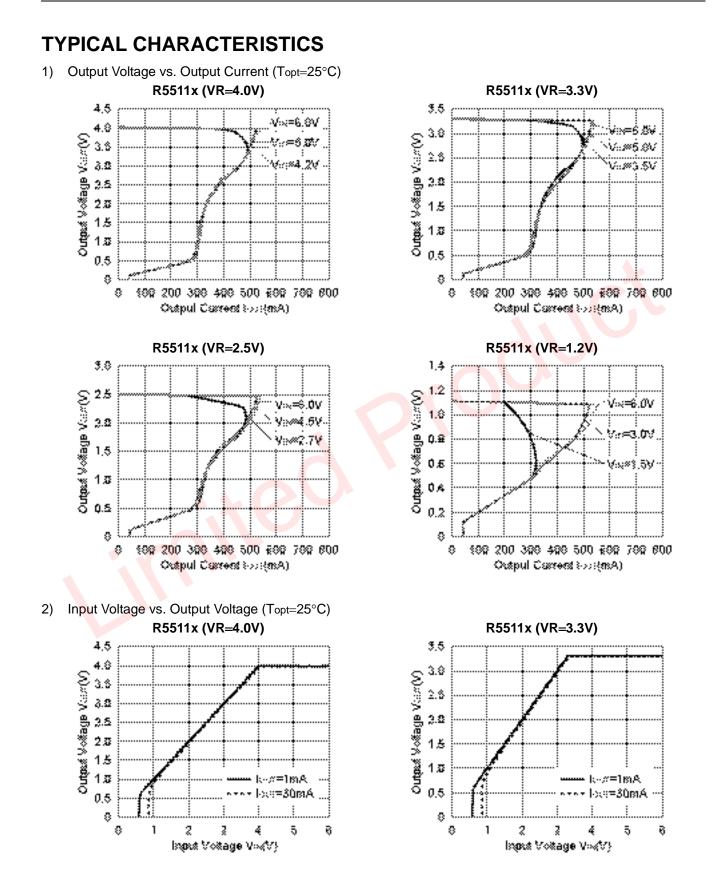
R5511DxxxxD (CMOS)

CE

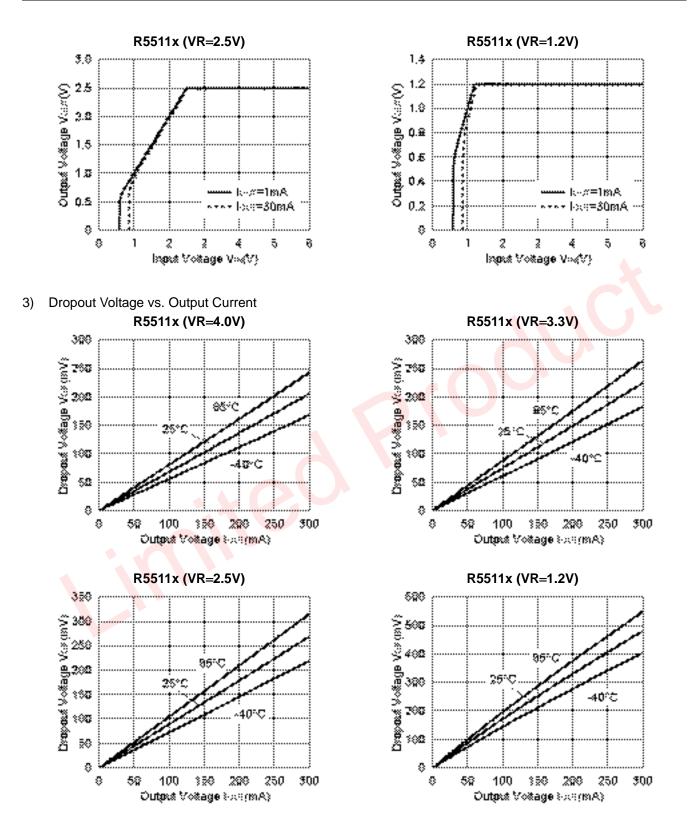
 V_{SENSE}

C2

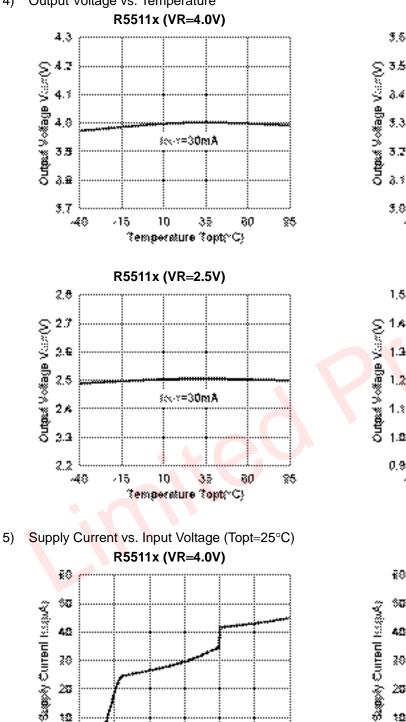
R5511x

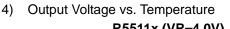






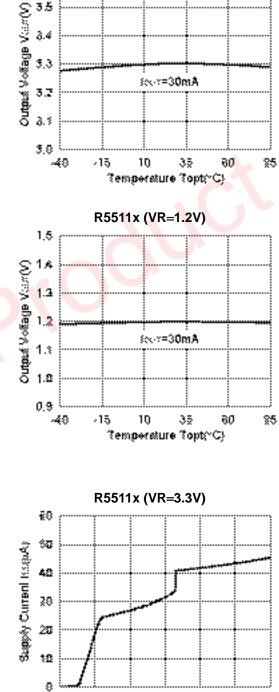
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t2

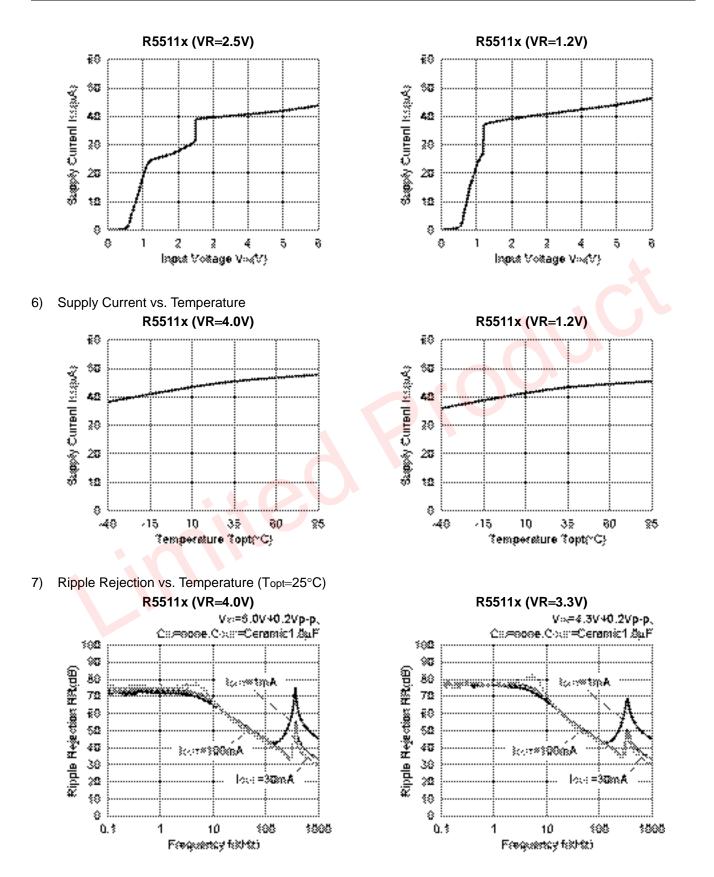
Input Votage VodV)



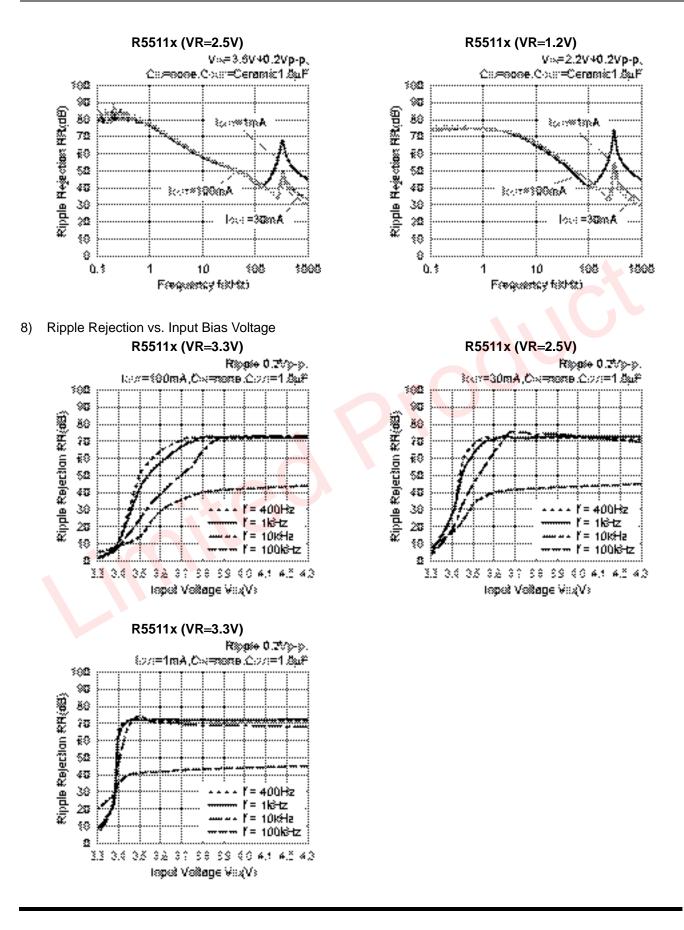
R5511x (VR=3.3V)

ingest Voltage VoldV}



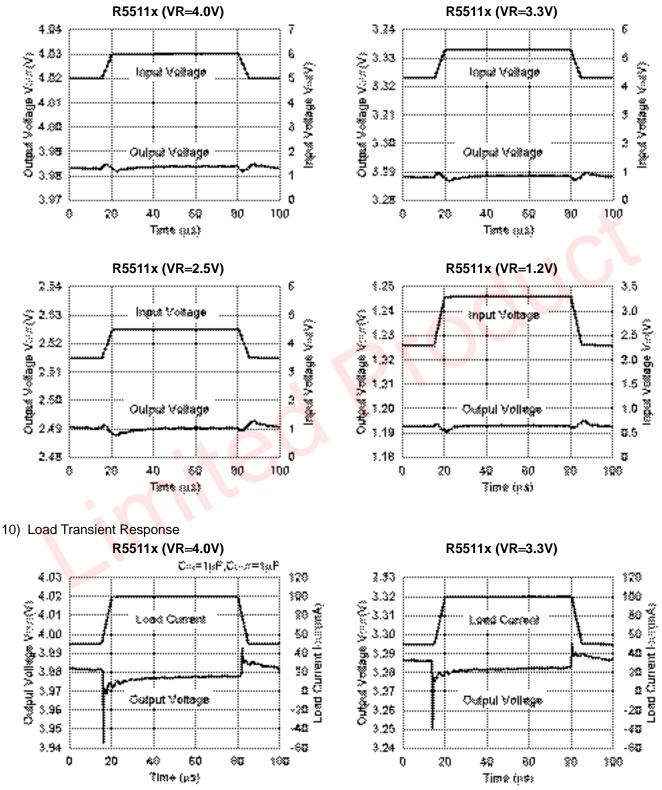


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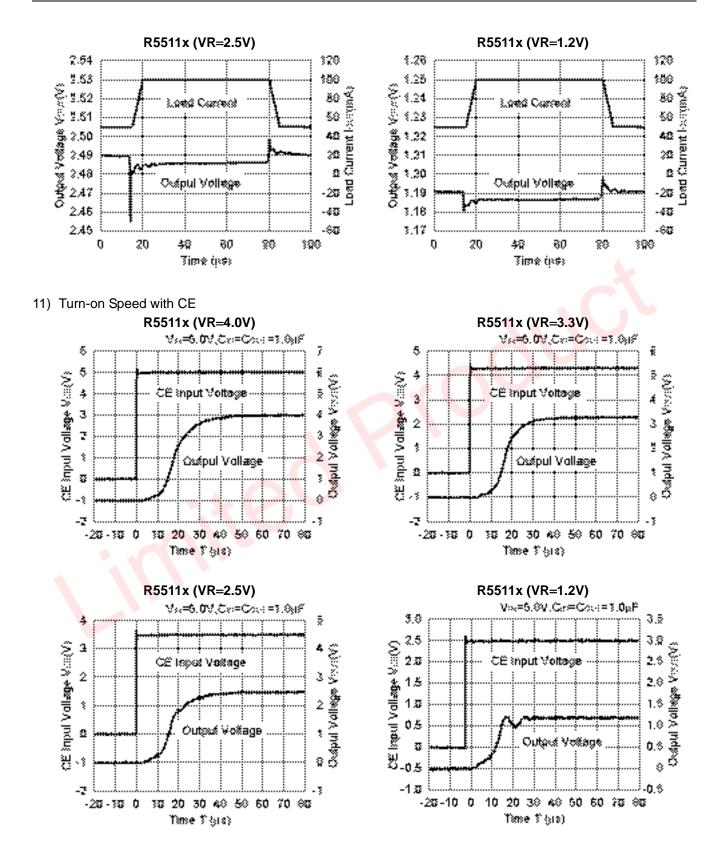


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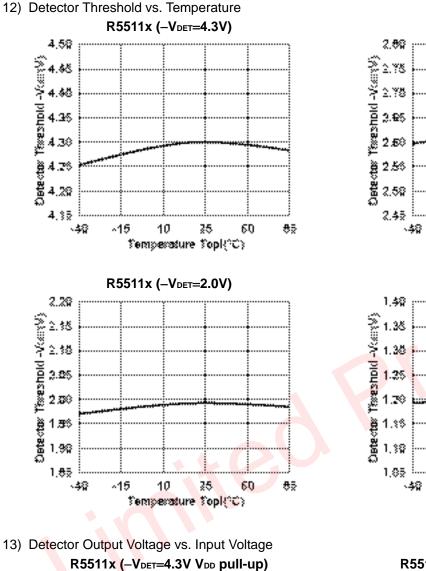
9) Input Transient Response

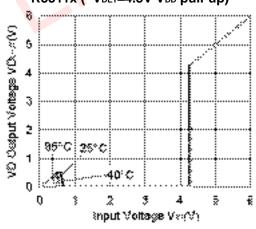


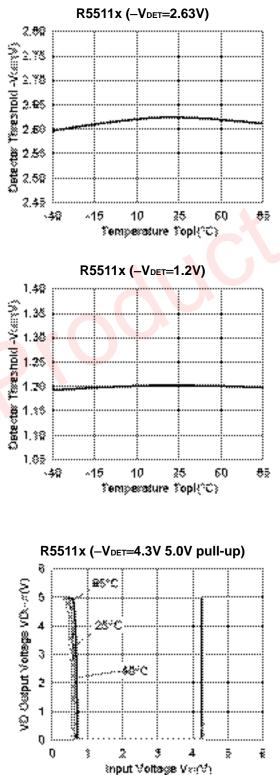
R5511x



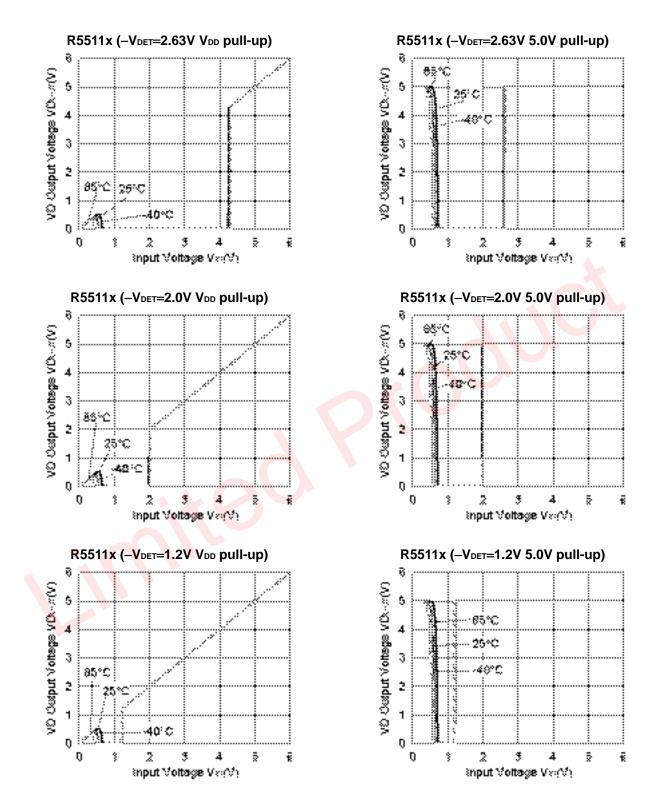
R5511x





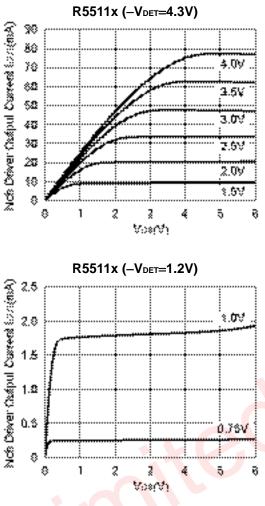


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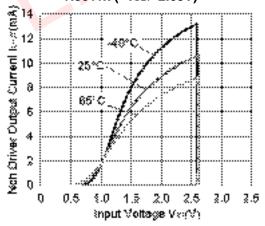


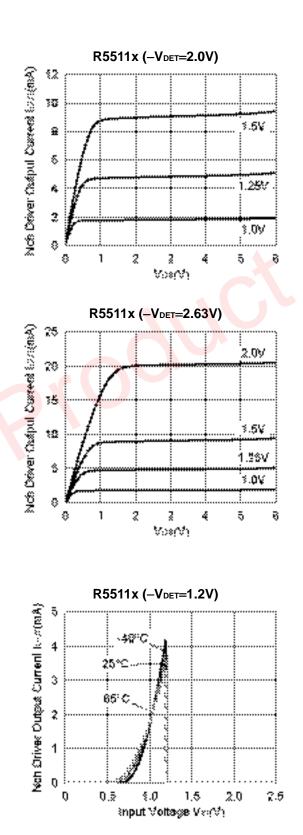
R5511x

14) Nch Driver Output Current vs. VDs

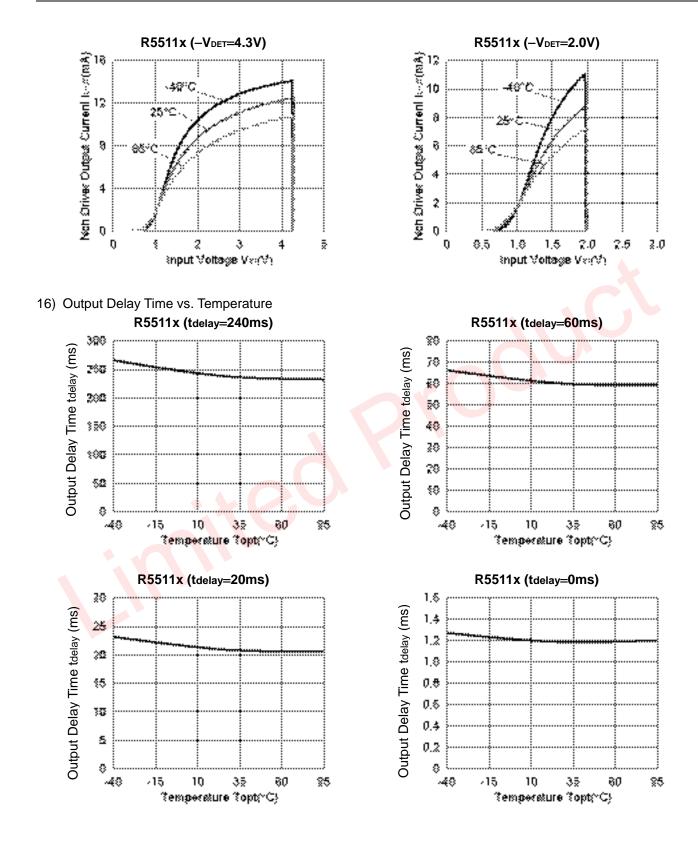


15) Nch Driver Output Current vs. Input Voltage R5511x (–VDET=2.63V)





R5511x



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