

# MASK ROM



☆ New product  
★ Under development

## ■ MASK ROMs (Continued)

Capacity	Bit configuration	Pinout**	Model No.	User's No.	Access time (ns) MAX. Cycle time (ns) MIN.	Current consumption (mA) MAX.	Supply voltage (V)	Package	
4M	× 8	J	LH53V4T00E	LH-5V4TXX	120	35	2.7 to 3.6	32TSOP(I)*2, *4	
		J	LH53V4R00AN/AT	LH-5V4HXX	120	35	2.7 to 3.6	32SOP/32TSOP(I)*4	
		J	LH53V4R00N/T	LH-5V4RXX	150	35	2.7 to 3.6	32SOP/32TSOP(I)*4	
		J	LH53V4Y00N/E	LH-5V4YXX	450	25	1.8 to 3.6	32SOP/32TSOP(I)*2, *4	
		J	LH53H4100D/N	LH-5H41XX	80	90	4.5 to 5.5	32DIP/32SOP	
		J	LH534700D/N	LH-5S47XX	100	75	4.5 to 5.5	32DIP/32SOP	
		J	LH534R00BD/BN	LH-5S4RXX	120	65	4.5 to 5.5	32DIP/32SOP	
		F	LH53V4B00T	LH-5V4BXX	150	35	3.0 to 3.6	40TSOP(I)*4	
	× 16	F	LH534A00T	LH-5S4AXX	120	65	4.5 to 5.5	32TSOP(I)*4	
		F	LH534B00T	LH-5S4BXX	120	60	4.5 to 5.5	40TSOP(I)*4	
		J	LH534096D/N/U	LH-5S4HXX	100	75	4.5 to 5.5	40DIP/40SOP/44QFJ	
		J	LH534Y00AD/AN	LH-5S4YXX	120	65	4.5 to 5.5	40DIP/40SOP	
		× 8/× 16	M	LH53V4P00N/T	LH-5V4PXX	120	35	2.7 to 3.6	40SOP/48TSOP(I)*4
			M	LH534600CD/CN/CT	LH-5S46XX	100	75	4.5 to 5.5	40DIP/40SOP/48TSOP(I)*4
			M	LH534P00BD/BN/BT	LH-5S4PXX	120	65	4.5 to 5.5	40DIP/40SOP/48TSOP(I)*4
			F	☆ LH53V4C00E	LH-5V4CXX	120	35	3.0 to 3.6	48TSOP(I)*3, *4
8M	× 8	J	LH538700AD/AN/AS*	LH-5S8VXX	100	100	4.5 to 5.5	32DIP/32SOP/32TSOP(II)*4**	
		J	LH538R00CD/CN/CS*	LH-5S8RXX	120	60	4.5 to 5.5	32DIP/32SOP/32TSOP(II)*4**	
	× 8/× 16	M	☆ LH53V8500NT	LH-5V85XX	150	35	2.7 to 3.6	44SOP/48TSOP(I)*4	
		M	LH538600CD/CN/CT	LH-5S86XX	100	100	4.5 to 5.5	42DIP/44SOP/48TSOP(I)*4	
		M	☆ LH538W00D/N	LH-MN8WXX	100	100	4.5 to 5.5	42DIP/44SOP	
		M	LH538P00CD/CN/CT	LH-5S8PXX	120	60	4.5 to 5.5	42DIP/44SOP/48TSOP(I)*4	
		F	LH53FV8P00T	LH-5G8PXX	120	35	3.0 to 3.6	56TSOP(I)*4	
		F	★ LH53FV8500E	LH-5G85XX	150	35	2.7 to 3.6	48TSOP(I)*3, *4	
16M	× 8/× 16	M	LH53V16500AN/AT	LH-MV7FXX	150	35	2.7 to 3.6	44SOP/48TSOP(I)*4	
		M	LH5316600AD/AN/AS	LH-537UXX	100	100	4.5 to 5.5	42DIP/44SOP/44TSOP(II)*4	
		M	LH5316P00DD/DN/DS	LH-537NXX	120	70	4.5 to 5.5	42DIP/44SOP/44TSOP(II)*4	
		M	LH5316P00DT/DTR	LH-537ZXX	120	70	4.5 to 5.5	48TSOP(I)*4/48TSOP(I)*5	
		F	LH53FV16P00AT	LH-5G7PXX	120	35	3.0 to 3.6	56TSOP(I)*4	
24M	× 8	M	LH5324000D	LH-5360XX	150	65	4.5 to 5.5	42DIP	
		M	LH5324C00D	LH-536CXX	120	80	4.5 to 5.5	42DIP	
	× 16	M	LH5324A00AD	LH-536GXX	150	65	4.5 to 5.5	42DIP	
		M	LH5324P00N	LH-536PXX	120	80	4.5 to 5.5	44SOP	
32M	× 8	M	LH5332Y00D	LH-MN5YXX	100	100	4.5 to 5.5	42DIP	
		M	☆ LH5332000BD	LH-MN5RXX	150	65	4.5 to 5.5	42DIP	
	× 8/× 16	M	LH5332H00D	LH-MN5HXX	100	100	4.5 to 5.5	42DIP	
		M	LH5332C00BD	LH-MN5LXX	120	80	4.5 to 5.5	42DIP	
		M	★ LH53V32P00N/T	LH-MV5PXX	120	35	2.7 to 3.6	44SOP/48TSOP(I)*4	
		M	LH53V32500AN	LH-MV5GXX	150	35	2.7 to 3.6	44SOP	
		M	LH53V32500AT	LH-MV5CXX	150	35	2.7 to 3.6	48TSOP(I)*4	
		M	LH5332600AN	LH-MN5UXX	100	100	4.5 to 5.5	44SOP	
		M	LH5332600AT	LH-MN5VXX	100	100	4.5 to 5.5	48TSOP(I)*4	
		M	LH5332P00BN	LH-MN5KXX	120	80	4.5 to 5.5	44SOP	
M	LH5332P00BT	LH-MN5SXX	120	80	4.5 to 5.5	48TSOP(I)*4			
64M	× 16	M	★ LH5364H00D	LH-MN0HXX	100	100	4.5 to 5.5	42DIP	
		M	☆ LH5364P00D	LH-MN0CXX	120	80	4.5 to 5.5	42DIP	
	× 8/× 16	M	☆ LH53V64P00T	LH-MV0BXX	120*1	35	3.0 to 3.6*1	48TSOP(I)*4	
		M	☆ LH53V64P00N	LH-MV0PXX	120*1	35	3.0 to 3.6*1	44SOP	
		M	★ LH5364600N	LH-MN06XX	100	100	4.5 to 5.5	44SOP	
		M	★ LH5364600T	LH-MN0BXX	100	100	4.5 to 5.5	48TSOP(I)*4	
		M	LH5364P00N	LH-MN0PXX	120	80	4.5 to 5.5	44SOP	
		M	LH5364P00T	LH-MN0DXX	120	80	4.5 to 5.5	48TSOP(I)*4	
× 8/× 16	M	LH5364P00N-1	LH-MN0PXX	150	70	4.5 to 5.5	44SOP		
	M	LH5364P00T-1	LH-MN0DXX	150	70	4.5 to 5.5	48TSOP(I)*4		

※ J: JEDEC standard EPROM pinout M: Mask ROM specific pinout F: Flash memory pinout  
QFJ = PLCC

\*1 2.7 V read available (access time at 2.7 V = 150 ns) \*2 Nominal dimensions: 8 x 13.4 mm \*3 Nominal dimensions: 12 x 20 mm  
\*4 Normal bend \*5 Reverse bend