

# FAST 74F805, 74F1805

## NOR Drivers

### FAST Products

#### FEATURES

- High capacitive drive capability
- Choice of configuration  
Corner V<sub>CC</sub> and GND—'F805  
Center V<sub>CC</sub> and GND—'F1805
- Typical propagation delay of 2.3ns

#### 74F805-Hex Two-Input NOR Driver 74F1805-Hex Two-Input NOR Driver Product Specification

TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
74F805	2.3ns	10mA
74F1805	2.3ns	10mA

#### ORDERING INFORMATION

PACKAGES	COMMERCIAL RANGE V <sub>CC</sub> = 5V±10%; T <sub>A</sub> = 0°C to +70°C
20-Pin Plastic DIP	N74F805N, N74F1805N
20-Pin Plastic SOL	N74F805D, N74F1805D

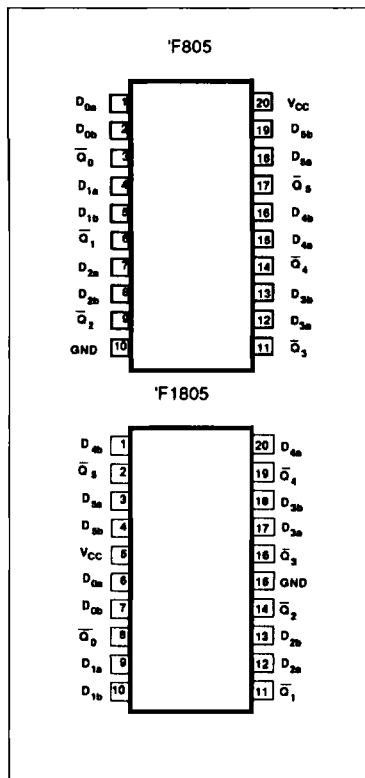
#### INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

PINS	DESCRIPTION	74F(U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
D <sub>0a</sub> - D <sub>nb</sub>	Data inputs	1.0/0.033	20μA/20μA
Q <sub>0</sub> - Q <sub>5</sub>	Data outputs	2400/80	48mA/48mA

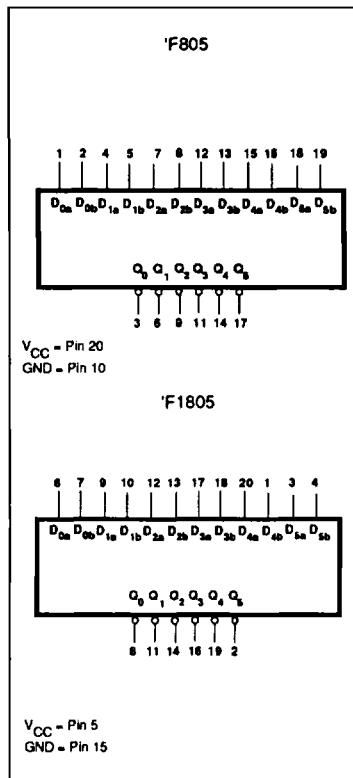
#### NOTE:

One (1.0) FAST Unit Load is defined as: 20μA in the High state and 0.6mA in the Low state.

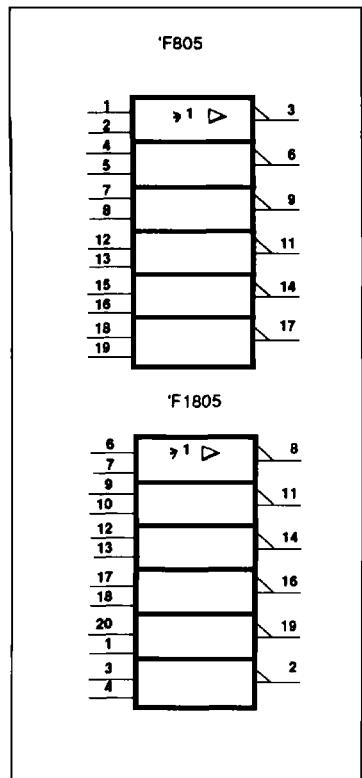
#### PIN CONFIGURATION



#### LOGIC SYMBOL



#### LOGIC SYMBOL(IEEE/IEC)



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## FUNCTION TABLE

INPUTS		OUTPUT
D <sub>na</sub>	D <sub>nb</sub>	Q <sub>n</sub>
H	X	L
X	H	L
L	L	H

- H = High voltage level  
 L = Low voltage level  
 X = Don't care

**ABSOLUTE MAXIMUM RATINGS** (Operation beyond the limits set forth in this table may impair the useful life of the device. Unless otherwise noted these limits are over the operating free-air temperature range.)

SYMBOL	PARAMETER	RATING		UNIT
		Min	Max	
V <sub>CC</sub>	Supply voltage	-0.5 to +7.0		V
V <sub>IN</sub>	Input voltage	-0.5 to +7.0		V
I <sub>IN</sub>	Input current	-30 to +5		mA
V <sub>OUT</sub>	Voltage applied to output in High output state	-0.5 to +V <sub>CC</sub>		V
I <sub>OUT</sub>	Current applied to output in Low output state	96		mA
T <sub>A</sub>	Operating free-air temperature range	0 to +70		°C
T <sub>STG</sub>	Storage temperature	-65 to +150		°C

## RECOMMENDED OPERATION CONDITIONS

SYMBOL	PARAMETER	LIMITS			UNIT
		Min	Nom	Max	
V <sub>CC</sub>	Supply voltage	4.5	5.0	5.5	V
V <sub>IH</sub>	High-level input voltage	2.0			V
V <sub>IL</sub>	Low-level input voltage			0.8	V
I <sub>K</sub>	Input clamp current			-18	mA
I <sub>OH</sub>	High-level output current			-48	mA
I <sub>OL</sub>	Low-level output current			48	mA
T <sub>A</sub>	Operating free-air temperature range	0		70	°C

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## DC ELECTRICAL CHARACTERISTICS (Over recommended operating free-air temperature range unless otherwise noted.)

SYMBOL	PARAMETER	TEST CONDITIONS <sup>1</sup>		LIMITS			UNIT	
		Min	Typ <sup>2</sup>	Max				
$V_{OH}$	High-level output voltage	$V_{CC} = \text{MIN}$ , $V_{IL} = \text{MAX}$	$\pm 10\%V_{CC}$	2.0			V	
		$V_{IH} = \text{MIN}$ , $I_{OH} = \text{MAX}$	$\pm 5\%V_{CC}$	2.0			V	
$V_{OL}$	Low-level output voltage	$V_{CC} = \text{MIN}$ , $V_{IL} = \text{MAX}$	$\pm 10\%V_{CC}$		0.38	0.55	V	
		$V_{IH} = \text{MIN}$ , $I_{OL} = \text{MAX}$	$\pm 5\%V_{CC}$		0.38	0.55	V	
$V_{IK}$	Input clamp voltage	$V_{CC} = \text{MIN}$ , $I_I = I_{IK}$			-0.73	-1.2	V	
$I_I$	Input current at maximum input voltage	$V_{CC} = \text{MAX}$ , $V_I = 7.0V$			100	$\mu A$		
$I_{IH}$	High-level input current	$V_{CC} = \text{MAX}$ , $V_I = 2.7V$			20	$\mu A$		
$I_{IL}$	Low-level input current	$V_{CC} = \text{MAX}$ , $V_I = 0.5V$			-20	$\mu A$		
$I_O$	Output current <sup>3</sup>	$V_{CC} = \text{MAX}$ , $V_O = 2.25V$		-60		-150	mA	
$I_{CC}$	Supply current (total)	$I_{CCH}$	$V_{CC} = \text{MAX}$	$V_{IN} = \text{GND}$		3	5	mA
		$I_{CCL}$		$V_{IN} = 4.5V$		17	25	mA

## NOTES:

1. For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
2. All typical values are at  $V_{CC} = 5V$ ,  $T_A = 25^\circ C$ .
3. The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current,  $I_{OS}$ .

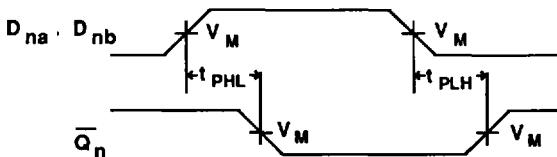
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## AC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	TEST CONDITION	LIMITS					UNIT	
			$T_A = +25^\circ\text{C}$		$T_A = 0^\circ\text{C to } +70^\circ\text{C}$				
			$V_{CC} = 5\text{V}$	$C_L = 50\text{pF}$	$V_{CC} = 5\text{V} \pm 10\%$	$C_L = 50\text{pF}$	$R_L = 500\Omega$		
$t_{PLH}$	Propagation delay $D_{na}, D_{nb}$ to $\bar{Q}_n$	Waveform 1	Min 1.0	Typ 2.0	Max 4.0	Min 1.0	Typ 4.0	Max 4.5	ns
$t_{PHL}$			1.0	2.5	4.5	1.0	4.0	4.5	

## AC WAVEFORMS

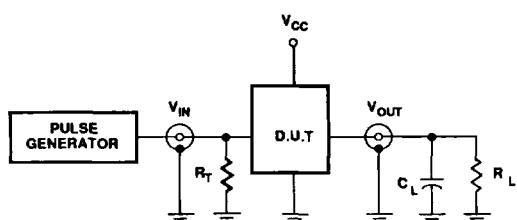


Waveform 1.

## Propagation Delay for Input to Output

NOTE: For all waveforms,  $V_M = 1.5\text{V}$ .

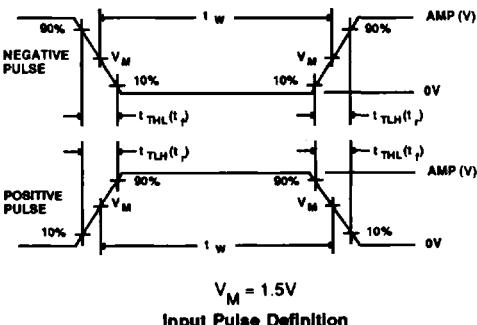
## TEST CIRCUIT AND WAVEFORMS



Test Circuit For Totem-Pole Outputs

## DEFINITIONS

- $R_L$  = Load resistor; see AC CHARACTERISTICS for value.  
 $C_L$  = Load capacitance includes jig and probe capacitance; see AC CHARACTERISTICS for value.  
 $R_T$  = Termination resistance should be equal to  $Z_{OUT}$  of pulse generators.

 $V_M = 1.5\text{V}$ 

## Input Pulse Definition

FAMILY	INPUT PULSE REQUIREMENTS				
	Amplitude	Rep. Rate	$t_W$	$t_{TLH}$	$t_{THL}$
74F	3.0V	1MHz	500ns	2.5ns	2.5ns