Vishay Semiconductors

ADD-A-PAK Generation VII Power Modules Standard Diodes, 80 A



ADD-A-PAK

| PRODUCT SUMMARY | | | | | |
|--------------------|-------------------------------|--|--|--|--|
| I _{F(AV)} | 80 A | | | | |
| Туре | Modules - Diode, High Voltage | | | | |

MECHANICAL DESCRIPTION

The ADD-A-PAK generation VII, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

FEATURES

- · High voltage
- Industrial standard package



- · Low thermal resistance
- UL approved file E78996
- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- Up to 1600 V
- High surge capability
- · Easy mounting on heatsink

ELECTRICAL DESCRIPTION

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

| MAJOR RATINGS AND CHARACTERISTICS | | | | | | |
|-----------------------------------|-----------------|-------------|-------------------|--|--|--|
| SYMBOL | CHARACTERISTICS | VALUES | UNITS | | | |
| I _{F(AV)} | 110 °C | 80 | | | | |
| I _{F(RMS)} | | 126 | Α | | | |
| ı | 50 Hz | 1500 | A | | | |
| I _{FSM} | 60 Hz | 1570 | | | | |
| 2 † | 50 Hz | 11.25 | kA ² s | | | |
| 1-1 | 60 Hz | 10.26 | KA ² S | | | |
| $I^2\sqrt{t}$ | | 112.5 | kA²√s | | | |
| V _{RRM} | Range | 400 to 1600 | V | | | |
| T _J | | - 40 to 150 | °C | | | |
| T _{Stg} | | - 40 (0 150 | C | | | |



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ELECTRICAL SPECIFICATIONS

| VOLTAGE RATINGS | | | | | | | | |
|-----------------|-----------------|--|--|--|--|--|--|--|
| TYPE NUMBER | VOLTAGE CODE | V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V | V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V | I _{RRM} MAXIMUM AT T _J = 150 °C mA | | | | |
| | 04 | 400 | 500 | | | | | |
| | 06 | 600 | 700 | | | | | |
| | 08 | 800 | 900 | | | | | |
| VSK.71 | 10 | 1000 | 1100 | 10 | | | | |
| | 12 | 1200 | 1300 | | | | | |
| | 14 | 1400 | 1500 | | | | | |
| | 16 | 1600 | 1700 | | | | | |

| FORWARD CONDUCTION | | | | | | |
|---|---------------------|--|--|--|---------|---------------------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | | VALUES | UNITS |
| Maximum average forward current at case temperature | I _{F(AV)} | 180° conduction, half sine wave | | 80 110 | A °C | |
| Maximum RMS forward current | I _{F(RMS)} | DC at 90 °C | case temperat | ure | 126 | 0 |
| | () | t = 10 ms | · | | 1500 | |
| Maximum peak, one-cycle forward, | | t = 8.3 ms | reapplied | | 1570 | Α |
| non-repetitive surge current | I _{FSM} | t = 10 ms | 100 % V _{RRM} | | 1260 | |
| | | t = 8.3 ms | reapplied | Sinusoidal half wave, | 1320 | |
| | l ² t | t = 10 ms | No voltage | intitial T _J = T _J maximum | 11.25 | - kA ² s |
| Maximum I ² t for fusing | | t = 8.3 ms | reapplied | | 10.26 | |
| Maximum 1-t for fusing | | t = 10 ms | 100 % V _{RRM} | | 7.95 | |
| | | t = 8.3 ms | reapplied | | 7.23 | |
| Maximum $I^2\sqrt{t}$ for fusing | I ² √t | t = 0.1 ms t | o 10 ms, no vol | tage reapplied | 112.5 | kA ^{2√} s |
| Low level value of threshold voltage | V _{F(TO)1} | (16.7 % x π | $x I_{F(AV)} < I < \pi x$ | $I_{F(AV)}$, $T_J = T_J$ maximum | 0.73 | V |
| High level value of threshold voltage | V _{F(TO)2} | $(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$ | | | 0.83 | V |
| Low level value of forward slope resistance | r _{f1} | (16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum | | 3.22 | mΩ | |
| High level value of forward slope resistance | r _{f2} | $(I > \pi \times I_{F(AV)})$, $T_J = T_J$ maximum | | 2.89 | 1115.2 | |
| Maximum forward voltage drop | V_{FM} | $I_{FM} = \pi \times I_{F(I)}$ | _{AV)} , T _J = 25 °C, | t _p = 400 μs square wave | 1.6 | V |

| BLOCKING | | | | | | |
|--------------------------------------|------------------|-------------------------|----------------------------|-------|--|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS | | |
| Maximum peak reverse leakage current | I _{RRM} | T _J = 150 °C | 10 | mA | | |
| Maximum RMS insulation voltage | V _{INS} | 50 Hz | 3000 (1 min) 3600 (1 s) | V | | |



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| THERMAL AND MECHANICAL SPECIFICATIONS | | | | | | |
|--|--|---|---------------|-------------------|--|--|
| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS | | |
| Junction and storage temperature r | ange T _J , T _{Stg} | | - 40 to 150 | °C | | |
| Maximum internal thermal resistance junction to case per leg | ee, R _{thJC} | DC operation | 0.28 | °C/W | | |
| Typical thermal resistance, case to heatsink per module | R _{thCS} | Mounting surface flat, smooth and greased | 0.1 | C/W | | |
| to hea | ntsink | A mounting compound is recommended and the | 4 | N | | |
| Mounting torque ± 10 % but | usbar | torque should be rechecked after a period of 3 hours to allow for the spread of the compound. | 3 | Nm | | |
| Approximate weight | | | 75 | g | | |
| Approximate weight | | | 2.7 | OZ. | | |
| Case style | | JEDEC | ADD-A-PAK Ger | n. VII (TO-240AA) | | |

| △R CONDUCTION PER JUNCTION | | | | | | | | | | | |
|----------------------------|---------------------------|-------|-------|-------|-------|-----------------------------|-------|------|-------|--------|-------|
| DEVICES | SINE HALF WAVE CONDUCTION | | | | | RECTANGULAR WAVE CONDUCTION | | | | LINUTO | |
| DEVICES | 180° | 120° | 90° | 60° | 30° | 180° | 120° | 90° | 60° | 30° | UNITS |
| VSK.71 | 0.075 | 0.088 | 0.113 | 0.155 | 0.228 | 0.06 | 0.094 | 0.12 | 0.158 | 0.23 | °C/W |

Note

Table shows the increment of thermal resistance R_{th,JC} when devices operate at different conduction angles than DC

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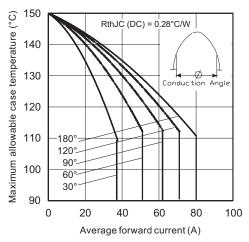


Fig. 1 - Current Ratings Characteristics

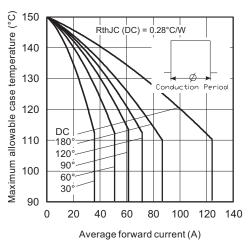


Fig. 2 - Current Ratings Characteristics

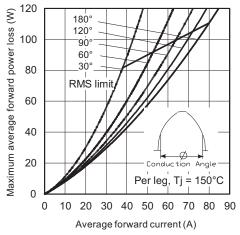


Fig. 3 - Forward Power Loss Characteristics

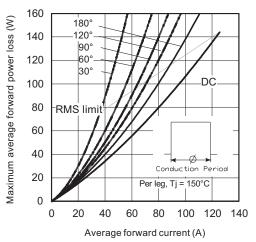
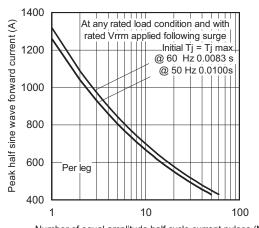


Fig. 4 - Foward Power Loss Characteristics



Number of equal amplitude half cycle current pulses (N)

Fig. 5 - Maximum Non-Repetitive Surge Current

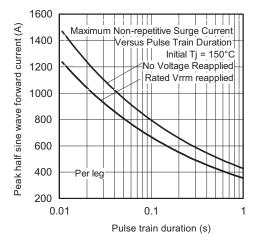


Fig. 6 - Maximum Non-Repetitive Surge Current

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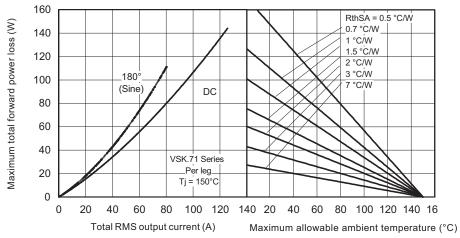


Fig. 7 - Forward Power Loss Characteristics

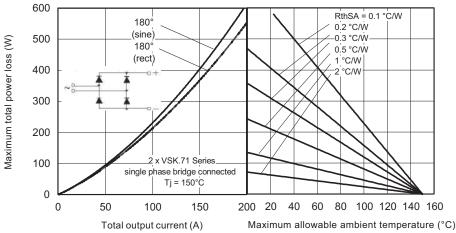


Fig. 8 - Forward Power Loss Characteristics

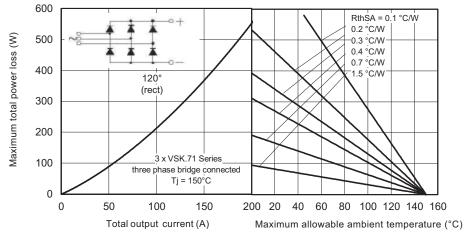


Fig. 9 - Forward Power Loss Characteristics

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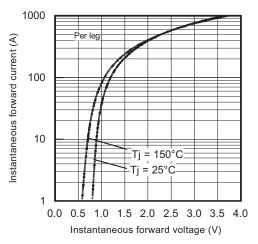


Fig. 10 - Forward Voltage Characteristics

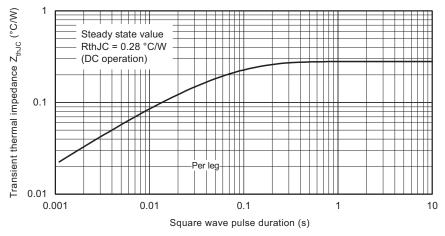
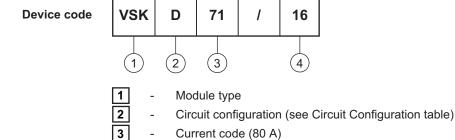


Fig. 11 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE



- Voltage code (see Voltage Ratings table)

Note

• To order the optional hardware go to www.vishay.com/doc?95172



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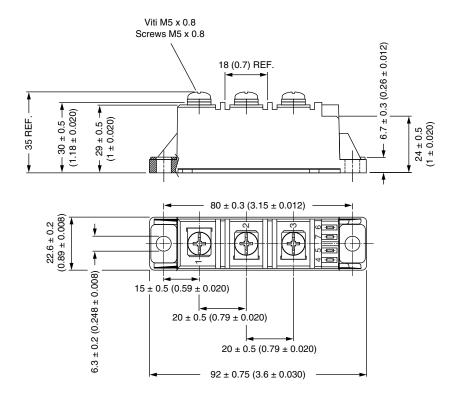
| CIRCUIT CONFIGURATION | | | | | | |
|----------------------------|-------------------------------|-------------------------|--|--|--|--|
| CIRCUIT DESCRIPTION | CIRCUIT CONFIGURATION CODE | CIRCUIT DRAWING | | | | |
| Two diodes doubler circuit | D | VSKD (1) ~ (2) ~ (3) | | | | |
| Two diodes common cathodes | С | VSKC (1) - (2) (3) | | | | |
| Two diodes common anodes | J | VSKJ (1) - + (2) + (3) | | | | |
| Single diode | E | VSKE (2) - (3) | | | | |

| LINKS TO RELATED DOCUMENTS | | | | |
|----------------------------|--------------------------|--|--|--|
| Dimensions | www.vishay.com/doc?95369 | | | |

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ADD-A-PAK Generation VII - Diode

DIMENSIONS in millimeters (inches)





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