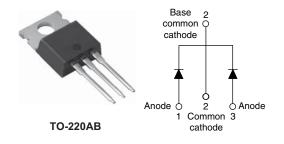
### Vishay High Power Products

# Schottky Rectifier, 2 x 10 A



2 x 10 A

35 to 45 V

**PRODUCT SUMMARY** 

I<sub>F(AV)</sub>

 $V_{\mathsf{R}}$ 

SHA

### FEATURES

- 175 °C T<sub>J</sub> operation
- Center tap TO-220 package
- Low forward voltage drop
- High frequency operation



- RoHS\*
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

### DESCRIPTION

The 20CTQ...PbF center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	SYMBOL CHARACTERISTICS VALUES					
I <sub>F(AV)</sub>	Rectangular waveform	20	A			
V <sub>RRM</sub>	Range	35 to 45	V			
I <sub>FSM</sub>	$t_p = 5 \ \mu s \ sine$	1060	A			
V <sub>F</sub>	10 Apk, $T_J = 125 \ ^\circ C$ (per leg)	0.57	V			
TJ	Range	- 55 to 175	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	20CTQ035PbF	20CTQ040PbF	20CTQ045PbF	UNITS	
Maximum DC reverse voltage	V <sub>R</sub>	- 35	40	45	V	
Maximum working peak reverse voltage V <sub>RW</sub>		33	40	45	v	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 145 °C, rectangular waveform		20			
Maximum peak one cycle non-repetitive surge current per leg	I <sub>FSM</sub>	5 $\mu s$ sine or 3 $\mu s$ rect. pulse	Following any rated load condition and with rated	1060	A		
See fig. 7		10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	265			
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J$ = 25 °C, $I_{AS}$ = 2.0 A, L = 6.5 mH		13	mJ		
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2.0	А		

\* Pb containing terminations are not RoHS compliant, exemptions may apply



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
	V <sub>FM</sub> <sup>(1)</sup>	10 A	T OF OC	0.64	V	
Maximum forward voltage drop per leg		20 A	T <sub>J</sub> = 25 °C	0.76		
See fig. 1		10 A	T 105 %O	0.57		
		20 A	T <sub>J</sub> = 125 °C	0.68		
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	$T_J = 25 \ ^{\circ}C$	$V_{\rm B}$ = Rated $V_{\rm B}$	2	mA	
See fig. 2		T <sub>J</sub> = 125 °C	$v_{\rm R}$ = naleu $v_{\rm R}$	15		
Maximum junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		900	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs	

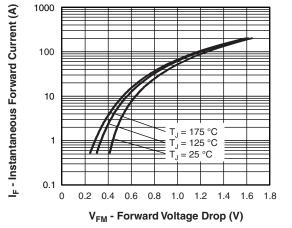
#### Note

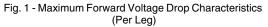
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

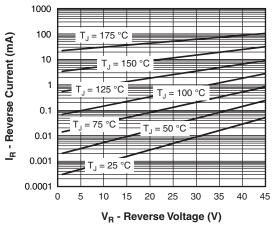
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4	3.25		
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.63	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased 0.50			
Approximate weight				2	g	
Approximate weight				0.07	oz.	
Manualian tanan	minimum			6 (5)	kgf ⋅ cm	
Mounting torque maximum				12 (10)	(lbf $\cdot$ in)	
Marking device			2		OCTQ035	
			Case style TO-220AB	20CT	Q040	
				20CT	20CTQ045	

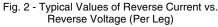


Schottky Rectifier, 2 x 10 A Vishay High Power Products









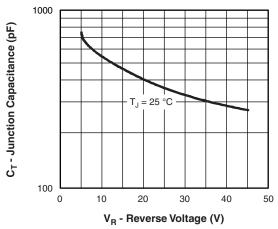
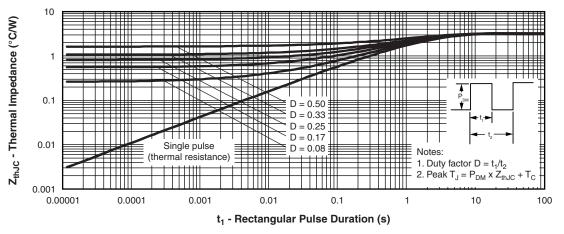
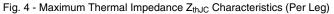


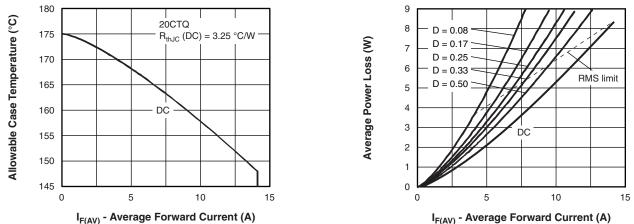
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

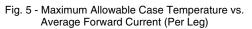




## 20CTQ...PbF Series

Vishay High Power Products Schottky Rectifier, 2 x 10 A







**SHA** 

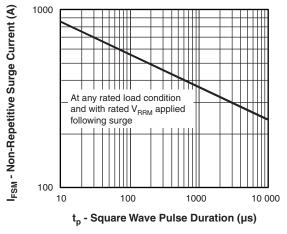


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

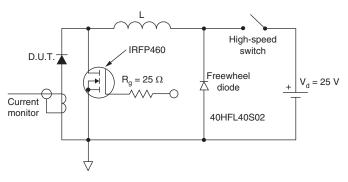


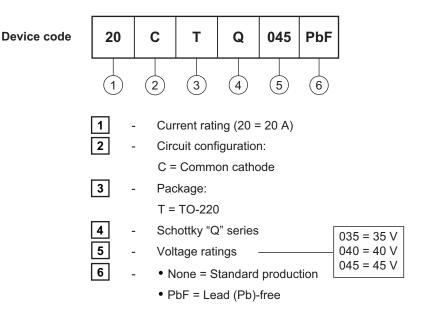
Fig. 8 - Unclamped Inductive Test Circuit





Schottky Rectifier, 2 x 10 A Vishay High Power Products

### ORDERING INFORMATION TABLE



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95222				
Part marking information	www.vishay.com/doc?95225			



**Vishay Semiconductors** 

**TO-220AB** 

#### **DIMENSIONS** in millimeters and inches





.ead	assignments

**Diodes** 

1. - Anode/open 2. - Cathode 3. - Anode

SYMBOL	MILLIN	IETERS	INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

#### Notes

- <sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension and finish uncontrolled in L1
- <sup>(3)</sup> Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left( 4\right) }$  Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 10.51 0.414 10.11 0.398 3,6 Е E1 6.86 8.89 0.270 0.350 6 E2 0.76 0.030 7 --2.41 2.67 0.095 0.105 е 0.208 e1 4.88 5.28 0.192 H1 6.09 6.48 0.240 0.255 6,7 13.52 14.02 0.532 0.552 L L1 3.32 3.82 0.131 0.150 2 ØΡ 3.54 3.73 0.139 0.147 2.60 0.102 Q 3.00 0.118 90° to 93° 90° to 93° θ

Conforms to JEDEC outline TO-220AB

- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



Vishay

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