

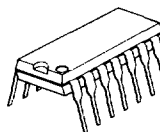
## NJM2901

These devices offer higher frequency operation and faster switching than can be had from internally compensated quad op amps. Indeed for single-supply applications, the Darlington PNP input stage allows them to compare voltages that include ground. The two-stage common-emitter output circuit provides gain and output sink capacity of 6mA at an output level of 400mV. The output collector is left open, permitting the designer to drive devices in the range of 2V to 36V.

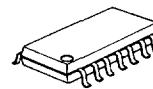
### Absolute Maximum Ratings (Ta=25°C)

Supply Voltage	V <sup>+</sup>	36V (±18V)
Differential Input Voltage	V <sub>ID</sub>	36V
Input Voltage	V <sub>IN</sub>	-0.3~+36V
Power Dissipation	P <sub>D</sub> (N-Type)	570mW
	(M,V-Type)	300mW
Operating Temperature Range	T <sub>opr</sub>	-40~+85°C
Storage Temperature Range	T <sub>stg</sub>	-50~+125°C

### Package Outline



NJM2901N



NJM2901M  
NJM2901E

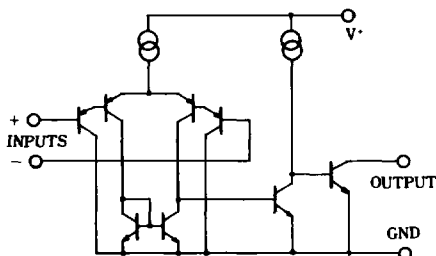


NJM2901V

### Electrical Characteristics (Ta=25°C, V<sup>+</sup>=5V)

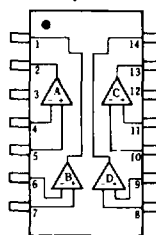
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input Offset Voltage	V <sub>IO</sub>	R <sub>S</sub> =0Ω, V <sub>O</sub> ≅1.4V	—	2	7	mV
Input Offset Current	I <sub>IO</sub>	I <sub>IN</sub> <sup>+</sup> = I <sub>IN</sub> <sup>-</sup>	—	5	50	nA
Input Bias Current	I <sub>B</sub>		—	25	250	nA
Input Common Mode Voltage Range	V <sub>ICM</sub>		0~3.5	—	—	V
Large Signal Voltage Gain	A <sub>V</sub>	R <sub>L</sub> =15kΩ	—	106	—	dB
Response Time	t <sub>R</sub>	R <sub>L</sub> =5.1kΩ	—	1.3	—	μs
Output Sink Current	I <sub>SINK</sub>	V <sub>IN</sub> <sup>-</sup> =1V, V <sub>IN</sub> <sup>+</sup> =0V, V <sub>O</sub> =1.5V	6	16	—	mA
Output Saturation Voltage	V <sub>SAT</sub>	V <sub>IN</sub> <sup>-</sup> =1V, V <sub>IN</sub> <sup>+</sup> =0V, I <sub>SINK</sub> =3mA	—	200	400	mV
Output Leakage Current	I <sub>LEAK</sub>	V <sub>IN</sub> <sup>-</sup> =0V, V <sub>IN</sub> <sup>+</sup> =1V, V <sub>O</sub> =5V	—	0.1	1.0	μA
Quiescent Current	I <sub>CC</sub>	R <sub>L</sub> =∞	—	0.8	2	mA

### Equivalent Circuit (1/4 Shown)



### Connection Diagram

N,M,V-Type  
(Top View)

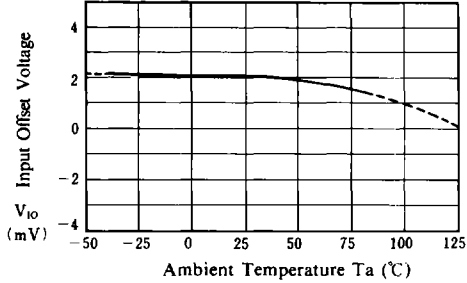


#### PIN FUNCTION

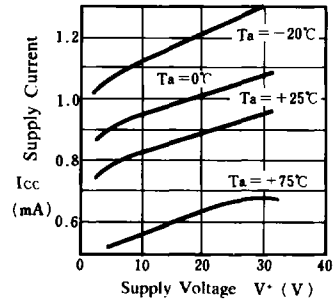
1. B OUTPUT
2. A OUTPUT
3. V<sup>+</sup>
4. A - INPUT
5. A + INPUT
6. B - INPUT
7. B + INPUT
8. C - INPUT
9. C + INPUT
10. D - INPUT
11. D + INPUT
12. GND
13. D OUTPUT
14. C OUTPUT

## ■ Typical Characteristics

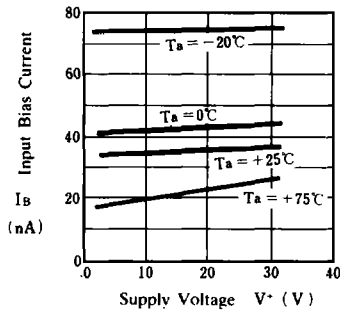
**Input Offset Voltage vs. Temperature**  
( $V^+ = 5\text{ V}$ )



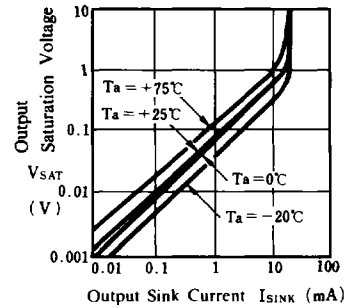
**Supply Current**  
( $R_L = \infty$ )



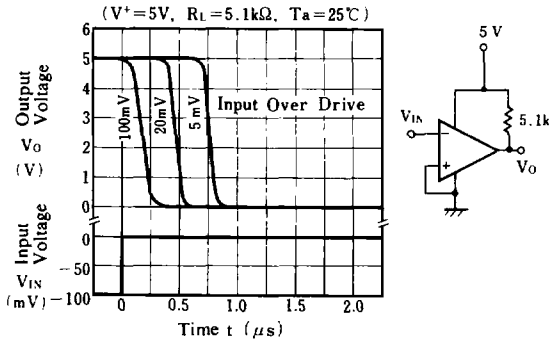
**Input Bias Current**



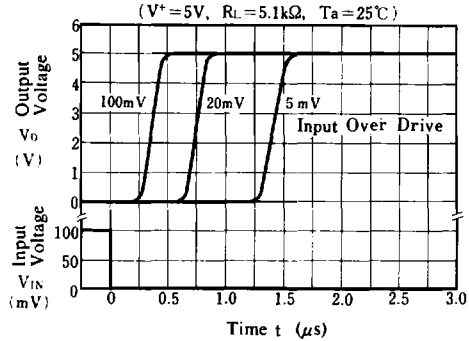
**Output Saturation Voltage**  
( $V^+ = 5\text{ V}$ )



**Response Time for Various Input Over Drives**  
( $V^+ = 5\text{ V}$ ,  $R_L = 5.1\text{ k}\Omega$ ,  $T_a = 25^\circ\text{C}$ )

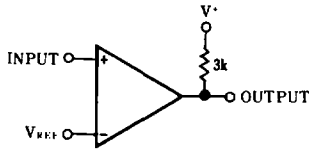


**Response Time for Various Input Over Drives**  
( $V^+ = 5\text{ V}$ ,  $R_L = 5.1\text{ k}\Omega$ ,  $T_a = 25^\circ\text{C}$ )

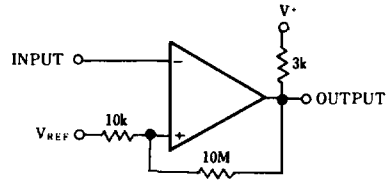


■ Typical Applications

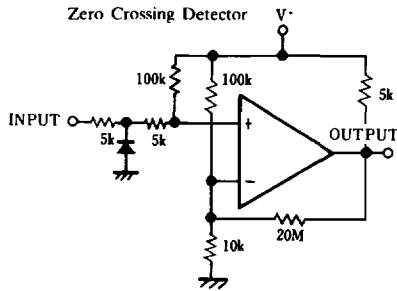
Basic Comparator



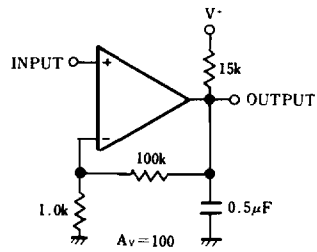
Comparator With Hysteresis



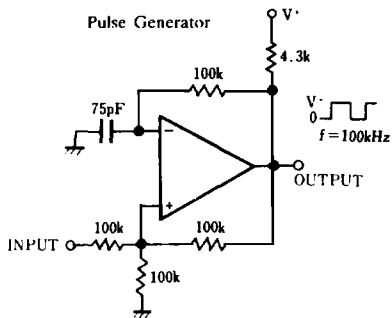
Zero Crossing Detector



Low Frequency Op Amp.



Pulse Generator



Limit Comparator

