



048223

HS-C²MOS™ INTEGRATED CIRCUITS

PRODUCT PREVIEW

OCTAL BUS BUFFER WITH NONINVERTED 3-STATE OUTPUTS

DESCRIPTION

The M54/74HCT244 is a high speed CMOS OCTAL BUS BUFFER WITH NONINVERTED 3-STATE OUTPUTS fabricated in silicon gate C²MOS technology. It has the same high speed performance of LSTTL combined with true CMOS low power consumption. This device is a non-inverting buffer and has two active low enables (1G and 2G). Each enable independently controls 4 buffers. This device does not have Schmitt trigger inputs.

This device is designed to be used with 3-state memory address drivers, etc.

All inputs are equipped with protection circuits against static discharge or transient excess voltage. This integrated circuit has totally compatible, input and output characteristic, with standard 54/74 LSTTL logic families.

M54HCT/74HCT devices are designed to directly interface HSC²MOS systems with TTL and NMOS components. These devices are also plug in replacement for LSTTL devices giving a reduction of power consumption.

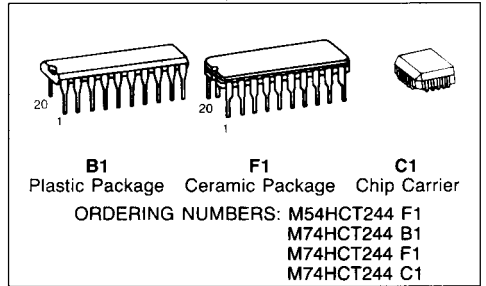
FEATURES

- Low Power Dissipation
 $I_{CC} = 4 \mu A$ (Max.) at $T_A = 25^\circ C$
- High Noise Immunity
 $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (Min.)
- Output Drive Capability
15 LSTTL Loads
- Symmetrical Output Impedance
 $|I_{OH}| = I_{OL} = 6 mA$ (Min.)
- Balanced Propagation Delays
 $t_{PLH} = t_{PHL}$
- Pin and Function compatible with 54/74LS244

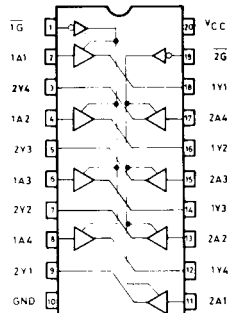
TRUTH TABLE

INPUTS		OUTPUTS
\bar{G}	A_n	Y_n
L	L	L
L	H	H
H	Don't Care	Z

Z: High Impedance



PIN CONNECTIONS (top view)



Dual in line

CHIP CARRIER

