TOSHIBA 2SJ360

TOSHIBA FIELD EFFECT TRANSISTOR SILICON P CHANNEL MOS TYPE (L²-π-MOS V)

2 S J 3 6 0

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS.

CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS.

4V GATE DRIVE

Low Drain-Source ON Resistance : $R_{DS(ON)} = 0.55\Omega$ (Typ.)

High Forward Transfer Admittance : |Yfs|=0.9S (Typ.)

Low Leakage Current : $I_{DSS} = -100 \mu A \text{ (Max.)} (V_{DS} = -60 \text{V})$

Enhancement-Mode $V_{th} = -0.8 \sim -2.0 V$

 $(V_{DS} = -10V, I_D = -1mA)$

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERIS	SYMBOL	RATING	UNIT	
Drain-Source Voltage	$v_{ m DSS}$	-60	V	
Drain-Gate Voltage (RG	$v_{ m DGR}$	-60	V	
Gate-Source Voltage	v_{GSS}	±20	V	
Dunain Commond	DC	I_{D}	-1	A
Drain Current	Pulse	$I_{ m DP}$	-3	Α
Drain Power Dissipation	$P_{\mathbf{D}}$	0.5	w	
Drain Power Dissipation	P_{D}^{*}	1.5	w	
Channel Temperature	$\mathrm{T_{ch}}$	150	°C	
Storage Temperature Ra	$ m T_{stg}$	-55~150	°C	

 P_D^* : Mounted on ceramic substrate (600mm²×0.8t)

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel To Ambient	$R_{th(ch-a)}$	250	°C/W

THIS TRANSISTOR IS AN ELECTROSTATIC SENSITIVE DEVICE. PLEASE HANDLE WITH CAUTION.

INDUSTRIAL APPLICATIONS

Unit in mm 1.6MAX. 4 6MAX 0.4 ± 0.05 1.7MAX 0.45 - 0.05 +0.08 + 0.08 0.4 - 0.05 1.5 ± 0.1 1.5 ± 0.1 1. GATE 2. DRAIN 3. SOURCE JEDEC **EIAJ** SC-62 TOSHIBA 2-5K1B

Weight: 0.05g

Marking



TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARA	CTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		$I_{ m GSS}$	$V_{GS} = \pm 16V, V_{DS} = 0V$	_	_	±10	μ A
Drain Cut-off Current		$I_{ m DSS}$	$V_{DS} = -60V, V_{GS} = 0V$		_	100	μ A
Drain-Source Breakdown Voltage		V _{(BR)DSS}	$I_{D} = -10 \text{mA}, V_{GS} = 0 \text{V}$	-60	_	_	v
Gate Threshold Voltage		v_{th}	$V_{DS} = -10V, I_{D} = -1mA$	-0.8	_	-2.0	V
Drain-Source ON Resistance		R _{DS(ON)}	$V_{GS} = -4V, I_D = -0.5A$ $V_{GS} = -10V, I_D = -0.5A$	_	0.86	1.2 0.73	Ω
Forward Trai	nsfer	Y _{fs}	$V_{DS} = -10V, I_{D} = -0.5A$	0.5	0.9	_	s
Input Capacitance		C_{iss}		_	155	_	
Reverse Transfer Capacitance		C_{rss}	$egin{array}{l} V_{ m DS}\! =\! -10 { m V}, \ V_{ m GS}\! =\! 0 { m V} \ { m f}\! =\! 1 { m MHz} \end{array}$	_	20	_	pF
Output Capacitance		C_{oss}		_	75	_	
Switching Time	Rise Time	t _r	$V_{\text{GS}} = \frac{0\text{V}}{10\text{V}} \qquad I_{\text{D}} = -0.5\text{A} \\ V_{\text{Out}} = \frac{0\text{V}}{10\text{V}} \qquad \text{RL} = 60\Omega$ $V_{\text{DD}} = -30\text{V}$	_	17	_	
	Turn-on Time	t _{on}		_	20	_	
	Fall Time	tf		_	20	_	ns
	Turn-off Time	t _{off}	$egin{aligned} ext{VIN}: & t_{\mathbf{r}}, & t_{\mathbf{f}} < 5 ext{ns} \\ ext{Duty} & \leq 1\%, & t_{\mathbf{W}} = 10 \mu ext{s} \end{aligned}$	_	100	_	
Total Gate Charge (Gate- Source Plus Gate-Drain)		$\mathbf{Q}_{\mathbf{g}}$	$V_{DD} = -48V, V_{GS} = -10V$ $I_{D} = -1A$	_	6.5	_	~
Gate-Source Charge		$\mathbf{Q}_{\mathbf{g}\mathbf{s}}$			4.5	_	nC
Gate-Drain ("Miller") Charge		\mathbf{Q}_{gd}		_	2.0	_	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	$I_{ m DR}$	_	1	_	-1	A
Pulse Drain Reverse Current	$I_{ m DRP}$	_	1	_	-3	Α
Diode Forward Voltage	$v_{ m DSF}$	$I_{DR} = -1A$, $V_{GS} = 0V$	l		1.7	V
Reverse Recovery Time	t_{rr}	$I_{DR} = -1A$, $V_{GS} = 0V$	l	50	_	ns
Reverse Recovery Charge	Q_{rr}	$dI_{DR}/dt = 50A/\mu s$	_	50	_	μC

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