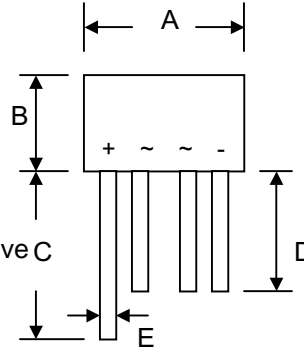


Features

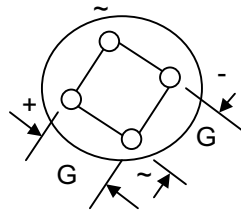
- Glass Passivated Die Construction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability
- Ideal for Printed Circuit Boards
- Case to Terminal Isolation Voltage 2500V
- UL Recognized File # E223064
- Green Products in Compliance with the RoHS Directive C



Dim	WOB			
	Min	Max	Min	Max
A	8.60	9.10	0.339	0.358
B	5.00	5.50	0.197	0.217
C	27.9	—	1.10	—
D	25.4	—	1.00	—
E	0.71	0.81	0.028	0.032
G	4.60	5.60	0.181	0.220
	In mm		In inch	

Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: As Marked on Body
- Weight: 1.1 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	W005G-G	W010G-G	W020G-G	W040G-G	W060G-G	W080G-G	W100G-G	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	50	100	200	400	600	800	1000	V
RMS Reverse Voltage	$V_{R(RMS)}$	35	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1) @ $T_A = 50^\circ\text{C}$	I_O	1.5							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	50							A
Forward Voltage (per element) @ $I_F = 1.5\text{A}$	V_{FM}	1.0							V
Peak Reverse Current @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage @ $T_A = 100^\circ\text{C}$	I_{RM}	5.0 500							μA
Operating Temperature Range	T_J	-55 to +150							$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 to +150							$^\circ\text{C}$

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case.

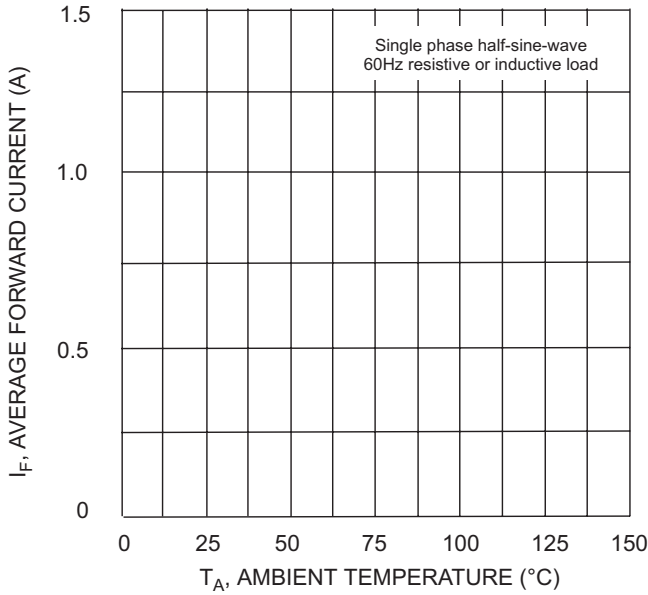


Fig. 1 Forward Current Derating Curve

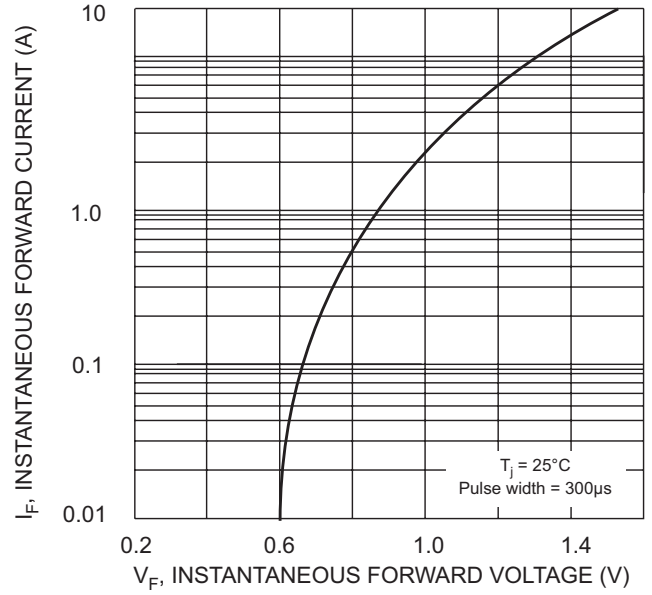


Fig. 2 Typical Forward Characteristics, per element

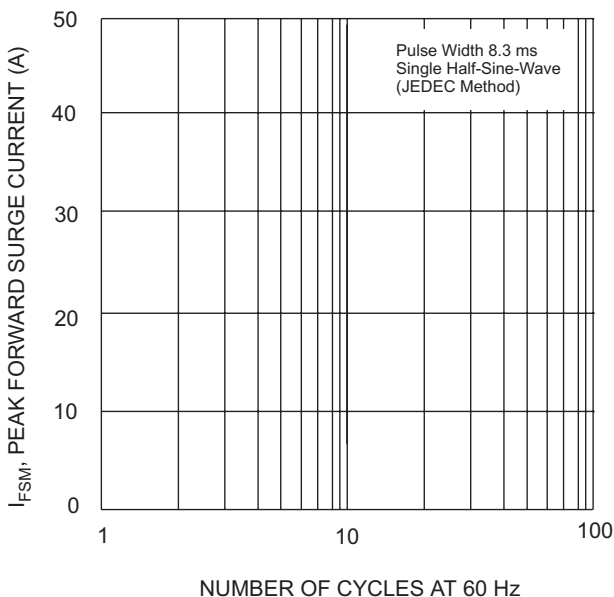


Fig. 3 Max Non-Repetitive Surge Current

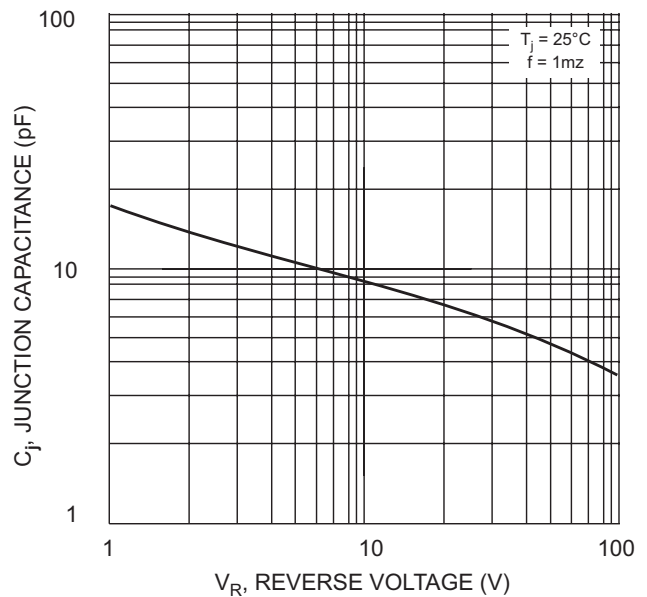


Fig. 4 Typical Junction Capacitance

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