



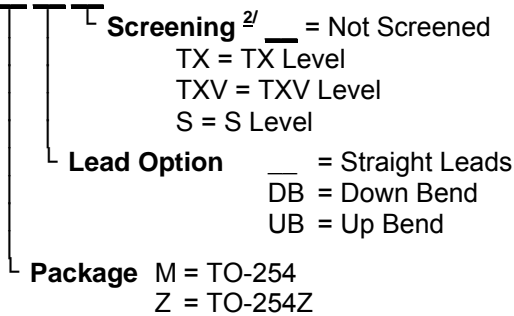
# Solid State Devices, Inc.

14701 Firestone Blvd \* La Mirada, Ca 90638  
 Phone: (562) 404-4474 \* Fax: (562) 404-1773  
 ssdi@ssdi-power.com \* www.ssdi-power.com

## DESIGNER'S DATA SHEET

### Part Number / Ordering Information <sup>1/</sup>

**SFF75N10**



**SFF75N10M**  
**SFF75N10Z**

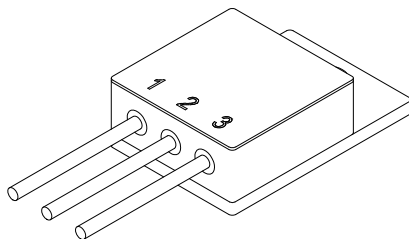
**75 AMP**  
**N-CHANNEL**  
**POWER MOSFET**  
**100 Volts**  
**0.018 Ω**

### Features:

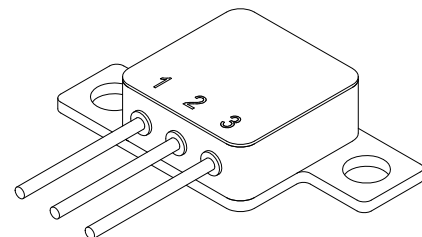
- Rugged Construction with Polysilicon Gate
- Low  $R_{DS(ON)}$  and High Transconductance
- Excellent High Temperature Stability
- Very Fast Switching Speed
- Fast Recovery and Superior  $dV/dt$  Performance
- Increased Reverse Energy Capability
- Low Input and Transfer Capacitance for Easy Paralleling
- Ceramic Seals for Improved Hermeticity
- Hermetically Sealed Package
- TX, TXV, Space Level Screening Available

Maximum Ratings <sup>3/</sup>		Symbol	Value	Units
Drain – Source Voltage		$V_{DS}$	100	Volts
Gate – Source Voltage	Continuous transient	$V_{GS}$	$\pm 20$ $\pm 30$	Volts
Collector Current	Continuous peak	$I_D$	56 <sup>4/</sup> 140	Amps
Operating & Storage Temperature		Top & Tstg	-55 to +175	°C
Maximum Thermal Resistance Junction to Case		$R_{\theta JC}$	0.83	°C/W
Total Device Dissipation	$T_C = 25^\circ C$	$P_D$	180	W
	$T_C = 55^\circ C$		144	
Avalanche Energy	repetitive Single pulse	$E_{AR}$	80	mJ
		$E_{AS}$	2500	

TO-254 (M)



TO-254Z (Z)



<sup>1/</sup> For Ordering Information, Price, and Availability- Contact Factory.

<sup>2/</sup> Screening Based on MIL-PRF-19500. Screening Flows Available on Request.

<sup>3/</sup> Unless Otherwise Specified, All Electrical Characteristics @25°C

<sup>4/</sup> Maximum Current Limited by Package, Die Rated at 140A.

**NOTE:** All specifications are subject to change without notification.  
 SCD's for these devices should be reviewed by SSDI prior to release.

**DATA SHEET #: F00153J**

**DOC**

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**SFF75N10M**  
**SFF75N10Z**

Electrical Characteristics @ T <sub>J</sub> = 25°C (Unless Otherwise Specified)		Symbol	Min	Typ	Max	Units
Drain to Source Breakdown Voltage V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA		BV <sub>DSS</sub>	100	—	—	V
Drain to Source On State Resistance V <sub>GS</sub> = 10 V	I <sub>D</sub> = 37.5 A I <sub>D</sub> = 75 A	R <sub>DS(on)</sub>	— —	0.013 0.014	0.018 0.020	Ω
Gate Threshold Voltage V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 1 mA		V <sub>GS(th)</sub>	3.0	4.3	5.0	V
Forward Transconductance V <sub>DS</sub> > I <sub>D(on)</sub> X R <sub>DS(on)</sub> Max, I <sub>DS</sub> = 37.5 A		g <sub>fs</sub>	15	25	—	Smho
Zero Gate Voltage Drain Current V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>A</sub> = 125°C		I <sub>DSS</sub>	— —	0.005 5	25 250	μA
Gate to Source Leakage Forward Gate to Source Leakage Reverse	V <sub>DS</sub> = 20 V	I <sub>GSS</sub>	— —	5 5	+100 -100	nA
Total Gate Charge Gate to Source Charge Gate to Drain Charge	V <sub>GS</sub> = 12 V V <sub>DS</sub> = 50 V I <sub>D</sub> = 75 A	Q <sub>g</sub> Q <sub>gs</sub> Q <sub>gd</sub>	— — —	185 50 85	250 75 135	nC
Turn on Delay Time Rise Time Turn off Delay Time Fall Time	V <sub>DD</sub> = 50 V I <sub>D</sub> = 37.5 A R <sub>G</sub> = 2.35 Ω V <sub>GS</sub> = 10 V	t <sub>d(on)</sub> t <sub>r</sub> t <sub>d(off)</sub> t <sub>f</sub>	— — — —	35 40 60 45	75 80 120 85	nsec
Diode Forward Voltage I <sub>S</sub> = 75A, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 25°C		V <sub>SD</sub>	—	1.1	1.5	V
Diode Reverse Recovery Time Reverse Recovery Charge	T <sub>J</sub> = 25°C I <sub>F</sub> = 10 A di/dt = 100 A/μsec	t <sub>rr</sub> Q <sub>RM</sub>	— —	160 0.45	250 2.5	nsec μC
Input Capacitance Output Capacitance Reverse Transfer Capacitance	V <sub>GS</sub> = 0 V V <sub>DS</sub> = 25 V f = 1 MHz	C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub>	— — —	4800 2000 550	— — —	pF

For thermal derating curves and other characteristics please contact SSDI Marketing Department.

**Available Part Numbers:**

**SFF75N10M; SFF75N10MDB; SFF75N10MUB;  
 SFF75N10Z; SFF75N10ZDB; SFF75N10ZUB**

**PIN ASSIGNMENT (Standard)**

Package	Drain	Source	Gate
TO-254 (M)	Pin 1	Pin 2	Pin 3
TO-254Z (Z)	Pin 1	Pin 2	Pin 3

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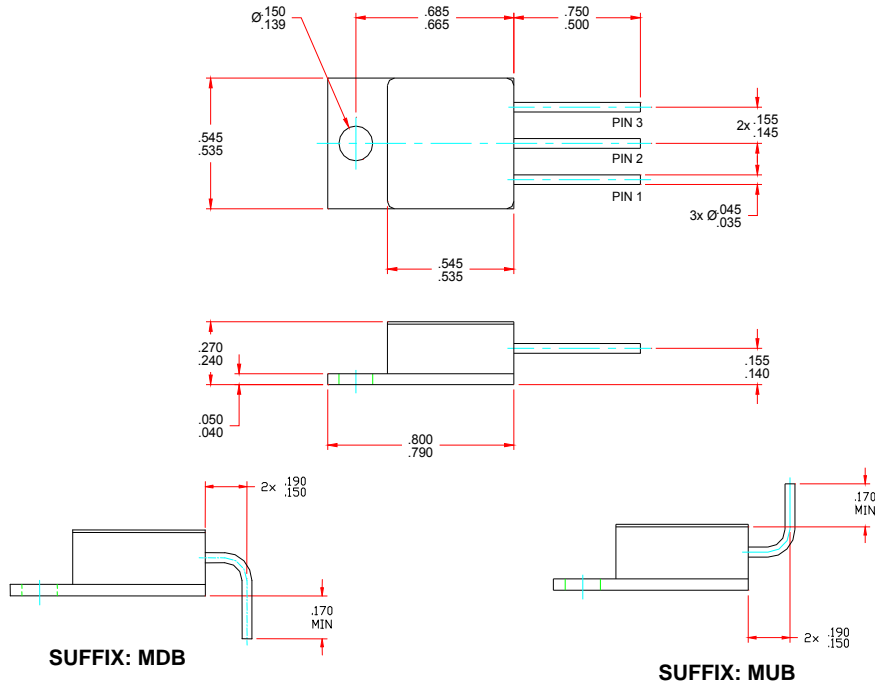


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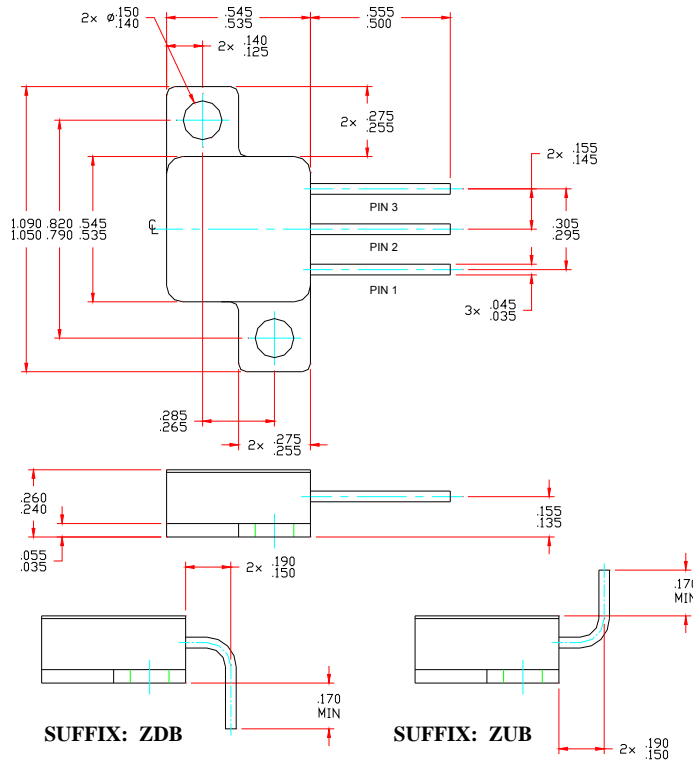
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**SFF75N10M**  
**SFF75N10Z**

**Case Outline: TO-254 (M)**



**Case Outline: TO-254Z (Z)**



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