

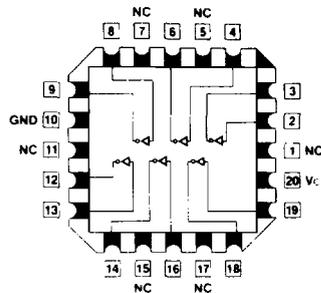
54AC/74AC04 • 54ACT/74ACT04

Hex Inverter

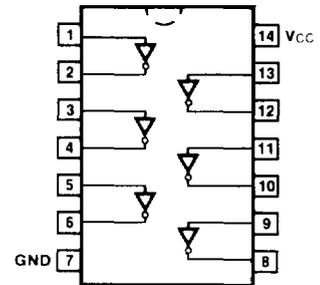
Connection Diagrams

- Outputs Source/Sink 24 mA
- 'ACT04 has TTL-Compatible Inputs

Ordering Code: See Section 6



Pin Assignment for LCC



Pin Assignment for DIP, Flatpak and SOIC

DC Characteristics (unless otherwise specified)

Symbol	Parameter	54AC/ACT	74AC/ACT	Units	Conditions
I_{CC}	Maximum Quiescent Supply Current	80	40	μA	$V_{IN} = V_{CC}$ or Ground, $V_{CC} = 5.5 V$, $T_A = \text{Worst Case}$
I_{CC}	Maximum Quiescent Supply Current	4.0	4.0	μA	$V_{IN} = V_{CC}$ or Ground, $V_{CC} = 5.5 V$, $T_A = 25^\circ C$
$I_{CC(T)}$	Maximum Additional I_{CC}/Input ('ACT04)	1.6	1.5	mA	$V_{IN} = V_{CC} - 2.1 V$, $V_{CC} = 5.5 V$, $T_A = \text{Worst Case}$

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AC Characteristics

Symbol	Parameter	V_{CC}^* (V)	74AC			54AC		74AC		Units	Fig. No.
			$T_A = +25^\circ C$ $C_L = 50 pF$			$T_A = -55^\circ C$ to $+125^\circ C$ $C_L = 50 pF$		$T_A = -40^\circ C$ to $+85^\circ C$ $C_L = 50 pF$			
			Min	Typ	Max	Min	Max	Min	Max		
t_{PLH}	Propagation Delay	3.3 5.0	1.0 1.0	4.5 4.0	9.0 7.0	1.0 1.0	11.0 8.5	1.0 1.0	10.0 7.5	ns	3-5
t_{PHL}	Propagation Delay	3.3 5.0	1.0 1.0	4.5 3.5	8.5 6.5	1.0 1.0	10.0 7.5	1.0 1.0	9.5 7.0	ns	3-5

*Voltage Range 3.3 is $3.3 V \pm 0.3 V$
Voltage Range 5.0 is $5.0 V \pm 0.5 V$

Military parameters given herein are for general references only. For current military specifications and subgroup testing information please request Fairchild's Table I data sheet from your Fairchild sales engineer or account representative.

AC Characteristics

Symbol	Parameter	Vcc* (V)	74ACT			54ACT		74ACT		Units	Fig. No.
			TA = +25°C CI = 50 pF			TA = -55°C to +125°C CL = 50 pF		TA = -40°C to +85°C CL = 50 pF			
			Min	Typ	Max	Min	Max	Min	Max		
tPLH	Propagation Delay	5.0	4.5							ns	3-6
tPHL	Propagation Delay	5.0	3.9							ns	3-6

*Voltage Range 5.0 is 5.0 V ± 0.5 V

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Capacitance

Symbol	Parameter	54/74AC/ACT	Units	Conditions
		Typ		
CIN	Input Capacitance	4.5	pF	Vcc = 5.5 V
CPD	Power Dissipation Capacitance	30.0	pF	Vcc = 5.5 V