TOSHIBA Transistor Silicon NPN Epitaxial Planar Type

# MT6P04AT

### VHF~UHF Band Low Noise Amplifier Applications

• Two devices are built in to the super-thin and ultra super mini (6 pins) package: TU6

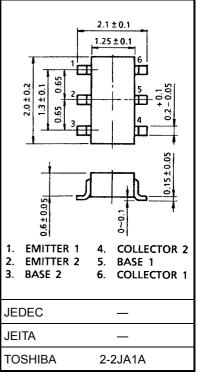
#### **Mounted Devices**

	Q1/Q2: SSM (TESM)
Three-pins (SSM/TESM) mold products are corresponded.	MT3S04AS (MT3S04AT)

## **Maximum Ratings (Ta = 25°C)**

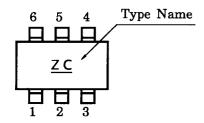
Characteristics	Symbol	Q1/Q2	Unit	
Collector-base voltage	V <sub>CBO</sub>	10	V	
Collector-emitter voltage	V <sub>CEO</sub>	5	V	
Emitter-base voltage	V <sub>EBO</sub>	2	V	
Collector current	I <sub>C</sub>	40	mA	
Base current	Ι <sub>Β</sub>	10	mA	
Collector power dissipation	PC	200	mW	
Junction temperature	Tj	125	°C	
Storage temperature range	T <sub>stg</sub>	-55~125	°C	

## Unit: mm

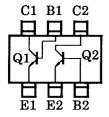


Weight: 0.0045 g (typ.)

### Marking



### Pin Assignment (top view)



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## **Electrical Characteristics Q1/Q2 (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 5 \text{ V}, I_{E} = 0$	_	_	0.1	μА	
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 1 V, I <sub>C</sub> = 0	_	_	1	μА	
DC current gain	h <sub>FE</sub>	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 5 mA	80	_	160		
Transition frequency	f <sub>T</sub> (1)	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}$	2	4.5	_	- GHz	
	f <sub>T</sub> (2)	$V_{CE} = 3 \text{ V}, I_{C} = 7 \text{ mA}$	5	7	_		
Insertion gain	S <sub>21e</sub>   <sup>2</sup> (1)	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}, f = 1 \text{ GHz}$	_	8.5	_	dB	
	S <sub>21e</sub>   <sup>2</sup> (2)	$V_{CE} = 3 \text{ V}, I_{C} = 20 \text{ mA}, f = 1 \text{ GHz}$	7.5	11	_	ub	
Noise figure —	NF (1)	$V_{CE} = 1 \text{ V}, I_C = 5 \text{ mA}, f = 1 \text{ GHz}$	_	1.3	2.2	dB	
	NF (2)	$V_{CE} = 3 \text{ V}, I_{C} = 7 \text{ mA}, f = 1 \text{ GHz}$	_	1.2	2	ub	
Reverse transfer capacitance	C <sub>re</sub>	$V_{CB} = 1 \text{ V}, I_E = 0, f = 1 \text{ MHz}$ (Note)	_	0.9	1.25	pF	

Note:  $C_{\text{re}}$  is measured by 3 terminal method with capacitance bridge.

## **Handling Precaution**

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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