

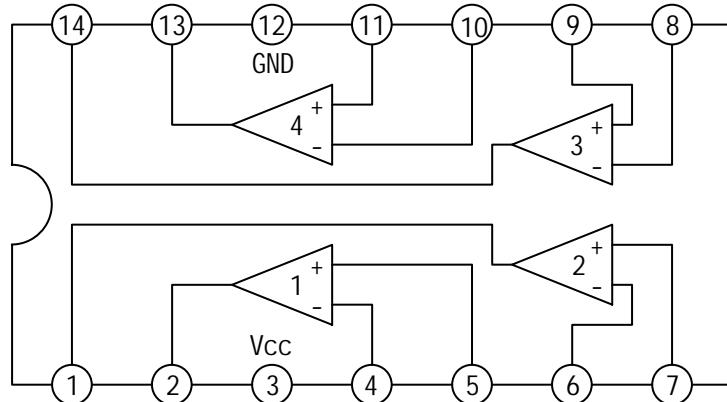
QUAD DIFFERENTIAL COMPARATOR—YD339

DESCRIPTION AND FEATURES

The YD339 consists of four independent voltage comparators designed specifically to operate from a single power supply over a wide voltage range ;

- *Single or dual supply operation ;
- *Wide operating supply range ($V_{CC}=2V \sim 36V$ or $\pm 1V \sim \pm 18V$) ;
- *Input common-mode voltage includes ground ;
- *Low supply current drain: $I_{CC}=0.8mA$ (Typical) ;
- *Open collector output for wired and connection ;
- *Low input bias current $I_{bias}=25nA$ (Typical) ;
- *Low output saturation voltage ;
- *Output compatible with TTL, DTL, and CMOS logic system.

BLOCK DIAGRAM



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ABSOLUTE MAXIMUM RATINGS (Tamb=25 °C)

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V _{CC}	±18 or 36	V
Differential Input Voltage	V _{ID}	±36	V
Input Voltage	V _I	-0.3 ~ 36V	V
Power Dissipation	P _d	625	mW
Operating Temperature	T _{OPR}	-40 ~ +85	
Storage Temperature	T _{STG}	-55 ~ +125	

ELECTRICAL CHARACTERISTICS(V_{CC}=5.0V, Tamb=25 °C, All voltage referenced to GND unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage	V _{IO}	V _{CM} =0 to V _{CC} -1.5 V _{O(P)} =1.4V, R _S =0		±1.5	±5.0	mV
Input Offset Current	I _{IO}			±2.3	±50	nA
Input Bias Current	I _B			57	250	nA
Input Common-mode Voltage	V _{I(R)}		0		V _{CC} -1.5	V
Supply Current	I _{CC}	R _L =		1.1	2.0	mA
Large Signal Voltage Gain	G _V	V _{CC} =15V, R _L > 15k		200		V/mV
Large Signal Response Time	t _{RES}	V _i =TTL logic swing V _{ref} =1.4V, V _{RL} =5V, R _L =5.1k		350		ns
Response Time	t _{RES}	V _{RL} =5V, R _L =5.1k		1400		ns
Output Sink Current	I _{SINK}	V _{i(-)} > 1V, V _{i(+)} =0V, V _{o(p)} < 1.5V	6	18		mA

OUTLINE DRAWING

DIP-14

unit:mm

