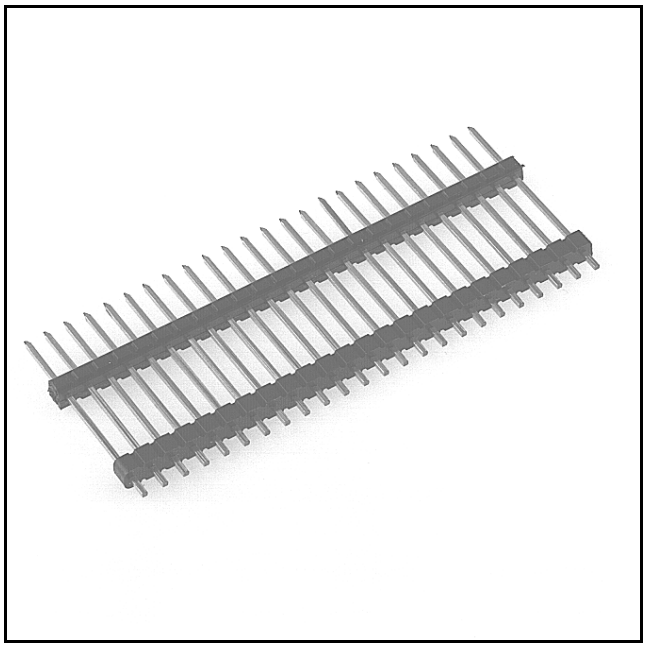
3M™ Pin Strip Header

.100″ and .100″ × .100″ Dual Insulator, Board Stacking 929 Series



- 36 sizes (1 to 36) position
- Standard .025 inch square posts
- .100 inch spacing between posts
- Dual insulation
- Board pin retention feature available
- See Regulatory Information Appendix (RIA) for chemical compliance information.

Date Modified: March 8, 2007

Physical

Insulation	
Material:	Glass Filled Polyester (PBT) or PCT
Flammability:	UL 94V-0
Color:	Black
Contact	
Material:	Copper Alloy
Plating	
Underplating:	50 µ″ [1.27 µm] Nickel
Wiping Area:	10 µ″ [0.25 µm] Gold
Marking:	None

Electrical

Current Rating:	2.5 A
Insulation Resistance:	$> 5 \times 10^9 \Omega$ at 500 V_{DC}
Withstanding Voltage:	1500 V_{RMS} at Sea Level

Environmental

Temperature Rating:	-40°C to +105°C
Process Rating:	260°C (per J-STD-020C) for PCT insulator parts (PBT insulator version), maximum insulator temperature 191°C (Wave Solder Process Only)
Moisture Sensitivity Level:	1 (per J-STD-020C) for PCT insulator parts

UL File No.: E68080

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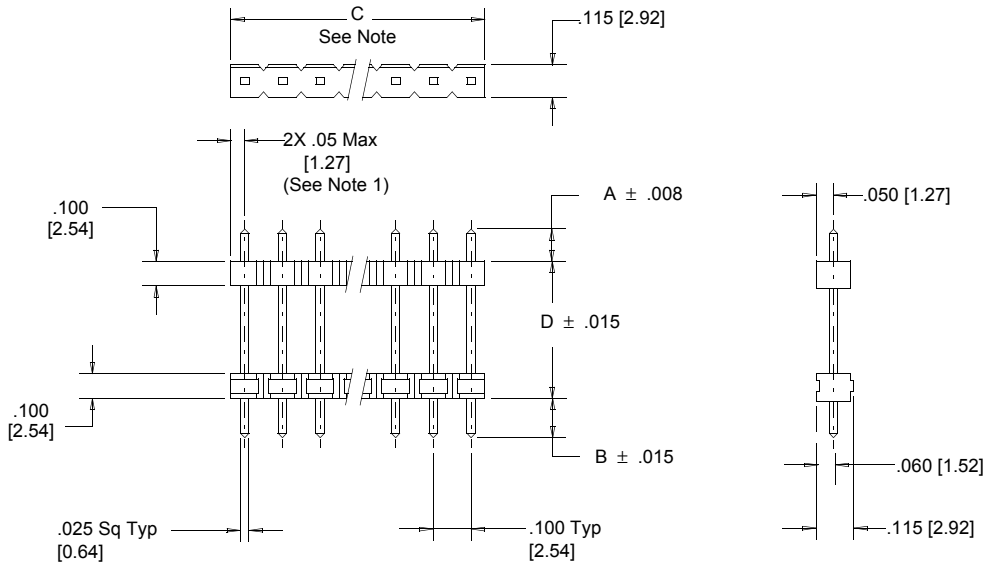
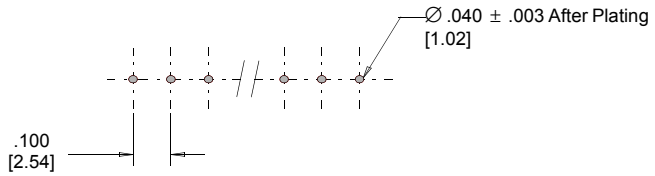
Mating Length Code	Tail Length Code	Dimensions		
		A	B	D
4217	24	.238 [6.05]	.110 [2.79]	.380 [9.65]
4217	28	.235 [5.97]	.110 [2.79]	.500 [12.70]
4217	32	.240 [6.10]	.110 [2.79]	.630 [16.00]
6121	01	.100 [2.54]	.355 [9.02]	.390 [9.91]
6581	01	.100 [2.54]	.465 [11.81]	.400 [10.16]
6781	01	.100 [2.54]	.545 [13.84]	.600 [15.24]
6101	01	.100 [2.54]	.350 [8.89]	.900 [22.86]

One Row





Recommended Hole Pattern

Inch
[mm]

Tolerance Unless Noted			
	.0	.00	.000
Inch	± .1	± .01	± .005

[ ] Dimensions for Reference only

Note:

C = .10" [2.5] × pin quantity per row. Dimension C is a maximum when the pin strip is cut to length, not when the strip is broken to length. Breaking strips to length not recommended.

Ordering Information

9XXXXX-XX-XX-XX

- Board Retention Feature
  - 2 = None
  - R = Kinked Tails
- Mating Length Code (See Table)
- Tail Length Code (See Table)
- Pin Quantity (01-36)
- Plating Suffix
  - I = 10μ″ [0.25μm] all over, PBT insulator (RIA E1 & C1 apply)
  - EU = 10μ″ [0.25um] all over, PCT insulator (RIA E1 & C1 apply)

3M
Interconnect Solutions
http://www.3M.com/interconnects/

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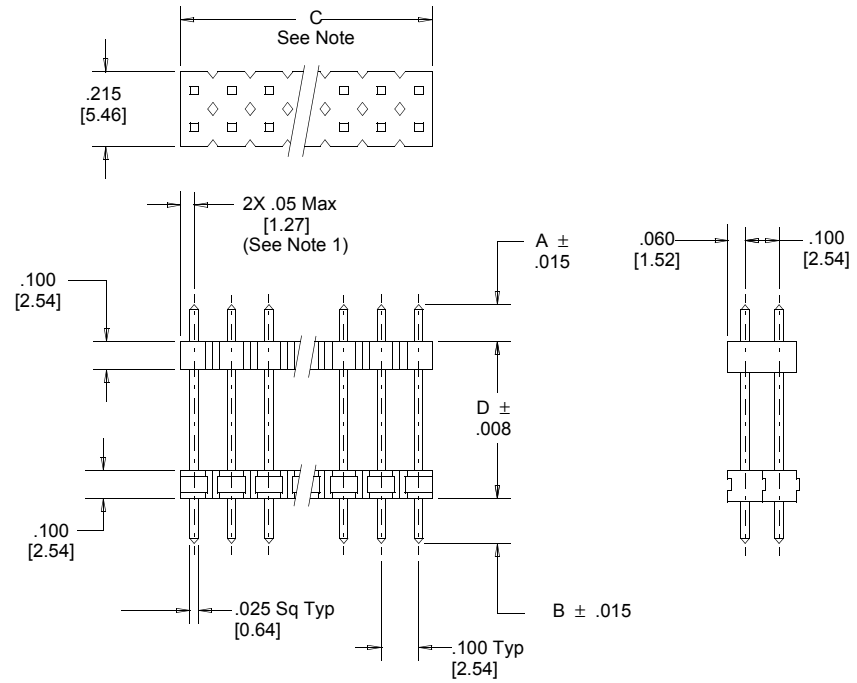
800-225-5373

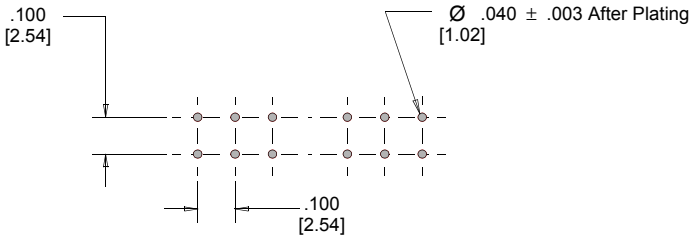
3M™ Pin Strip Header

.100″ and .100″ × .100″ Dual Insulator, Board Stacking 929 Series

Mating Length Code	Tail Length Code	Dimensions		
		A	B	D
4227	24	.238 [6.05]	.110 [2.79]	.380 [9.65]
4227	28	.235 [5.97]	.110 [2.79]	.500 [12.70]
4227	32	.240 [6.10]	.110 [2.79]	.630 [16.00]
6122	01	.100 [2.54]	.355 [9.02]	.390 [9.91]
6582	01	.100 [2.54]	.465 [11.81]	.400 [10.16]
6782	01	.100 [2.54]	.545 [13.84]	.600 [15.24]
6102	01	.100 [2.54]	.350 [8.89]	.900 [22.86]

Two Row





Recommended Hole Pattern

Inch
[mm]

Tolerance Unless Noted			
	.0	.00	.000
Inch	± .1	± .01	± .005

[ ] Dimensions for Reference only

Note:
C = .10" [2.5] × pin quantity per row. Dimension C is a maximum when the pin strip is cut to length Breaking dual row strips to length is not recommended.

Ordering Information

9XXXXX-XX-XX-XX

- Board Retention Feature: 2 = None; R = Kinked Tails
- Mating Length Code (See Table)
- Tail Length Code (See Table)
- Pin Code (Pins per Row)
- Plating Suffix: I = 10μ″ [0.25μm] all over, PBT insulator (RIA E1 & C1 apply); EU = 10μ″ [0.25um] all over, PCT insulator (RIA E1 & C1 apply)

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