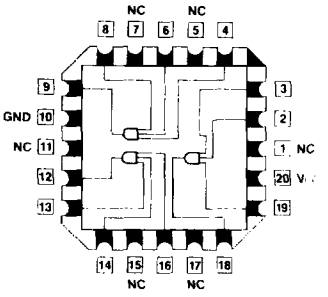
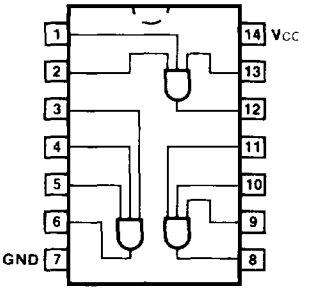
54F/74F11

Triple 3-Input AND Gate

Connection Diagrams





Ordering Code: See Section 5

Pin Assignment for LCC and PCC

Pin Assignment for DIP and SOIC

Input Loading/Fan-Out: See Section 3 for U.L. definitions

Pin Names	Description	54F/74F(U.L.) HIGH/LOW
	Inputs	0.5/0.375
	Outputs	25/12.5

DC Characteristics over Operating Temperature Range (unless otherwise specified)

Symbol	Parameter	54F/74F Min	54F/74F Typ	54F/74F Max	Units	Conditions	
I_{CCH}	Power Supply Current		4.1	6.2	mA	V_{IN} = Open	V_{CC} = Max
I_{CCL}			6.5	9.7		V_{IN} = Gnd	

AC Characteristics: See Section 3 for waveforms and load configurations

Symbol	Parameter	54F/74F T_A = +25°C V_{CC} = +5.0 V C_L = 50 pF			54F T_A, V_{CC} = Mil C_L = 50 pF		74F T_A, V_{CC} = Com C_L = 50 pF		Units	Fig. No.
		Min	Typ	Max	Min	Max	Min	Max		
t_{PLH}	Propagation Delay	3.0	4.2	5.6	2.5	7.5	3.0	6.6	ns	3-1
t_{PHL}		2.5	4.1	5.5	2.0	7.5	2.5	6.5		3-4