TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSV)

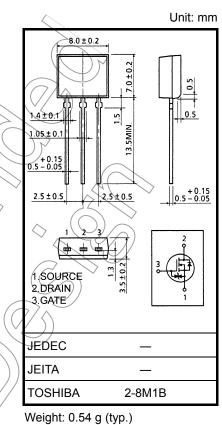
# 2SK3302

Switching Regulator and DC-DC Converter Applications

- Low drain-source ON resistance: R<sub>DS (ON)</sub> = 11.5 Ω (typ.)
- High forward transfer admittance: |Y<sub>fs</sub>| = 0.4 S (typ.)
- Low leakage current:  $I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement model:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	500	$(\mathcal{N} \land)$	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		V <sub>DGR</sub>	500	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC	(Note 1)	۱ <sub>D</sub>	0,5	A
	Pulse	(Note 1)	I <sub>DP</sub>	1.5	$\sim$ $\wedge$
Drain power dissipation			PD	1.3	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	14.3	mJ	
Avalanche current		IAR	0.5	A	
Repetitive avalanche energy (Note 3)		EAR	)) 0.13	mJ	
Channel temperature			Tch	150 <	°C
Storage temperature range		T <sub>stg</sub>	-55 to 150	)°C	



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

Characteristics	ibol Max	Unit
Thermal resistance, channel to ambient	ch-a) 96.1	°C/W

Note 4: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}, \text{ L} = 100 \text{ mH}, \text{ R}_{G} = 25 \Omega, \text{ I}_{AR} = 0.5 \text{ A}$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device. Please handle with caution.

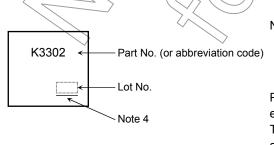
**Electrical Characteristics (Ta = 25°C)** 

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 25$ V, $V_{DS} = 0$ V	_	—	±10	μA
Gate-source breakdown voltage		V (BR) GSS	$I_G=\pm 10~\mu A,~V_{GS}=0~V$	±30	_	_	V
Drain cut-OFF current		I <sub>DSS</sub>	$V_{DS} = 500 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		_	100	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	500		_	V
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	2.0	)/-	4.0	V
Drain-source ON resistance		R <sub>DS (ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 0.25 \text{ A}$	$\sum$	10	18	Ω
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.25 \text{ A}$	0.2	0.4	_	S
Input capacitance		C <sub>iss</sub>		_	75	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz$		7	_	pF
Output capacitance		C <sub>oss</sub>			24		
Switching time	Rise time	tr	$V_{GS}^{10 V}$	— (		$\langle \rangle$	• ns
	Turn-on time	t <sub>on</sub>			18	) —	
	Fall time	t <sub>f</sub>	G ↓ 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		54		
	Turn-off time	t <sub>off</sub>	$v_{DD} \approx 2.50 v$ $p_{Uty} \leq 1\%, t_W = 10 \ \mu s$	) -	95		
Total gate charge (gate-source plus		Qg <		_	3.8		
Gate-source charge		Qgs	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 0.5 \text{ A}$		1.9		nC
Gate-drain ("miller") charge		Qgd			1.9		

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	$(\vee)$ –	_	_	0.5	А
Pulse drain reverse current (Note 1)	DRP		_	_	1.5	А
Forward voltage (diode)	VDSF	I <sub>DR</sub> = 0.5 A, V <sub>GS</sub> = 0 V	—	_	-1.5	V
Reverse recovery time	t <sub>rr</sub>	1 <sub>DR</sub> = 0.5 A, V <sub>GS</sub> = 0 V,	—	190	_	ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> /dt = 100 A/μs	_	380		nC

## Marking

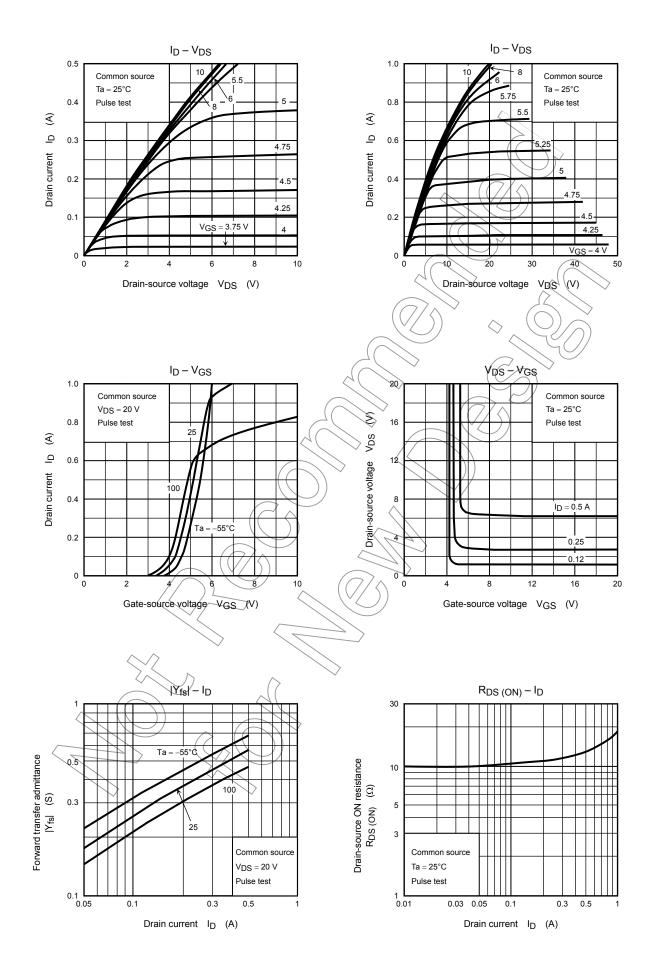


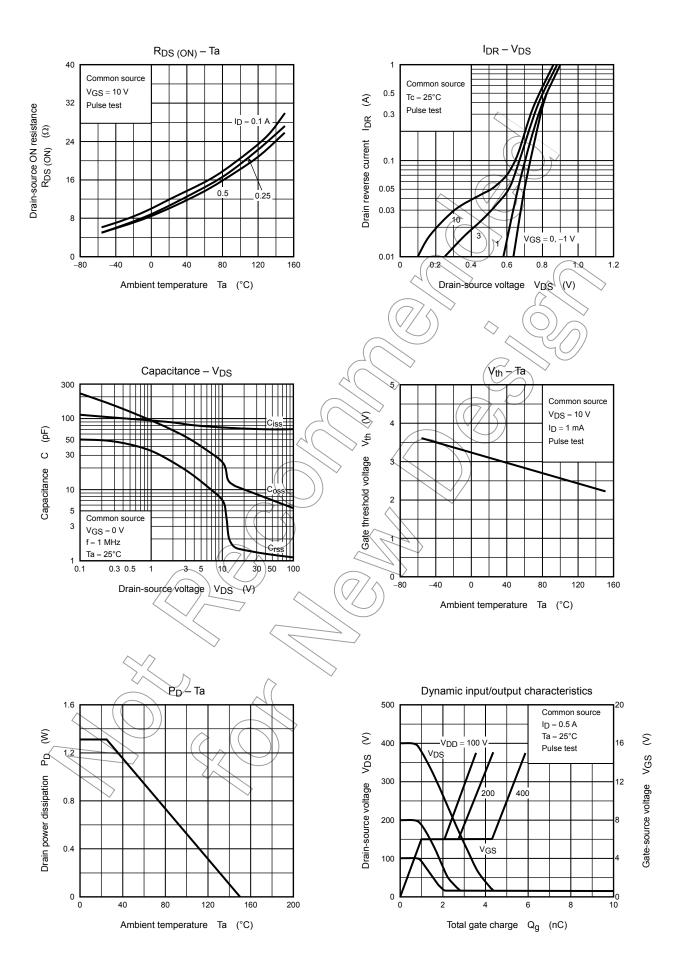
Note 4: A line under a Lot No. identifies the indication of product Labels.

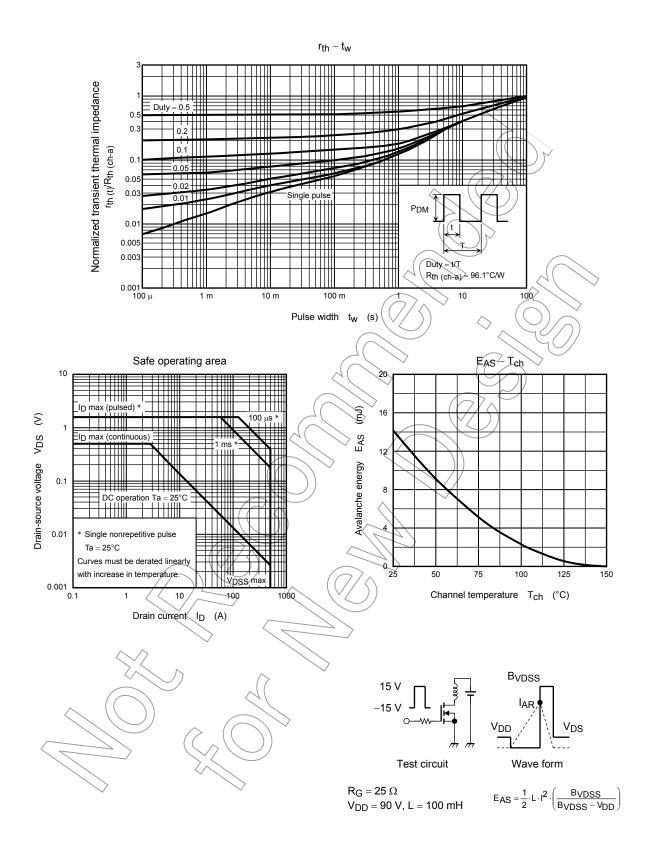
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

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