# DATA SHEET

# MOS FIELD EFFECT TRANSISTOR **2SJ326,326-Z**

## SWITCHING P-CHANNEL POWER MOS FET

#### DESCRIPTION

NEC

The 2SJ326 is P-channel MOS Field Effect Transistor designed for solenoid, motor and lamp driver.

#### FEATURES

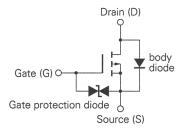
- Low On-state Resistance
  - $R_{\text{DS(on)}}$  = 0.28  $\Omega$  TYP. (Vgs = -10 V, Ip = -1.0 A)
  - $R_{DS(on)}$  = 0.50  $\Omega$  TYP. (VGs = -4 V, ID = -0.8 A)
- Low Ciss: Ciss = 320 pF TYP.
- Built-in G-S Gate Protection Diode

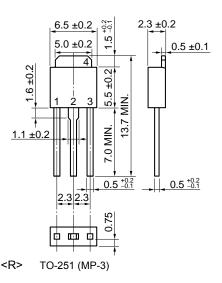
#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

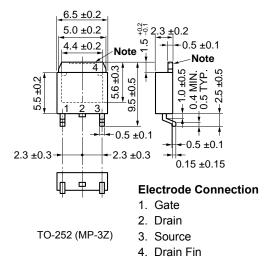
Drain to Source Voltage	VDSS	-60	V
Gate to Source Voltage (AC)	VGSS(AC)	∓20	V
Gate to Source Voltage (DC)	$V_{\text{GSS}(\text{DC})}$	-20, +10	V
Drain Current (DC)	D(DC)	∓2.0	А
Drain Current (pulse) Note	D(pulse)	∓8.0	Α
Total Power Dissipation (Tc = 25°C)	P <sub>T1</sub>	20	W
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T2</sub>	1.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

**Note** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

### EQUIVALENT CIRCUIT







**Note** The depth of notch at the top of the fin is from 0 to 0.2 mm.

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The mark <R> shows major revised points.

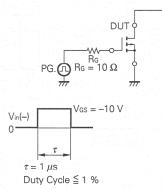
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

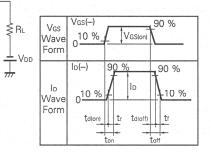
## PACKAGE DRAWINGS (Unit: mm)

### ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

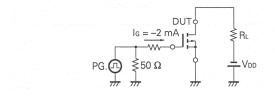
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS		
Drain to Source On-state Resistance	RDS(on)		0.28	0.37	Ω	Vgs = -10 V, Id = -1.0 A		
Drain to Source On-state Resistance	RDS(on)	19. LO T	0.50	0.68	Ω	Vgs = -4 V, ID = -0.8 A		
Gate to Source Cutoff Voltage	V <sub>GS(off)</sub>	-1.0	-1.5	-2.0	V	Vps = -10 V, lp = -1 mA		
Forward Transfer Admittance	yfs	1.0	1.8		S	VDS = -10 V, ID = -1.0 A		
Drain Leakage Current	loss			-10	μA	$V_{DS} = -60 V, V_{GS} = 0$		
Gate to Source Leakage Current	lgss			∓10	μA	Vgs = ∓16 V, Vds = 0		
Input Capacitance	Ciss		320		pF	$V_{DS} = -10 V$		
Output Capacitance	Coss		220		pF	V <sub>GS</sub> = 0 f = 1 MHz		
Reverse Transfer Capacitance	Crss	an an fi	75		pF			
Turn-On Delay Time	td(on)		5		ns	$V_{GS(on)} = -10 V$ $V_{DD} = -30 V$ $I_{D} = -1.0 A, R_{G} = 10 \Omega$ $R_{L} = 30 \Omega$		
Rise Time	tr		15		ns			
Turn-Off Delay Time	td(off)		40		ns			
Fall Time	tr		25		ns			
Total Gate Charge	QG		12	in the second	nC	V_{GS} = -10 V I_D = -2.0 A		
Gate to Source Charge	Qgs		1		nC			
Gate to Drain Charge	Qgd		5		nC	$V_{DD} = -48 \text{ V}$		
Body Diode Forward Voltage	VF		0.9		V	IF = 2.0 A, VGS = 0		
ESD	Vesd		±130		V	C = 200 pF, R = 0, Single Puls		
Reverse Recovery Time	trr		72		ns	IF = 2.0 A, VGS = 0 di/dt = 50 A/μs		
Reverse Recovery Charge	Qrr		30		nC			

#### **Test Circuit 1: Switching Time**

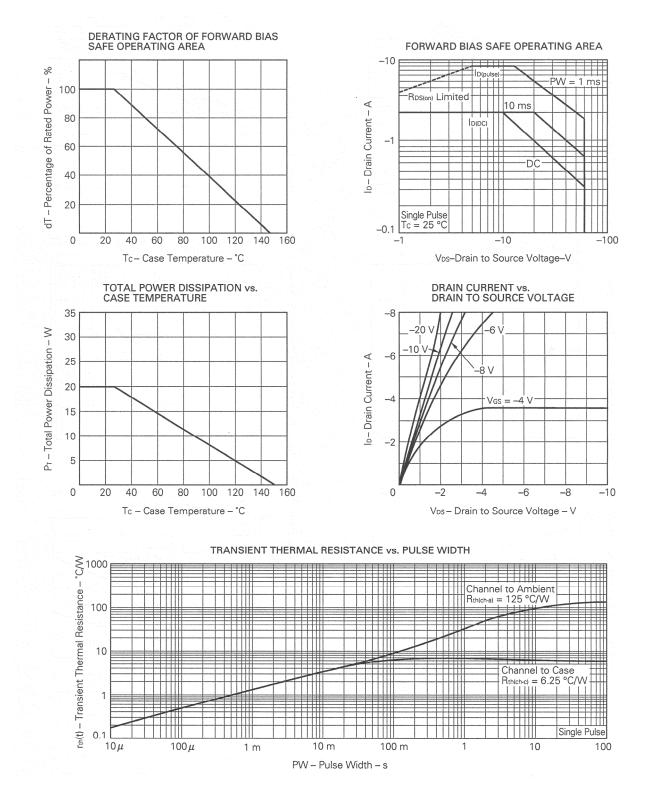




#### **Test Circuit 2: Gate Charge**

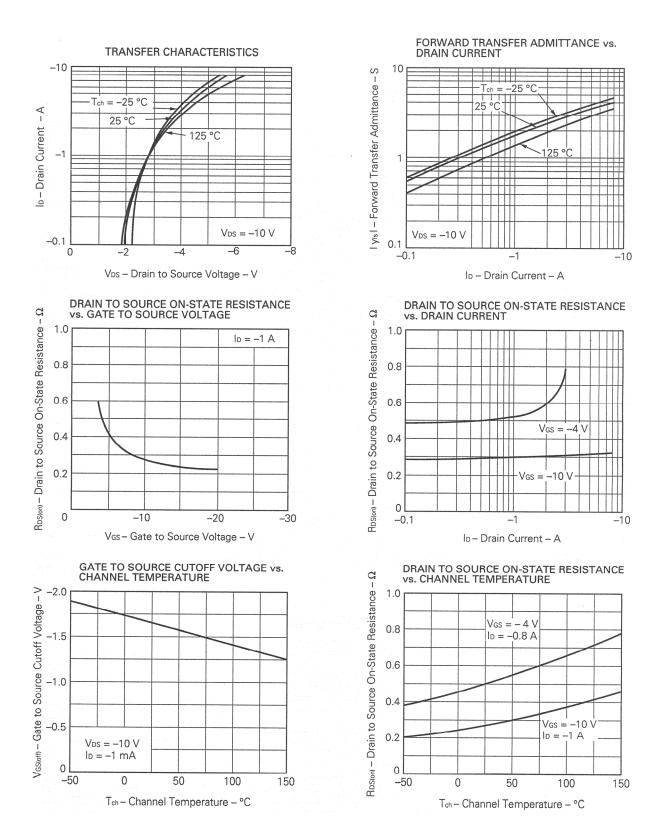


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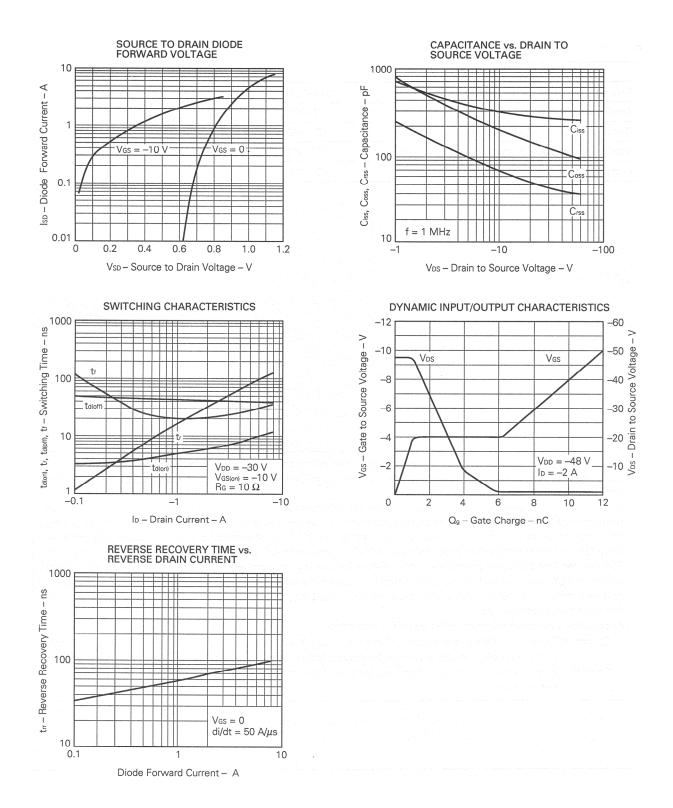


#### ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

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