

HD74LV1G08A

2-input AND Gate

R04DS0020EJ0900 Rev.9.00 Jan 10, 2014

Description

The HD74LV1G08A has two-input AND gate in a 5 pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV08A

Supply voltage range: 1.65 to 5.5 V

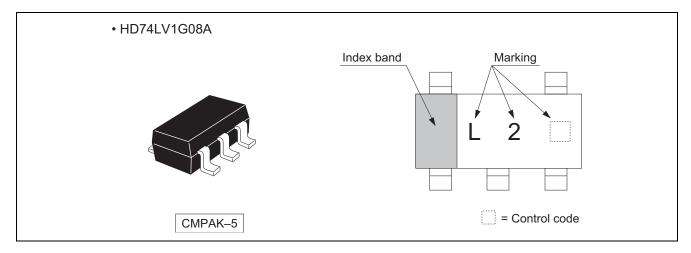
Operating temperature range : -40 to +85°C

- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V) All outputs V_{O} (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

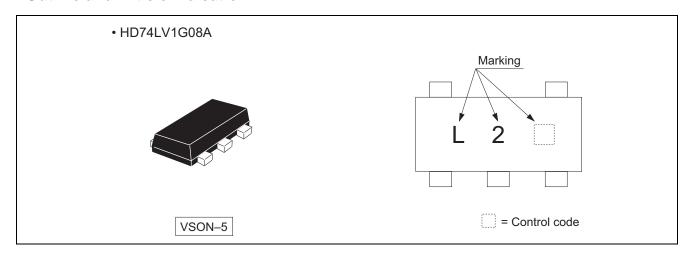
Part Name	Pookogo Typo	Package Code	Package	Taping Abbreviation	
Part Name	Package Type	(Previous Code)	Abbreviation	(Quantity)	
HD74LV1G08ACME	CMDAK 5 nin	PTSP0005ZC-A	CM	E (3000 pcs/reel)	
HD74LV IGU6ACIVIE	CMPAK-5 pin	(CMPAK-5V)	СМ		
HD74LV1G08AVSE	VSON-5 pin	PUSN0005KA-A	VS	E (3000 pcs/reel)	
HD74LV IGU6AVSE	V30N-5 pili	(TNP-5DV)	VS		

Note: Please consult the sales office for the above package availability.

Outline and Article Indication



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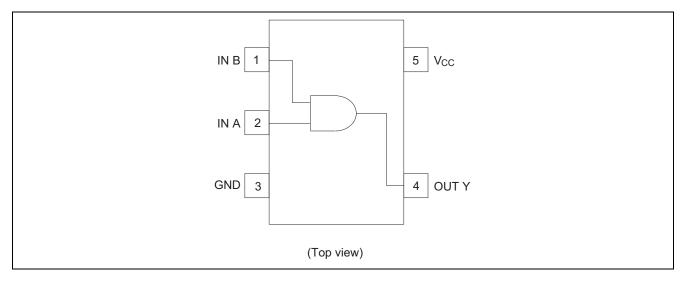


Function Table

Inp	Inputs					
Α	В	Output Y				
L	L	L				
Н	L	L				
L	Н	L				
Н	Н	Н				

H : High level L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	Vı	-0.5 to 7.0	V	
Output voltage range *1, 2		-0.5 to V _{CC} + 0.5	V	Output : H or L
Output voltage range	Vo	-0.5 to 7.0	ľ	V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V _I < 0
Output clamp current	I _{OK}	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I _O	±25	mA	$V_O = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
		_	1		V _{CC} = 1.65 to 1.95 V
		_	2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
	I _{OL}	_	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
Output ourront		_	12	mA	$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Output current	Іон	_	-1		V _{CC} = 1.65 to 1.95 V
		_	-2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	-6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	-12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
		0	300		V _{CC} = 1.65 to 1.95 V
Input transition rise or fall rate	A+ / A>	0	200	ns / V	$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
Input transition rise or fall rate	Δt / Δν	0	100	ns/v	$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristics

• $Ta = -40 \text{ to } 85^{\circ}\text{C}$

Item	Symbol	V _{CC} (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	V _{CC} ×0.75	_	_		
	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	2.3 to 2.7	V _{CC} ×0.7	_	_		
	V _{IH}	3.0 to 3.6	V _{CC} ×0.7	_	_		
Innut valtage		4.5 to 5.5	V _{CC} ×0.7	_	_	V	
Input voltage		1.65 to 1.95	_	_	V _{CC} ×0.25	V	
	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	2.3 to 2.7	_	_	V _{CC} ×0.3		
	V _{IL}	3.0 to 3.6	_	_	V _{CC} ×0.3		
		4.5 to 5.5	_	_	V _{CC} ×0.3		
		1.8	_	0.25	_		
Hystorosia valtaga \		2.5	_	0.30	_	V	$V_T^+ - V_T^-$
Hysteresis voltage	V _H	3.3	_	0.35	_	v	V _T - V _T
		5.0	_	0.45	_		
		Min to Max	V _{CC} -0.1	_	_		I _{OH} = -50 μA
		1.65	1.4	_	_		I _{OH} = -1 mA
	V_{OH}	2.3	2.0	_	_		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_		$I_{OH} = -6 \text{ mA}$
Output voltage		4.5	3.8	_	_	V	I _{OH} = -12 mA
Output voltage		Min to Max	_	_	0.1	V	$I_{OL} = 50 \mu A$
		1.65	_	_	0.3		I _{OL} = 1 mA
	V_{OL}	2.3	_	_	0.4		I _{OL} = 2 mA
		3.0	_	_	0.44		I _{OL} = 6 mA
		4.5	_	_	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent	I _{CC}	5.5	_	_	10	μΑ	$V_{IN} = V_{CC}$ or GND,
supply current	ICC	0.0		_	10	μΑ	I _O = 0
Output leakage		0	_	_	5	μΑ	V_{IN} or $V_O = 0$ to 5.5 V
current							
Input capacitance	C _{IN}	3.3	_	2.5	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

$\bullet \quad V_{CC} = 1.8 \pm 0.15 \ V$

lto m	Cumbal		Ta = 25°C		Ta = -40	to 85°C	l lmit	Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	12.8	22.7	1.0	25.0		C _L = 15 pF	Λ or D	V
delay time	t _{PHL}	_	19.4	32.8	1.0	38.5	ns	C _L = 50 pF	A or B	ĭ

$\bullet \quad V_{CC} = 2.5 \pm 0.2 \ V$

lto m	Cumbal		Ta = 25°C		Ta = -40	to 85°C	l lmit	Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	7.9	13.8	1.0	16.0		C _L = 15 pF	A or D	V
delay time	t _{PHL}	_	10.5	17.3	1.0	20.0	ns	C _L = 50 pF	A or B	Y

$\bullet \quad V_{CC} = 3.3 \pm 0.3 \ V$

Itam	Cumbal		Ta = 25°C		Ta = -40) to 85°C	l lmit	Test	FROM	то
Item	Symbol	Min Typ I		Max	Min	Max Unit		Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	5.6	8.8	1.0	10.5		C _L = 15 pF	A or D	V
delay time	t _{PHL}	_	7.5	12.3	1.0	14.0	ns	C _L = 50 pF	A or B	ĭ

$\bullet \quad V_{CC} = 5.0 \pm 0.5 \ V$

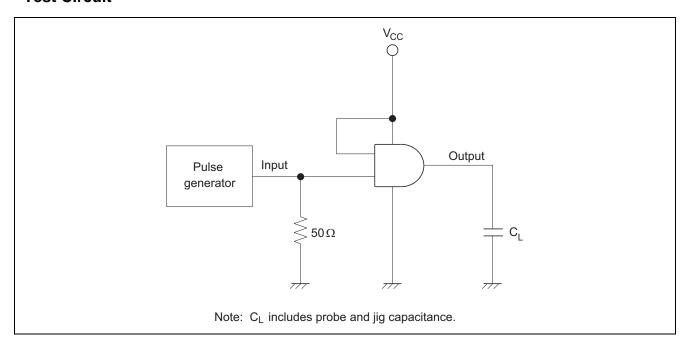
Itam	Cumbal		Ta = 25°C		Ta = -40	to 85°C	l lmit	Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	4.1	5.9	1.0	7.0		C _L = 15 pF	A or B	V
delay time	t _{PHL}	_	5.5	7.9	1.0	9.0	ns	C _L = 50 pF	AUID	r

Operating Characteristics

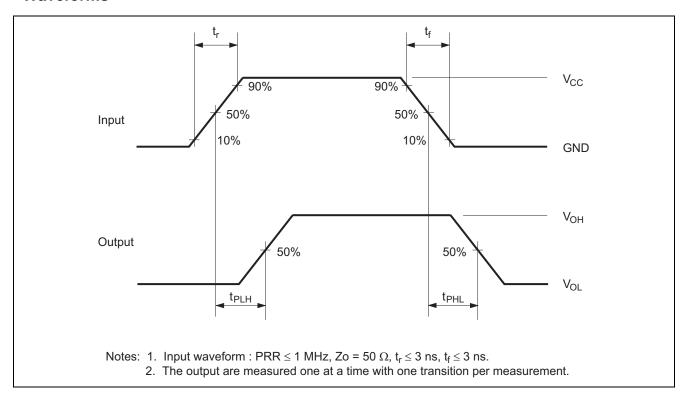
• $C_L = 50 pF$

ltom	Cumbal	V 00	Ta = 25°C			l lmit	Took Conditions	
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test Conditions	
Power dissipation		3.3	_	8.0	_	pF	f 40 MH=	
capacitance	C _{PD}	5.0		10.0	_	ρг	f = 10 MHz	

Test Circuit

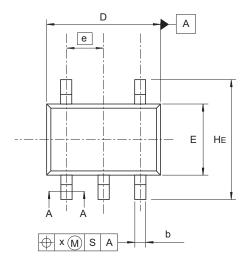


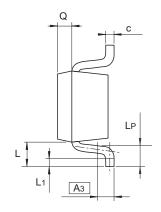
Waveforms

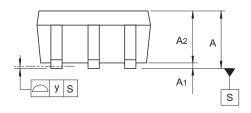


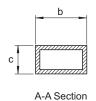
Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]	
SC-88A	PTSP0005ZC-A	CMPAK-5 / CMPAK-5V	0.006	



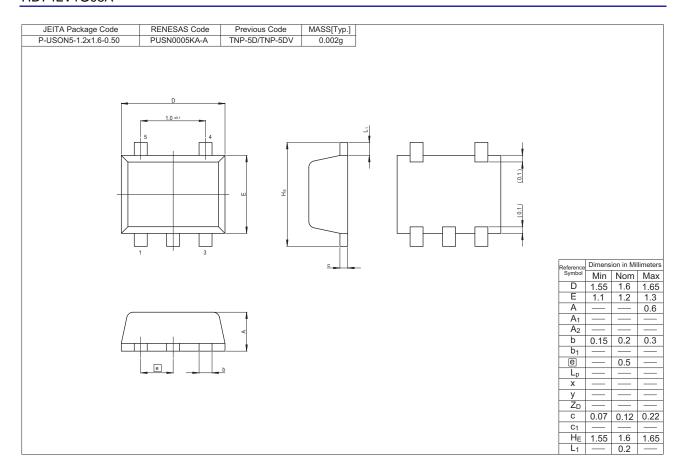






Reference	Dimensi	ons in mi	llimeters
Symbol	Min	Nom	Max
Α	8.0	_	1.1
A ₁	0		0.1
A ₂	8.0	0.9	1.0
A_3		0.25	
b	0.15	0.22	0.3
С	0.1	0.13	0.15
D	1.8	2.0	2.2
E	1.15	1.25	1.35
е	_	0.65	_
HE	1.8	2.1	2.4
L	0.3	_	0.7
L ₁	0.1	_	0.5
LP	0.2		0.6
Х			0.05
y Q			0.05
Q	_	0.25	_

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