

# **OC-M Type**

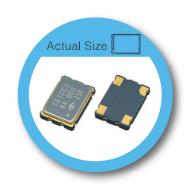
## 7.0 x 5.0 mm SMD Crystal Oscillator

#### **FEATURE**

- Typical  $7.0 \times 5.0 \times 1.3$  mm ceramic SMD package.
- Output frequency up to 200 MHz
- Tri-state enable/disable

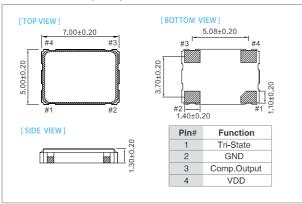
#### **TYPICAL APPLICATION**

- xDSL, WLAN, Fiber/10G-Bit Ethernet
- Notebook, PDA
- PC main board, VGA card

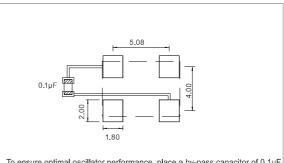


**RoHS Compliant** 

## **DIMENSION (mm)**



## **SOLDER PAD LAYOUT (mm)**



To ensure optimal oscillator performance, place a by-pass capacitor of 0.1µF as close to the part as possible between Vdd and GND pads.

#### **ELECTRICAL SPECIFICATION**

Parameter	3.3 V		2.5 V		1.8 V		unit
	Min.	Max.	Min.	Max.	Min.	Max.	unit
Supply Voltage Variation (VDD)	VDD-10%	VDD+10%	VDD-10%	<b>V</b> DD+10%	VDD-10%	VDD+10%	V
Frequency Range	2.048	200	2.048	166	2.048	110	MHz
VDD Sensitivity (±10 %)	<b>-</b> 2	2	-2	2	-2	2	ppm
Supply Current 2.048 MHz≦Fo≦110 MHz	_	20	_	15	_	10	
110 MHz <fo≦166 mhz<="" th=""><th>_</th><th>25</th><th>_</th><th>20</th><th>_</th><th>_</th><th>mA</th></fo≦166>	_	25	_	20	_	_	mA
166 MHz <fo≦200 mhz<="" th=""><th>_</th><th>30</th><th>_</th><th>_</th><th>_</th><th>_</th><th></th></fo≦200>	_	30	_	_	_	_	
Duty Cycle	40	60	40	60	40	60	%
Output Level (CMOS)							
Output High (Logic "1")	2.97	_	2.25	_	1.62	_	V
Output Low (Logic "0")	-	0.33	_	0.25	_	0.18	
Transition Time:Rise/Fall Time <sup>+</sup>							
$2.048  \text{MHz} \le \text{Fo} \le 200  \text{MHz}$	_	5	_	5	_	5	nSec
Start Time	_	5	_	5	_	5	mSec
Tri-State(Input to Pin 1)							
Enable (High voltage or floating)	2.31	_	1.75	_	1.26	_	V
Disable (Low voltage or GND)	_	0.99	_	0.75	_	0.54	V
Period Jitter(Pk-Pk)							
Specific Frequency"	_	40	_	40	_	40	pSec
Others	_	200	_	200	_	200	poec
Standby Current	_	15		15		15	μΑ
Aging ( @ 25°C 1st year)	_	±3	_	±3	_	±3	ppm
Storage Temp. Range	-55	125	-55	125	-55	125	°C

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

### FREQ. STABILITY vs. TEMP. RANGE

Temp. (°C)	±20	±25	±50
-10 ~ +60	0	0	0
-20 ~ +70	$\triangle$	0	0
-40 ~ +85	$\wedge$		

 $<sup>^*</sup>$  ○: Available △:Conditional X: Not available

Note: not all combination of options are available. Other specifications may be available upon reques

<sup>+</sup> Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

"Specific frequencies include 4.0, 6.0, 8.0, 12.0, 13.0, 16.0, 19.2, 20.0, 24.0, 26.0, 32.0, 38.4 and 40.0MHz

<sup>\*</sup> Inclusive of calibration @ 25 °C, operating temperature range, input voltage variation, load variation, aging (1st year), shock, and vibration