## Advance Information

## Multiple Output Clock Synthesizer

The MPC9108 is a multiple CMOS output clock synthesizer targeted for disk drive applications. The device interfaces to a 20MHz crystal as its frequency source. From this source the device provides a buffered copy of the 20MHz clock as well as synthesized 40MHz and 50MHz output clocks.

- Fully Integrated PLL
- Fully Integrated Crystal Oscillator
- Low cost, low jitter design
- Low cost 8-lead SOIC packaging

In addition to the output clock frequencies, the MPC9108 also offers a lock indicator output. When the internal PLL achieves phase and frequency lock the CLK\_LOCK signal will go HIGH. The pin will remain HIGH unless the PLL loses lock due to input clock or power supply disturbances.

The XTALIN pin (pin 1) can be over—driven with a standard 5V CMOS signal. When an externally generated reference is used the XTALOUT pin should be left open.

The MPC9108 operates from a 5.0V supply across the commercial temperature range of 0°C to 70°C. The 8–lead SOIC package is used to optimize board space efficiency as well as minimizing cost.

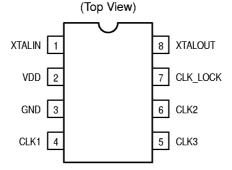
## **MPC9108**

# MULTIPLE OUTPUT CLOCK SYNTHESIZER



D SUFFIX 8-LEAD PLASTIC SOIC PACKAGE CASE 751-06

## Pinout: 8-Lead SOIC



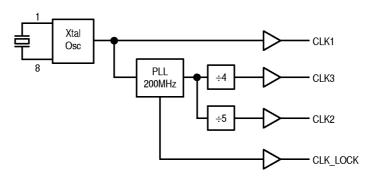
#### **Pin Descriptions**

Pin Name	Pin Number	I/O	Function			
XTALIN	1	I	20MHz Crystal Connection, External Reference Point			
VDD	2	-	+5V Power Supply			
GND	3	-	Ground			
CLK1	4	0	20MHz Output, Buffer Xtal Output			
CLK3	5	0	50MHz Output, PLL Controlled			
CLK2	6	0	40MHz Output, PLL Controlled			
CLK_LOCK	7	0	HIGH When PLL is Locked			
XTALOUT	8	0	Crystal Oscillator Connection			

This document contains information on a new product. Specifications and information herein are subject to change without notice.

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#### **BLOCK DIAGRAM**



#### **MAXIMUM RATINGS\***

Symbol	Parameter	Value	Unit
VCC	DC Supply Voltage (Referenced to GND)	-0.5 to +7.0	V
V <sub>IN</sub>	Input Voltage	-0.5 to +7.0	V
TА	Operating Temperature Range (In Free-Air)	0 to +70	°C
TA	Ambient Temperature Range (Under Bias)	-55 to +125	°C
TSTG	Storage Temperature Range	-65 to +150	°C

<sup>\*</sup> Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

### **DC CHARACTERISTICS** (0°C < T<sub>A</sub> < 70°C; $V_{DD}$ = 5V $\pm$ 10%; Unless Otherwise Stated)

Symbol	Characteristic	Min	Тур	Max	Unit	Condition
V <sub>IL</sub>	Input Low Voltage			0.8	٧	V <sub>DD</sub> = 5V
VIH	Input High Voltage	2.0			٧	V <sub>DD</sub> = 5V
IJL	Input Low Current			<b>-</b> 5	μА	V <sub>IN</sub> = 0.5V
ΊΗ	Input High Current			5	μА	$V_{IN} = V_{DD}$
V <sub>OL</sub>	Output Low Voltage CLKn			0.4	٧	I <sub>OL</sub> = 4mA
V <sub>OL</sub>	Output Low Voltage CLK_LOCK			0.4	٧	I <sub>OL</sub> = 10mA
VOH	Output High Voltage	0.8V <sub>DD</sub>			٧	I <sub>OH</sub> = -30mA
lDD	Supply Current		50	65	mA	No Load; Note 1.
FD	Output Frequency Change Over Supply & Temp		0.002	0.01	%	With Respect to Typ Freq
Isc	Short Circuit Current		40		mA	Each Output Clock
Cl	Input Capacitance			10	pF	Except X1, X2
CL	Xtal Load Capacitance		20		pF	Pins X1, X2

<sup>1.</sup> All clocks operating at highest frequencies.

MOTOROLA 2

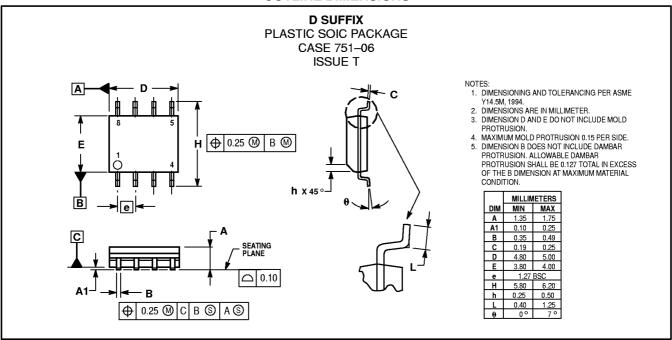
## AC CHARACTERISTICS (0°C < T<sub>A</sub> < 70°C; $V_{DD}$ = 5V $\pm$ 10%; Unless Otherwise Stated)

Symbol	Characteristic		Min	Тур	Max	Unit	Condition
<sup>†</sup> ICr	Input Clock Rise Time				20	ns	
<sup>†</sup> ICf	Input Clock Fall Time				20	ns	
t <sub>r</sub>	Output Rise Time	0.8 to 2.0V		1.0	2.0	ns	30pf Load
t <sub>r</sub>	Rise Time	20% to 80% V <sub>DD</sub>		2.0	4.0	ns	30pf Load
tf	Output Fall Time	2.0 to 0.8V		1.0	2.0	ns	30pf Load
tf	Fall Time	20% to 80% V <sub>DD</sub>		2.0	4.0	ns	30pf Load
dt	Duty Cycle	Pins 4, 6, 5	45/55	48/52	55/45	%	30pf Load
fi	Input Frequency			20		MHz	
<sup>t</sup> per	Output Period	Pins 4, 6, 5	-1		1	ns	
†lock	Output Lock Time		0.02	3.0	4.0	ms	

3

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#### **OUTLINE DIMENSIONS**



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