

March 1998



DM74LS157/DM74LS158 Quad 2-Line to 1-Line Data Selectors/Multiplexers

General Description

These data selectors/multiplexers contain inverters and drivers to supply full on-chip data selection to the four output gates. A separate strobe input is provided. A 4-bit word is selected from one of two sources and is routed to the four outputs. The LS157 presents true data whereas the LS158 presents inverted data to minimize propagation delay time.

Applications

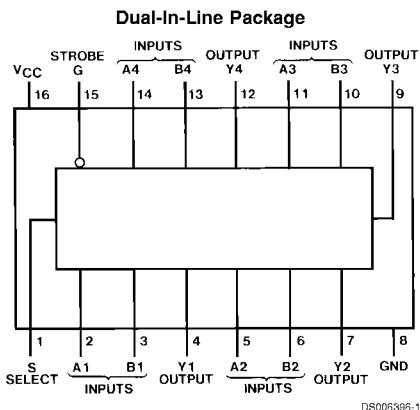
- Expand any data input point
- Multiplex dual data buses
- Generate four functions of two variables (one variable is common)

- Source programmable counters

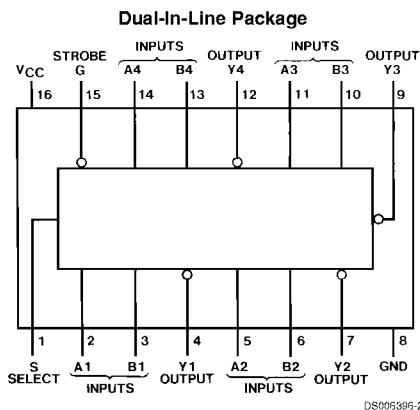
Features

- Buffered inputs and outputs
- Typical Propagation Time
 - LS157 9 ns
 - LS158 7 ns
- Typical Power Dissipation
 - LS157 49 mW
 - LS158 24 mW

Connection Diagrams



Order Number 54LS157DMQB, 54LS157FMQB,
54LS157LMQB, DM54LS157J, DM54LS157W,
DM74LS157M or DM74LS157N
See Package Number E20A, J16A,
M16A, N16E or W16A



Order Number 54LS158DMQB, 54LS158FMQB,
54LS158LMQB, DM54LS158J, DM54LS158W,
DM74LS158M or DM74LS158N
See Package Number E20A, J16A,
M16A, N16E or W16A

Function Table

Inputs				Output Y	
Strobe	Select	A	B	LS157	LS158
H	X	X	X	L	H
L	L	L	X	L	H
L	L	H	X	H	L
L	H	X	L	L	H
L	H	X	H	H	L

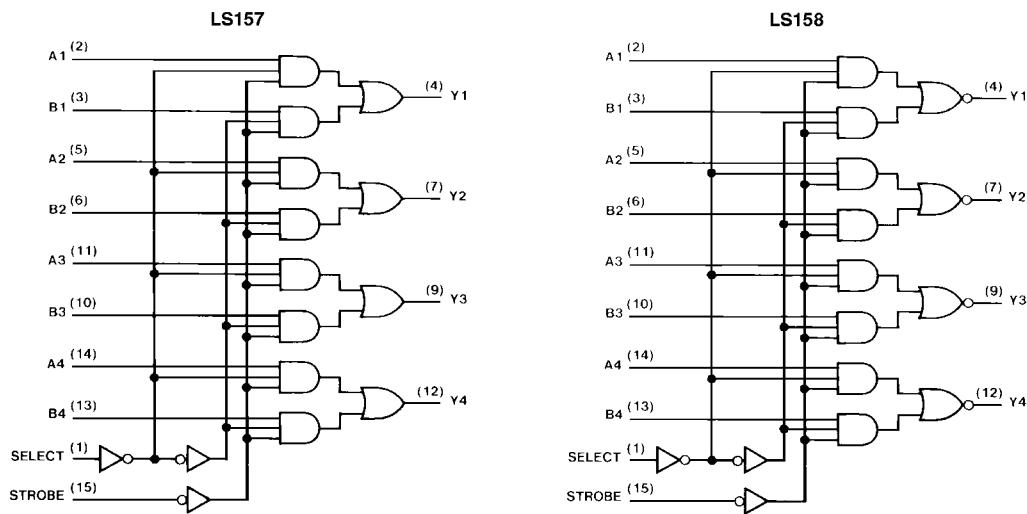
H = High Level, L = Low Level, X = Don't Care

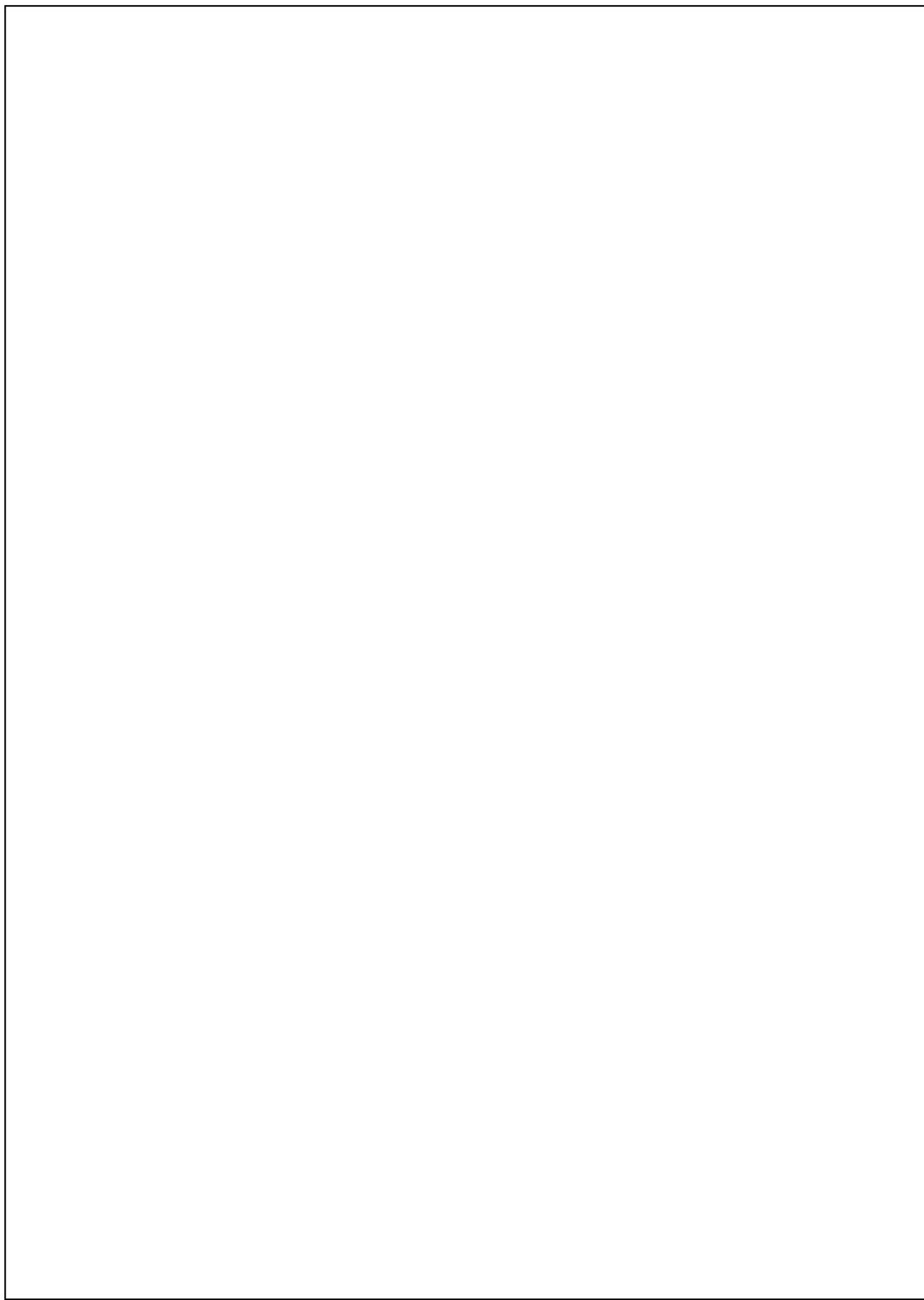
'LS158 Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^\circ C$

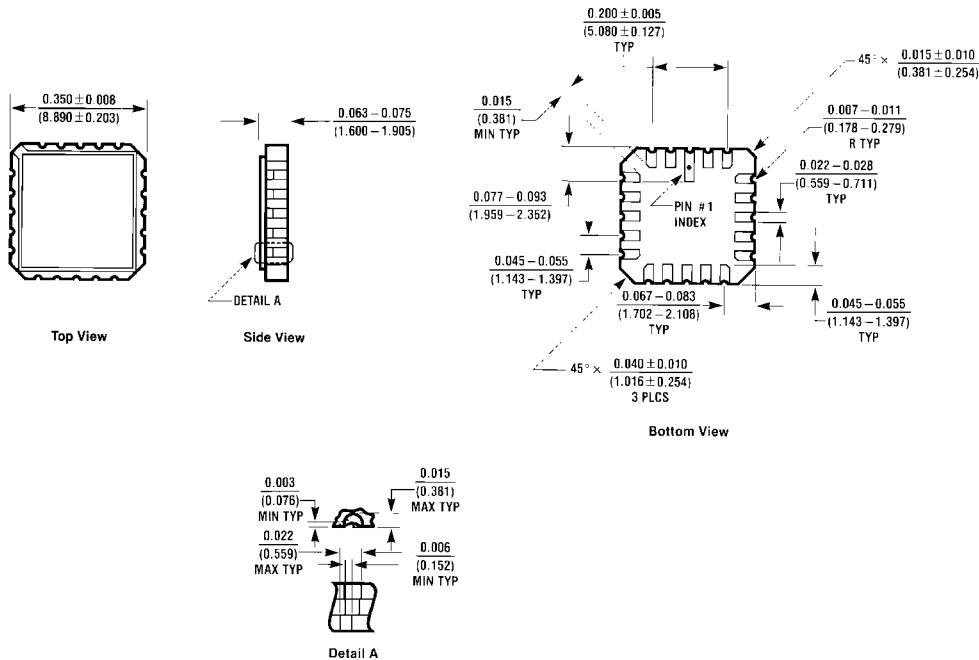
Symbol	Parameter	From (Input) To (Output)	$R_L = 2 k\Omega$				Units	
			$C_L = 15 \text{ pF}$		$C_L = 50 \text{ pF}$			
			Min	Max	Min	Max		
t_{PLH}	Propagation Delay Time Low to High Level Output	Data to Y		12		18	ns	
t_{PHL}	Propagation Delay Time High to Low Level Output	Data to Y		12		21	ns	
t_{PLH}	Propagation Delay Time Low to High Level Output	Strobe to Y		17		23	ns	
t_{PHL}	Propagation Delay Time High to Low Level Output	Strobe to Y		18		28	ns	
t_{PLH}	Propagation Delay Time Low to High Level Output	Select to Y		20		24	ns	
t_{PHL}	Propagation Delay Time High to Low Level Output	Select to Y		24		36	ns	

Logic Diagrams

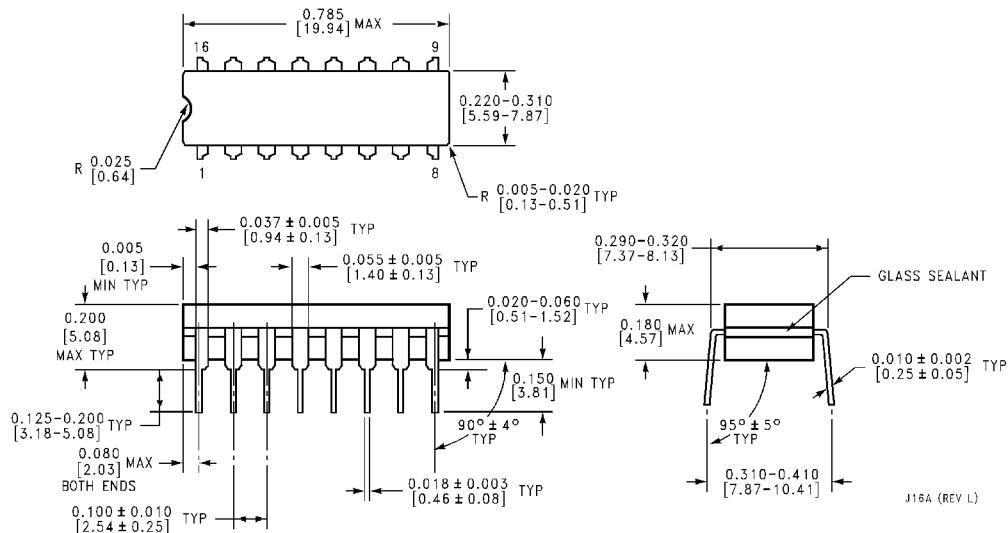




Physical Dimensions inches (millimeters) unless otherwise noted

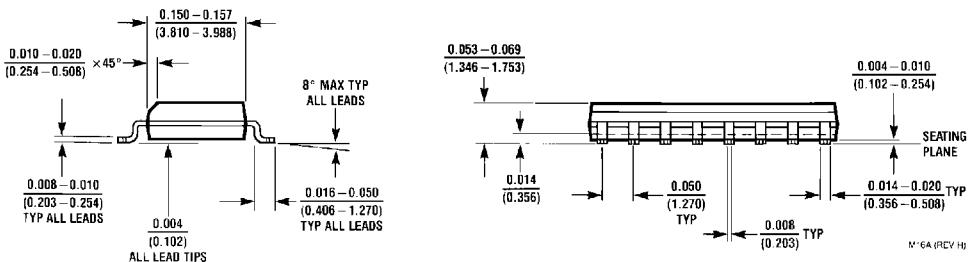
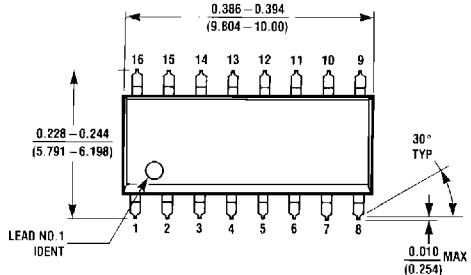


Ceramic Leadless Chip Carrier Package (E)
Order Number 54LS157LMQB or 54LS158LMQB
Package Number E20A

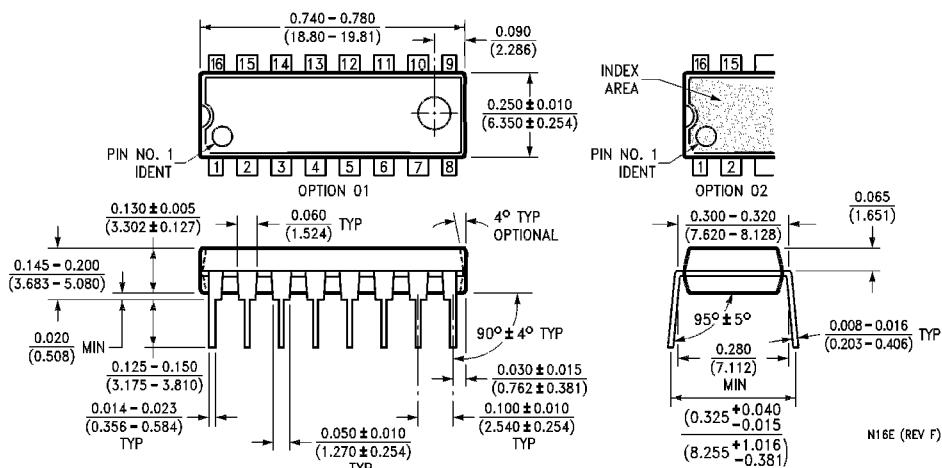


16-Lead Ceramic Dual-In-Line Package (J)
Order Number 54LS157DMQB, 54LS158DMQB, DM54LS157J or DM54LS158J
Package Number J16A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



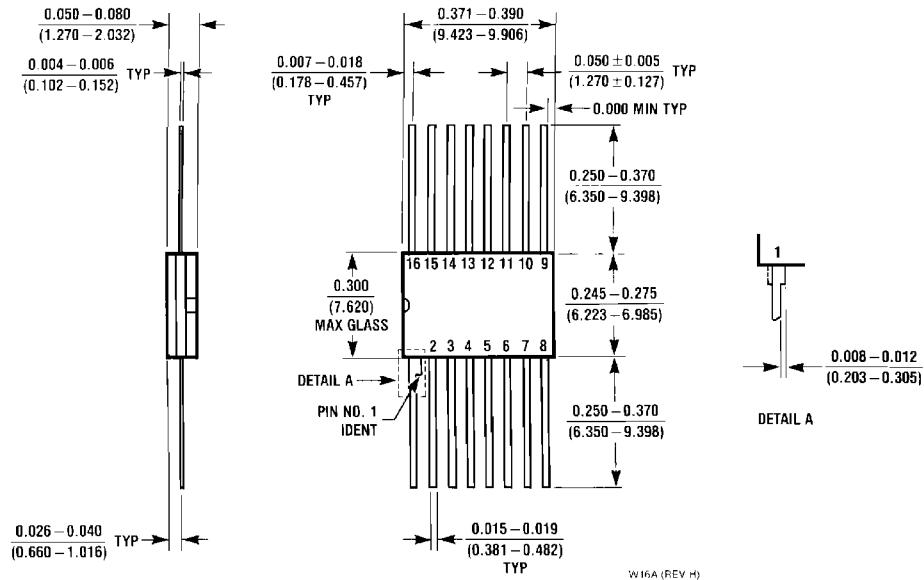
16-Lead Small Outline Molded Package (M)
Order Number DM74LS157M or DM74LS158M
Package Number M16A



16-Lead Molded Dual-In-Line Package (N)
Order Number DM74LS157N or DM74LS158N
Package Number N16E

DM74LS157/DM74LS158 Quad 2-Line to 1-Line Data Selectors/Multiplexers

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



16-Lead Ceramic Flat Package (W)
Order Number 54LS157FMQB, 54LS158FMQB, DM54LS157W or DM54LS158W
Package Number W16A

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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