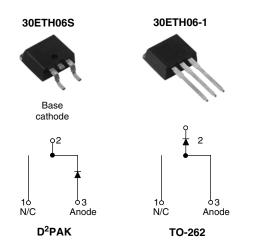


Vishay High Power Products

Hyperfast Rectifier, 30 A FRED PtTM



PRODUCT SUMMARY				
t _{rr} (typical)	28 ns			
I _{F(AV)}	30 A			
V_{R}	600 V			

FEATURES

- · Hyperfast recovery time
- Low forward voltage drop
- · Low leakage current
- 125 °C operating junction temperature
- · Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC-DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage	V_{RRM}		600	V	
Average rectified forward current	I _{F(AV)}	T _C = 103 °C	30	Λ	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	200	A	
Operating junction and storage temperatures	T_J, T_{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	600	-	-		
Forward voltage V _F		I _F = 30 A	-	2.0	2.6	V	
		I _F = 30 A, T _J = 150 °C	-	1.34	1.75	1	
Reverse leakage current I _R		$V_R = V_R$ rated	-	0.3	50	- μΑ	
		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	60	500		
Junction capacitance	C _T	V _R = 600 V	-	33	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -		nΗ			

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	28	35	
Reverse recovery time t _{rr}	T _J = 25 °C		-	31	-	ns	
		T _J = 125 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	77	-	
Peak recovery current	,	T _J = 25 °C		-	3.5	-	Α
	I _{RRM}	T _J = 125 °C		-	7.7	-	^
Reverse recovery charge	0	T _J = 25 °C		-	65	-	nC
	Q _{rr}	T _J = 125 °C		-	345	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}	R _{thJC}		0.7	1.1	
Thermal resistance, junction to ambient per leg	R _{thJA}	R _{thJA} Typical socket mount		-	70	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	
Weight			-	2.0	-	g
			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Maddan dada		Case style D ² PAK	30ETH06S			
Marking device		Case style TO-262	30ETH06-1			



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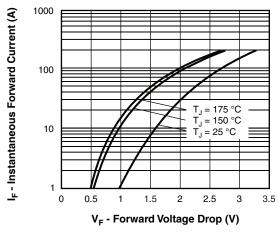


Fig. 1 - Typical Forward Voltage Drop Characteristics

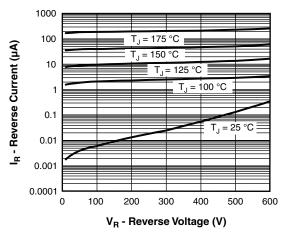


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

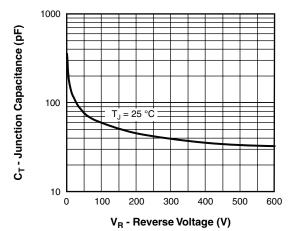


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

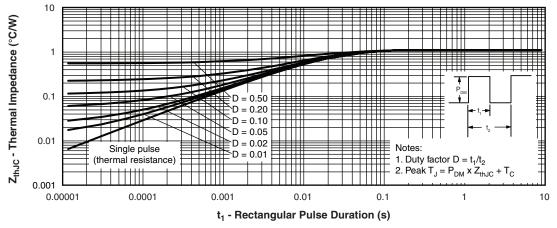
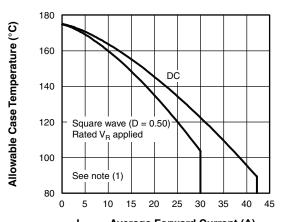


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

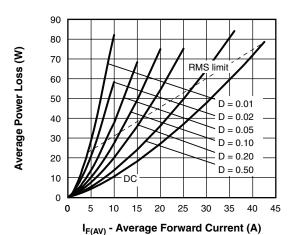


Fig. 6 - Forward Power Loss Characteristics

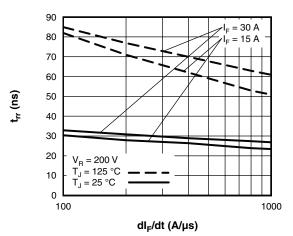


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

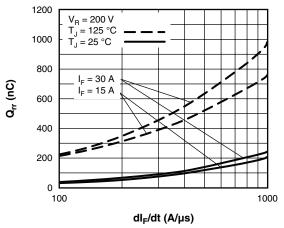


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at} \ (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at} \ V_{R1} = \text{Rated} \ V_R \\ \end{array}$



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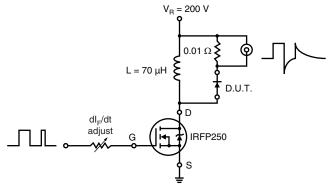
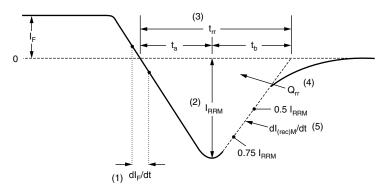


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
 - .
- (2) I_{RRM} peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (5) $dI_{(rec)M}/dt$ peak rate of change of current during t_b portion of t_{rr}

(4) Q_{rr} - area under curve defined by t_{rr}

and I_{RRM}

Fig. 10 - Reverse Recovery Waveform and Definitions

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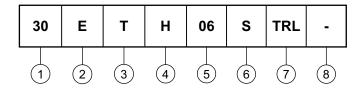
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ORDERING INFORMATION TABLE

Device code



1 - Current rating (30 = 30 A)

2 - E = Single diode

3 - T = TO-220, D²PAK

4 - H = Hyperfast recovery

5 - Voltage rating (06 = 600 V)

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented, for D²PAK package)

• TRR = Tape and reel (right oriented, for D²PAK package)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95014					
Part marking information	www.vishay.com/doc?95008				
Packaging information	www.vishay.com/doc?95032				



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Revision: 18-Jul-08

Document Number: 91000 www.vishay.com