

MRF305 (SILICON)

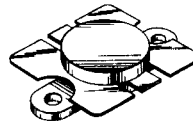
The RF Line

NPN SILICON RF POWER TRANSISTOR

... designed primarily for wideband large-signal driver and output amplifier stages in the 225-400 MHz frequency range.

- Specified 28 Volt, 400 MHz Characteristics --
Output Power = 30 Watt
Minimum Gain = 8.0 dB
Efficiency = 55% (Min)
- Built-In Matching Network for Broadband Operation
- 100% Tested for Load Mismatch at all Phase Angles with 30:1 VSWR

30 W-400 MHz
CONTROLLED "Q"
RF POWER
TRANSISTOR
NPN SILICON



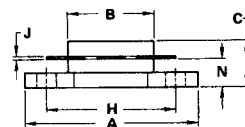
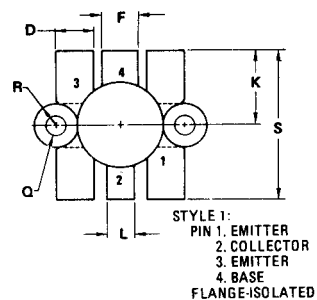
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	33	Vdc
Collector-Base Voltage	V_{CBO}	60	Vdc
Emitter-Base Voltage	V_{EBO}	4.0	Vdc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ (1) Derate above 25°C	P_D	70 0.4	Watts W/ $^\circ\text{C}$
Storage Temperature Range	T_{stg}	-65 to +200	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	θ_{JC}	2.5	$^\circ\text{C}/\text{W}$

{1} These devices are designed for RF operation. The total device dissipation rating applies only when the devices are operated as RF amplifiers.



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	24.38	25.15	0.960	0.990
B	12.45	12.95	0.490	0.510
C	5.97	7.62	0.235	0.300
D	5.46 TYP		0.215 TYP	
F	5.08	5.33	0.200	0.210
H	18.29	18.54	0.720	0.730
J	0.10	0.15	0.004	0.006
K	10.67	10.92	0.420	0.430
L	3.81	4.06	0.150	0.160
N	3.81	4.32	0.150	0.170
Q	2.92	3.18	0.115	0.125
R	3.05	3.30	0.120	0.130
S	21.34	21.84	0.840	0.860

CASE 278-03

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage ($I_C = 50 \text{ mAdc}, I_B = 0$)	BV_{CEO}	33	—	—	Vdc
Collector-Emitter Breakdown Voltage ($I_C = 50 \text{ mAdc}, V_{BE} = 0$)	BV_{CES}	60	—	—	Vdc
Emitter-Base Breakdown Voltage ($I_E = 3.0 \text{ mAdc}, I_C = 0$)	BV_{EBO}	4.0	—	—	Vdc
Collector Cutoff Current ($V_{CB} = 30 \text{ Vdc}, I_E = 0$)	I_{CBO}	—	—	2.0	mAdc
ON CHARACTERISTICS					
DC Current Gain ($I_C = 500 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$) ($I_C = 3.0 \text{ Adc}, V_{CE} = 5.0 \text{ Vdc}$)	h_{FE}	10 10	— —	100 —	—
DYNAMIC CHARACTERISTICS					
Output Capacitance ($V_{CB} = 28 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$)	C_{ob}	—	40	45	pF
FUNCTIONAL TESTS (Figure 1)					
Common-Emitter Amplifier Power Gain ($V_{CC} = 28 \text{ Vdc}, P_{out} = 30 \text{ W}, f = 400 \text{ MHz}$)	G_{pE}	8.0	—	—	dB
Collector Efficiency ($V_{CC} = 28 \text{ Vdc}, P_{out} = 30 \text{ W}, f = 400 \text{ MHz}$)	η	55	—	—	%
Electrical Ruggedness ($P_{out} = 30 \text{ W}, V_{CC} = 28 \text{ Vdc}, f = 400 \text{ MHz}, \text{VSWR } 30:1$, all phase angles)	—	No Degradation in P_{out}			—

FIGURE 1 – 400 MHz TEST CIRCUIT SCHEMATIC

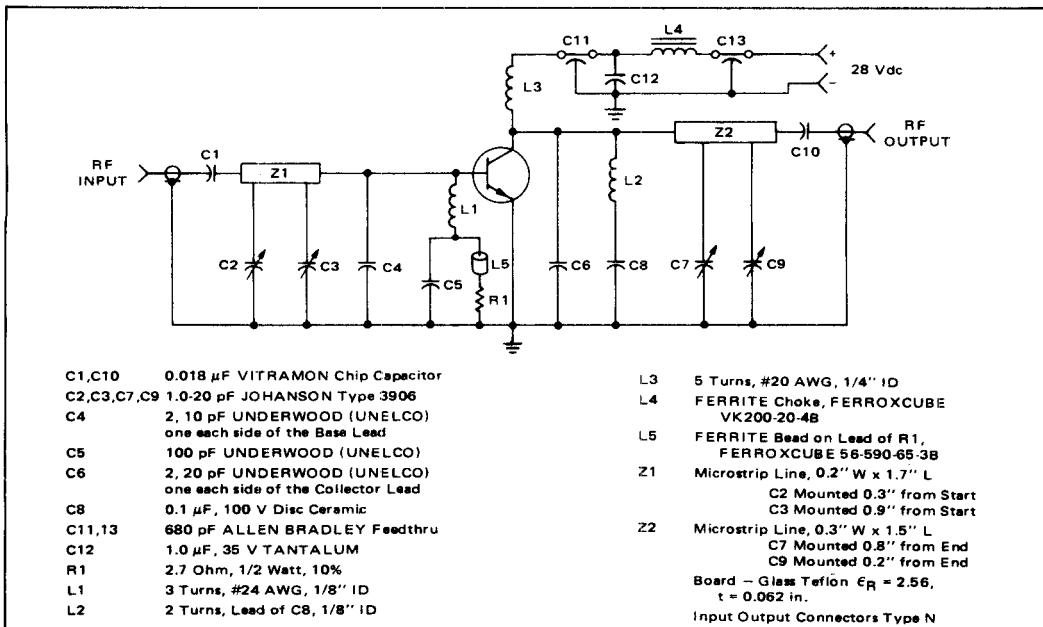


FIGURE 2 – OUTPUT POWER versus FREQUENCY

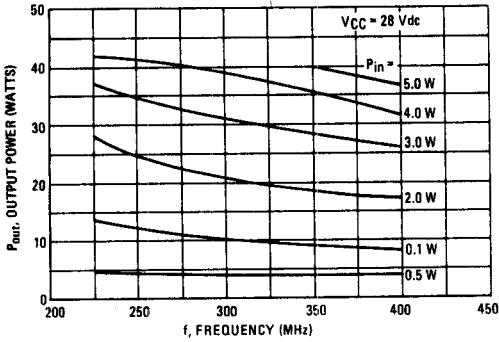


FIGURE 3 – OUTPUT POWER versus INPUT POWER

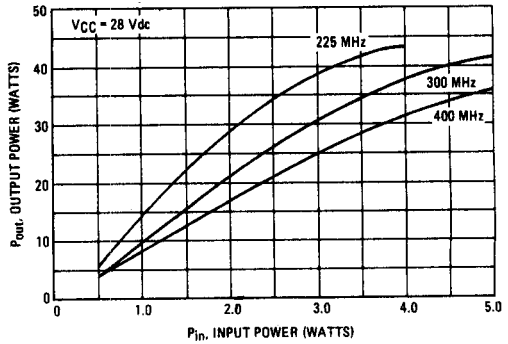


FIGURE 4 – POWER GAIN versus FREQUENCY

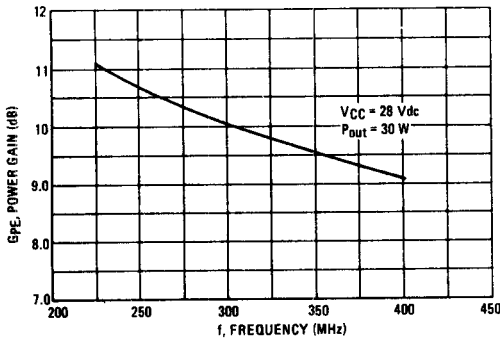


FIGURE 5 – OUTPUT POWER versus SUPPLY VOLTAGE – 400 MHz

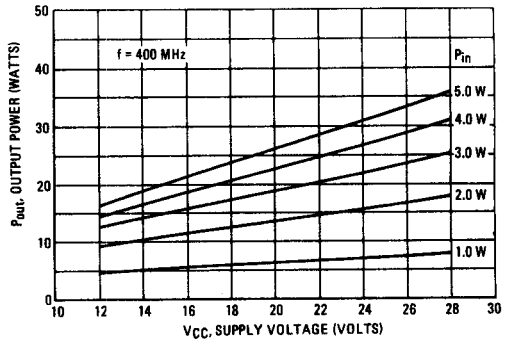


FIGURE 6 – OUTPUT POWER versus SUPPLY VOLTAGE – 225 MHz

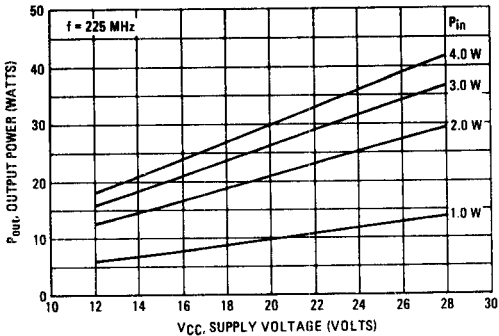


FIGURE 7 – SERIES EQUIVALENT IMPEDANCE

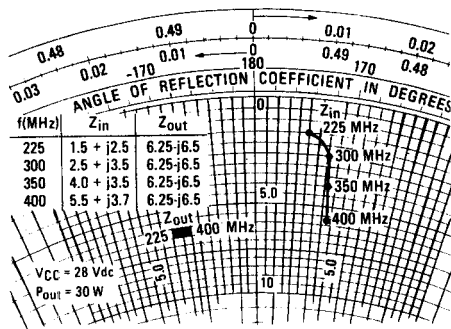


FIGURE 8 - INTERMODULATION DISTORTION versus
OUTPUT POWER (PEP)

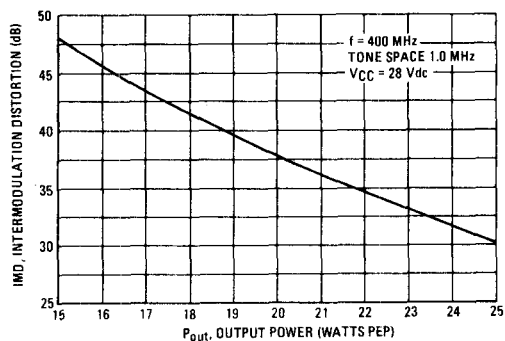


FIGURE 9 - 400 MHz TEST CIRCUIT

