TOSHIBA Transistor Silicon NPN · PNP Epitaxial Type (PCT process) (Bias Resistor Built-in Transistor)

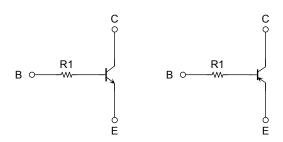
RN4992HFE, RN4993HFE

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

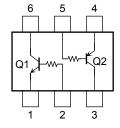
- Two devices are incorporated into an Extreme-Super-Mini (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

Equivalent Circuit

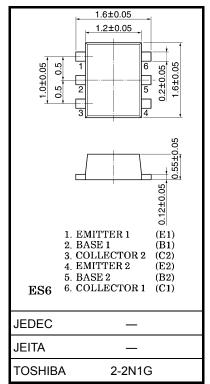
Q1 Q2



Equivalent Circuit (top view)



Unit: mm



Weight: 0.003g (typ.)



Absolute Maximum Ratings (Ta = 25°C) (Q1)

| Characteristics | Symbol | Rating | Unit |
|---------------------------|------------------|--------|------|
| Collector-base voltage | V_{CBO} | 40 | V |
| Collector-emitter voltage | V _{CEO} | 40 | V |
| Emitter-base voltage | V _{EBO} | 5 | V |
| Collector current | IC | 100 | mA |

Absolute Maximum Ratings (Ta = 25°C) (Q2)

| Characteristics | Symbol | Rating | Unit |
|---------------------------|------------------|--------|------|
| Collector-base voltage | V_{CBO} | -30 | V |
| Collector-emitter voltage | V _{CEO} | -30 | V |
| Emitter-base voltage | V _{EBO} | -5 | V |
| Collector current | IC | -100 | mA |

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

| Characteristics | Symbol | Rating | Unit |
|-----------------------------|-------------------------|----------------|------|
| Collector power dissipation | P _C (Note 1) | 100 | mW |
| Junction temperature | Tj | 150 | °C |
| Storage temperature range | T _{stg} | −55~150 | °C |

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating



Electrical Characteristics (Ta = 25°C) (Q1)

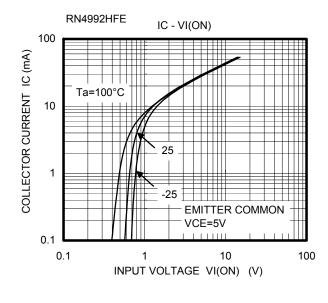
| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|--------------------------------------|-----------------------|---|-----|------|------|------|
| Collector cut-off current | I _{CBO} | $V_{CB} = 40 \text{ V}, I_{E} = 0$ | _ | _ | 100 | nA |
| Emitter cut-off current | I _{EBO} | V _{EB} = 5 V, I _C = 0 | _ | _ | 100 | nA |
| DC current gain | h _{FE} | $V_{CE} = 5 \text{ V}, I_{C} = 1 \text{ mA}$ | 300 | _ | _ | |
| Collector-emitter saturation voltage | V _{CE} (sat) | $I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$ | _ | 0.06 | 0.15 | ٧ |
| Transition frequency | f _T | V _{CE} = 10 V, I _C = 5 mA | _ | 250 | _ | MHz |
| Collector output capacitance | C _{ob} | V _{CB} = 10 V, I _E = 0, f = 1 MHz | _ | 3 | _ | pF |

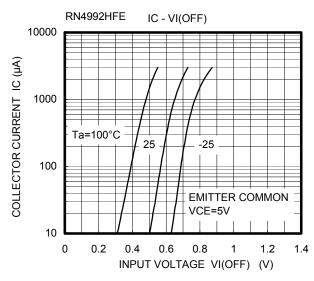
Electrical Characteristics (Ta = 25°C) (Q2)

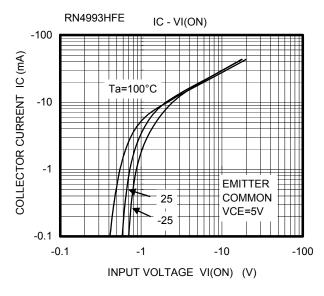
| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|--------------------------------------|-----------------------|--|-----|-------|-------|------|
| Collector cut-off current | I _{CBO} | $V_{CB} = -30 \text{ V}, I_E = 0$ | _ | _ | -100 | nA |
| Emitter cut-off current | I _{EBO} | $V_{EB} = -5 \text{ V}, I_C = 0$ | _ | _ | -100 | nA |
| DC current gain | h _{FE} | $V_{CE} = -5 \text{ V}, I_{C} = -1 \text{ mA}$ | 300 | _ | _ | |
| Collector-emitter saturation voltage | V _{CE} (sat) | $I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$ | _ | -0.06 | -0.15 | V |
| Transition frequency | f _T | $V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$ | _ | 200 | _ | MHz |
| Collector output capacitance | C _{ob} | $V_{CB} = -10 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$ | | 3 | _ | pF |

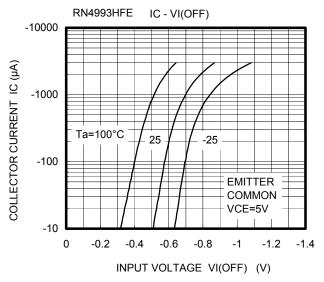
Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

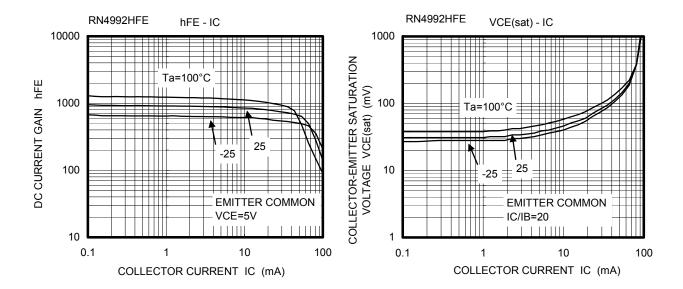
| Characte | eristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-----------------|-----------|--------|----------------|------|------|------|------|
| lanut na siatan | RN4992HFE | D4 | 17.6 | 22 | 26.4 | kO | |
| Input resistor | RN4993HFE | R1 | 37.6 | 37.6 | 47 | 56.4 | kΩ |

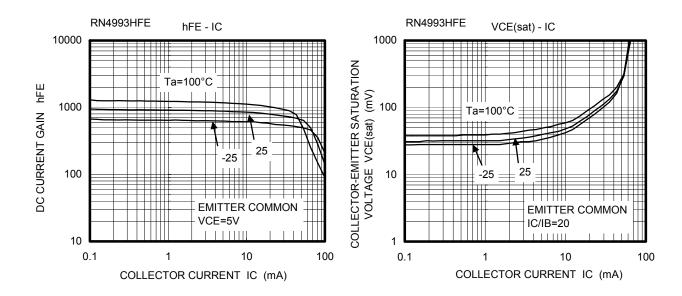




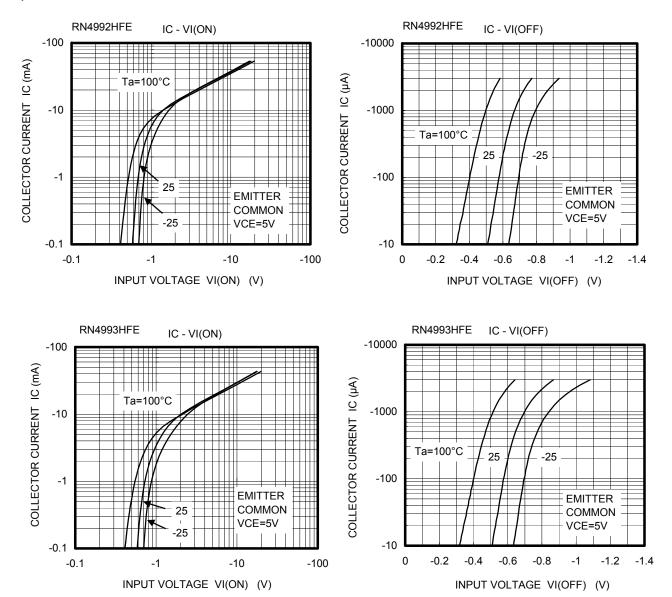




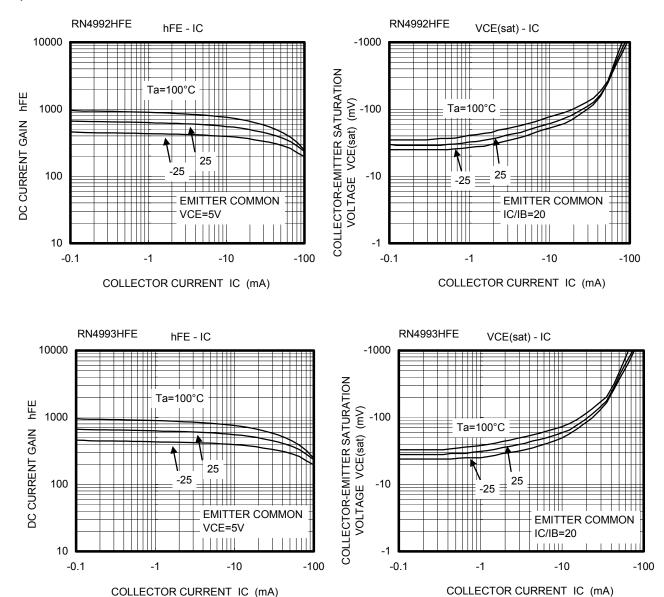


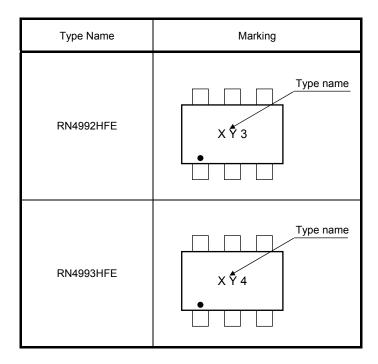


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