



CMSDM3590 N-CH  
CMSDM7590 P-CH

**SURFACE MOUNT  
N-CHANNEL AND P-CHANNEL  
ENHANCEMENT-MODE  
COMPLEMENTARY MOSFETS**

**SUPERmini™**



**SOT-323 CASE**

- Devices are *Halogen Free* by design

**APPLICATIONS:**

- Load/Power Switches
- Power Supply Converter Circuits
- Battery Powered Portable Devices

**MAXIMUM RATINGS: ( $T_A=25^\circ\text{C}$ )**

	<b>SYMBOL</b>	<b>CMSDM3590</b>	<b>CMSDM7590</b>	<b>UNITS</b>
Drain-Source Voltage	$V_{DS}$	20		V
Gate-Source Voltage	$V_{GS}$	8.0		V
Continuous Drain Current (Steady State)	$I_D$	160	140	mA
Continuous Drain Current ( $t_p \leq 5\text{s}$ )	$I_D$	200	180	mA
Power Dissipation	$P_D$	275		mW
Operating and Storage Junction Temperature	$T_J, T_{stg}$	-65 to +150		$^\circ\text{C}$
Thermal Resistance	$\Theta_{JA}$	455		$^\circ\text{C}/\text{W}$

**ELECTRICAL CHARACTERISTICS: ( $T_A=25^\circ\text{C}$ )**

<b>SYMBOL</b>	<b>TEST CONDITIONS</b>	<b>CMSDM3590</b>			<b>CMSDM7590</b>			<b>UNITS</b>
		<b>MIN</b>	<b>Typ</b>	<b>MAX</b>	<b>MIN</b>	<b>Typ</b>	<b>MAX</b>	
$I_{GSSF}, I_{GSSR}$	$V_{GS}=5.0\text{V}, V_{DS}=0\text{V}$	-	-	100	-	-	100	nA
$I_{DSS}$	$V_{DS}=5.0\text{V}, V_{GS}=0\text{V}$	-	-	50	-	-	50	nA
$I_{DSS}$	$V_{DS}=16\text{V}, V_{GS}=0\text{V}$	-	-	100	-	-	100	nA
$BV_{DSS}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	20	-	-	20	-	-	V
$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.4	-	1.0	0.4	-	1.0	V
$r_{DS(ON)}$	$V_{GS}=4.5\text{V}, I_D=100\text{mA}$	-	1.5	3.0	-	4.0	5.0	$\Omega$
$r_{DS(ON)}$	$V_{GS}=2.5\text{V}, I_D=50\text{mA}$	-	2.0	4.0	-	5.5	7.0	$\Omega$
$r_{DS(ON)}$	$V_{GS}=1.8\text{V}, I_D=20\text{mA}$	-	3.0	6.0	-	8.0	10	$\Omega$
$r_{DS(ON)}$	$V_{GS}=1.5\text{V}, I_D=10\text{mA}$	-	4.0	10	-	11	17	$\Omega$
$r_{DS(ON)}$	$V_{GS}=1.2\text{V}, I_D=1.0\text{mA}$	-	7.0	-	-	20	-	$\Omega$
$g_{FS}$	$V_{DS}=5.0\text{V}, I_D=125\text{mA}$	-	1.3	-	-	1.3	-	S
$C_{rss}$	$V_{DS}=15\text{V}, V_{GS}=0\text{V}, f=1.0\text{MHz}$	-	2.2	-	-	1.0	-	pF
$C_{iss}$	$V_{DS}=15\text{V}, V_{GS}=0\text{V}, f=1.0\text{MHz}$	-	9.0	-	-	12	-	pF
$C_{oss}$	$V_{DS}=15\text{V}, V_{GS}=0\text{V}, f=1.0\text{MHz}$	-	3.0	-	-	2.7	-	pF
$t_{on}$	$V_{DD}=10\text{V}, V_{GS}=4.5\text{V}, I_D=200\text{mA}$	-	40	-	-	60	-	ns
$t_{off}$	$V_{DD}=10\text{V}, V_{GS}=4.5\text{V}, I_D=200\text{mA}$	-	150	-	-	210	-	ns

**Central**™  
**Semiconductor Corp.**

**DESCRIPTION:**

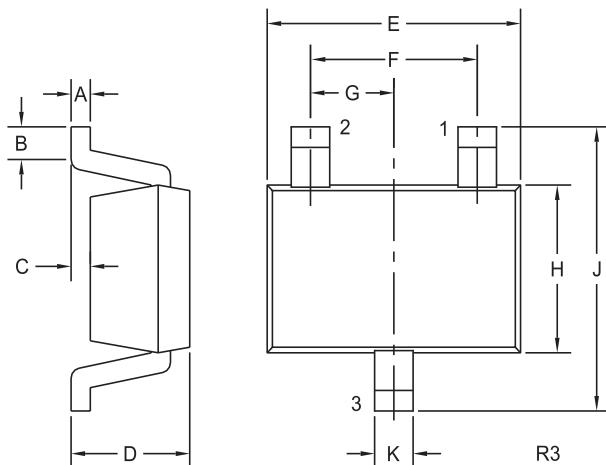
The CENTRAL SEMICONDUCTOR CMSDM3590 and CMSDM7590 are complementary N-Channel and P-Channel Enhancement-mode silicon MOSFETs designed for high speed pulsed amplifier and driver applications. These devices offer desirable MOSFET electrical characteristics in an economical industry standard SOT-323 package.

**MARKING CODES:** CMSDM3590: 35C  
CMSDM7590: 75C

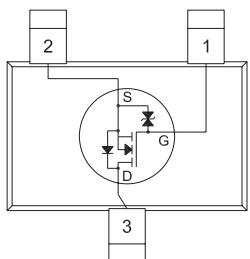
**FEATURES:**

- ESD Protection up to 2kV
- Power Dissipation: 275mW
- Low Threshold Voltage
- Logic Level Compatibility
- Small SOT-323 Surface Mount Package

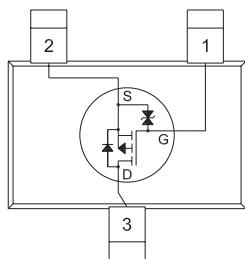
SOT-323 CASE - MECHANICAL OUTLINE



PIN CONFIGURATIONS



CMSDM3590



CMSDM7590

SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.002	0.008	0.05	0.20
B	0.004	-	0.10	-
C	-	0.004	-	0.10
D	0.031	0.043	0.80	1.10
E	0.071	0.087	1.80	2.20
F	0.051		1.30	
G	0.026		0.65	
H	0.045	0.053	1.15	1.35
J	0.079	0.087	2.00	2.20
K	0.008	0.016	0.20	0.40

SOT-323 (REV: R3)

LEAD CODE:

- 1) GATE
- 2) SOURCE
- 3) DRAIN

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R0 (19-May 2009)