

CPH3307

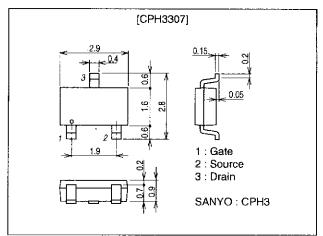
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

Package Dimensions

unit : mm 2152**A**



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-20	V
Gate-to-Source Voltage	VGSS		±10	>
Drain Current (DC)	ID		-3	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-12	A
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm²×0.8mm)	1.2	W
Channel Temperature	Tch		150	Ç
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Únit
			min	typ	max) Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-20			V
Zero-Gate Voltage Drain Current	DSS	V _{DS} =-20V, V _{GS} =0			-1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VDS=0			±10	μA
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-0.4		-1.4	٧
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-1.5A	3.5	5		S
Static Drain-to-Source On-State Resistance	Rps(on)1	Ip=-1.5A, VGS=-4V		70	90	mΩ
	R _{DS} (on)2	ID=-1A, VGS=-2.5V		95	130	mΩ

Marking: JG

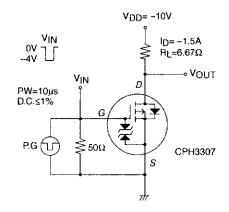
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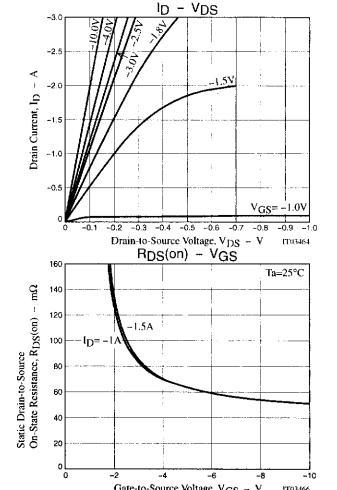
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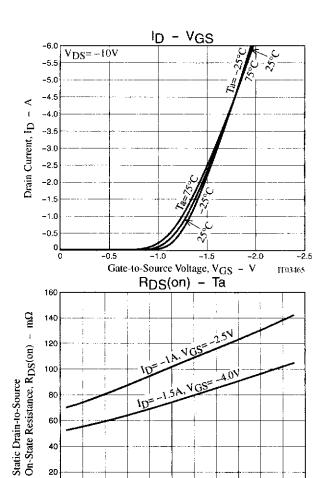
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	, Onit
Input Capacitance	Ciss	V _{DS} =-10V, f=1MHz		800		pF
Output Capacitance	Coss	V _{DS} =-10V, f=1MHz		160		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-10V, f=1MHz		100		pF
Turn-ON Delay Time	td(on)	See specified Test Circuit		14		ns
Rise Time	tr	See specified Test Circuit		50		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		56		ns
Fall Time	tf	See specified Test Circuit		51		ns
Total Gate Charge	Qg	V _{DS} =-10V, V _{GS} =-4V, I _D =-3A		7.8		nC
Gate-to-Source Charge	Qgs	V _{DS} =-10V, V _{GS} =-4V, † _D =-3A		1.7		пС
Gate-to-Drain "Miller" Charge	Qgd	VDS=-10V, VGS=-4V, ID=-3A		1.9	ļ	пC
Diode Forward Voltage	V _{SD}	IS=-3A, VGS=0		-0.8	-1.5	V

Switching Time Test Circuit

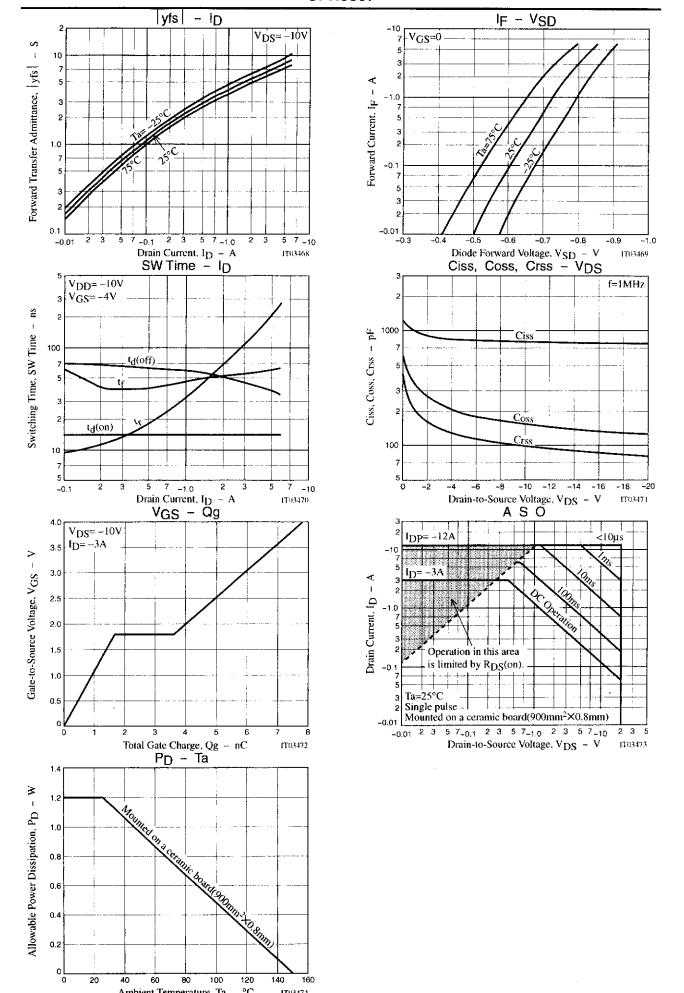






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-40 -20 0 20 40 60 80 100



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