

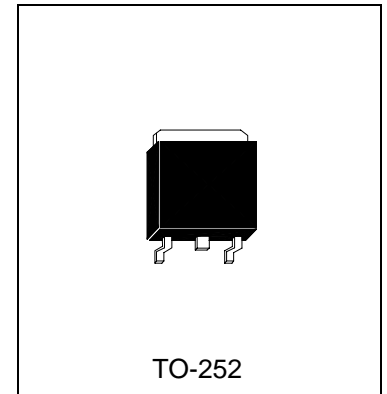


HJ882

NPN EPITAXIAL PLANAR TRANSISTOR

Description

The HJ882 is designed for using in output stage of 20 W audio amplifier, voltage regulator, DC-DC converter and relay driver.



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$)

- Maximum Temperatures
 - Storage Temperature -55 ~ +150 °C
 - Junction Temperature +150 °C
- Maximum Power Dissipation
 - Total Power Dissipation ($T_C=25^\circ\text{C}$) 10 W
- Maximum Voltages and Currents
 - BV_{CBO} Collector to Base Voltage 40 V
 - BV_{CEO} Collector to Emitter Voltage 30 V
 - BV_{EBO} Emitter to Base Voltage 5 V
 - I_C Collector Current (DC) 3 A
 - I_C Collector Current (Pulse) 7 A
 - I_B Base Current (DC) 600 mA

Thermal Characteristic

Characteristic	Symbol	Max	Unit
Thermal Resistance, junction to case	$R_{\theta jc}$	12.5	°C/W

Electrical Characteristics ($T_A=25^\circ\text{C}$)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV_{CBO}	40	-	-	V	$I_C=100\mu\text{A}, I_E=0$
BV_{CEO}	30	-	-	V	$I_C=1\text{mA}, I_B=0$
BV_{EBO}	5	-	-	V	$I_E=10\mu\text{A}, I_C=0$
I_{CBO}	-	-	1	μA	$V_{CB}=30\text{V}, I_E=0$
I_{EBO}	-	-	1	μA	$V_{EB}=3\text{V}, I_C=0$
$*V_{CE(sat)}$	-	-	0.5	V	$I_C=2\text{A}, I_B=0.2\text{A}$
$*V_{BE(sat)}$	-	1	2	V	$I_C=2\text{A}, I_B=0.2\text{A}$
$*h_{FE1}$	30	-	-		$V_{CE}=2\text{V}, I_C=20\text{mA}$
$*h_{FE2}$	100	-	500		$V_{CE}=2\text{V}, I_C=1\text{A}$
F=	-	90	-	MHz	$V_{CE}=5\text{V}, I_C=0.1\text{A}, f=100\text{MHz}$
Cob	-	45	-	pF	$V_{CB}=10\text{V}, f=1\text{MHz}$

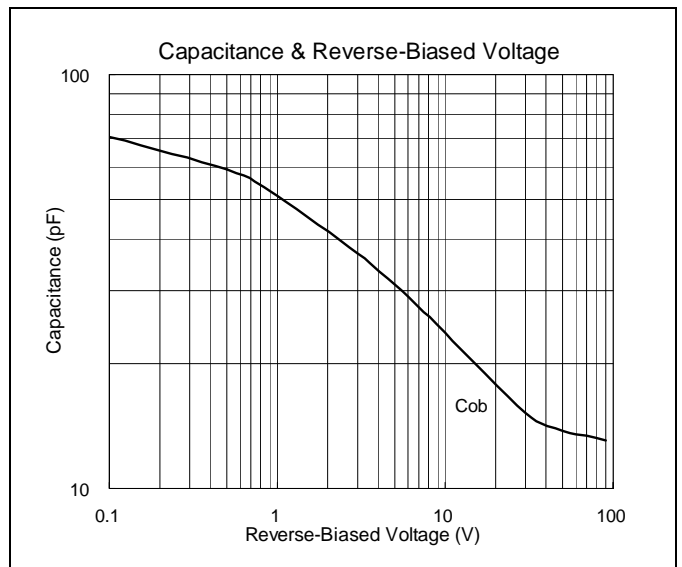
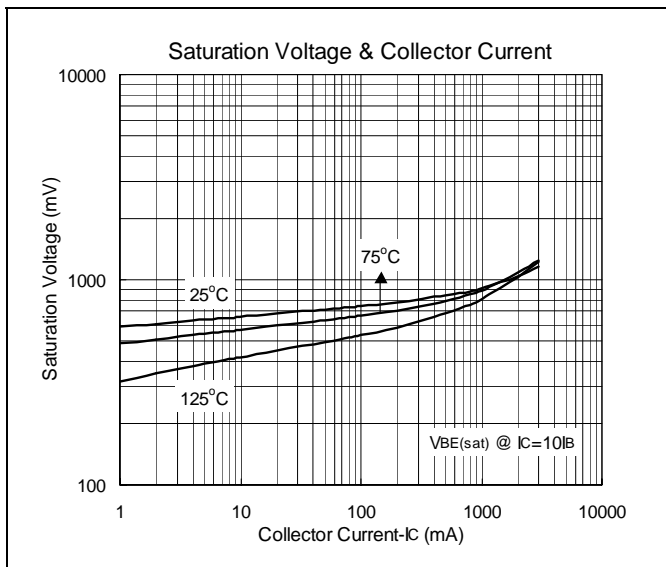
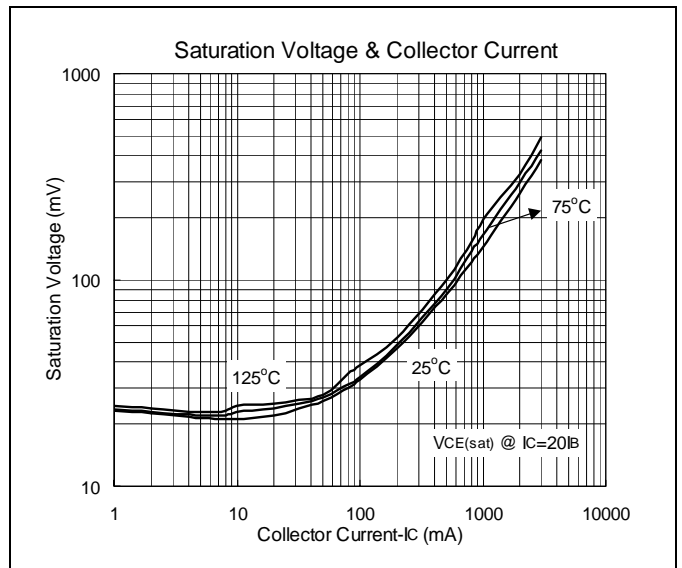
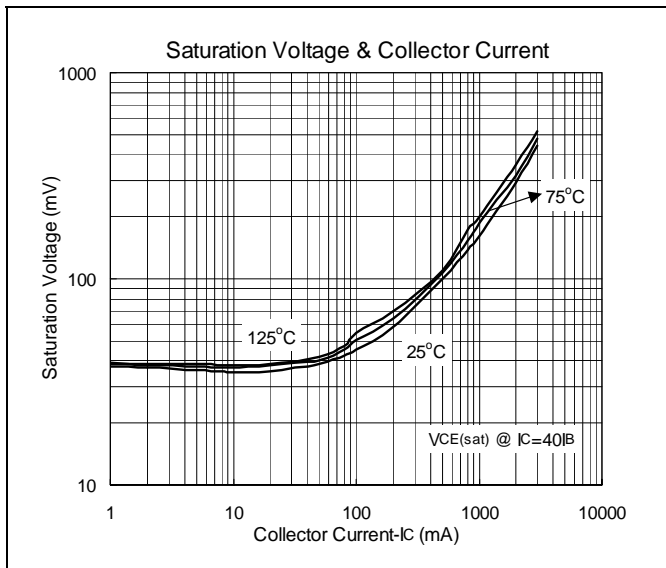
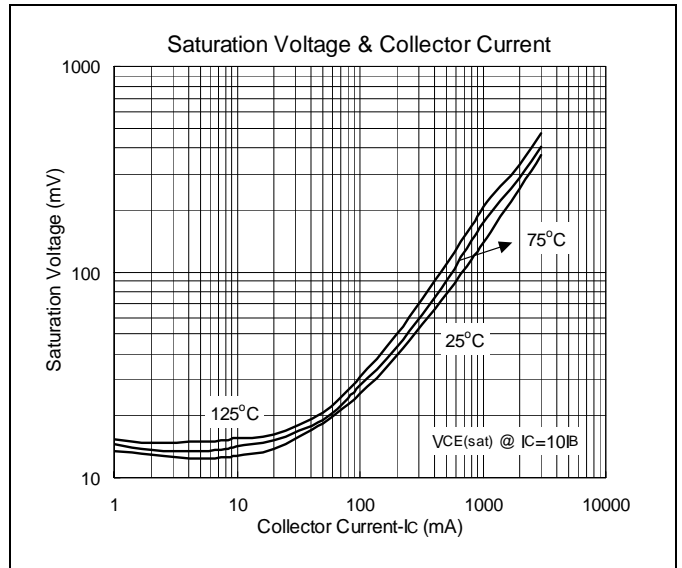
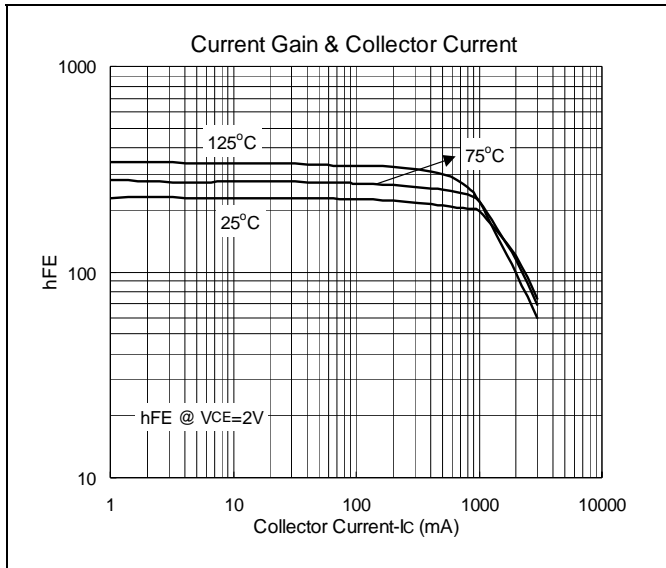
*Pulse Test: Pulse Width $\leq 380\mu\text{s}$, Duty Cycle $\leq 2\%$

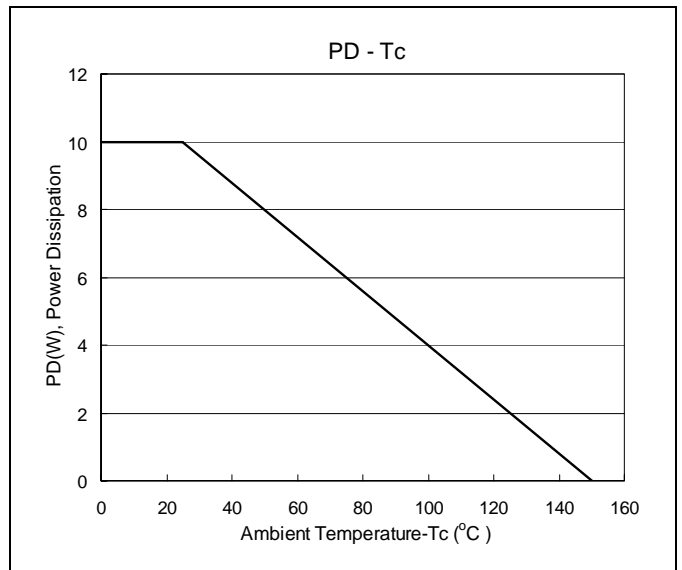
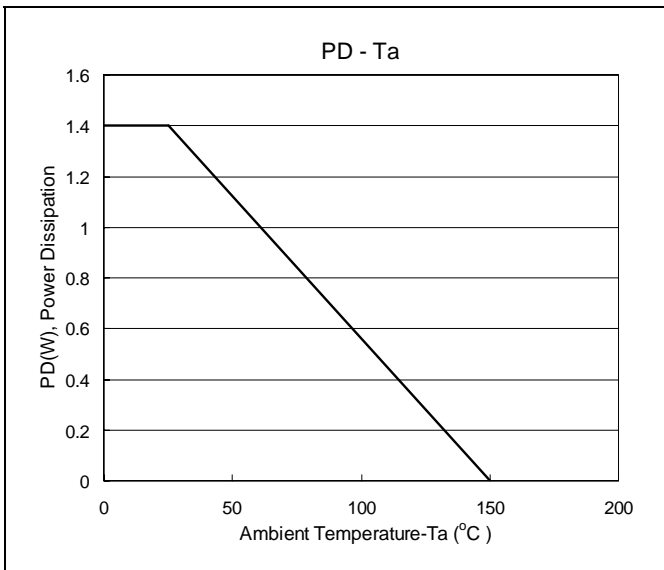
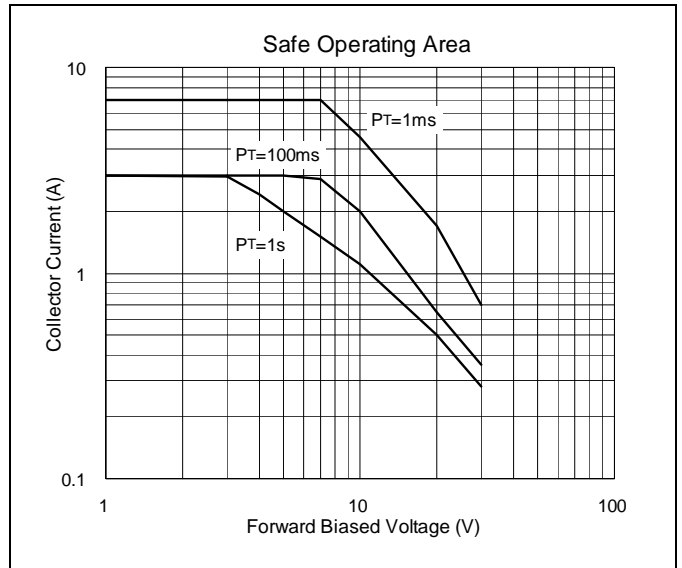
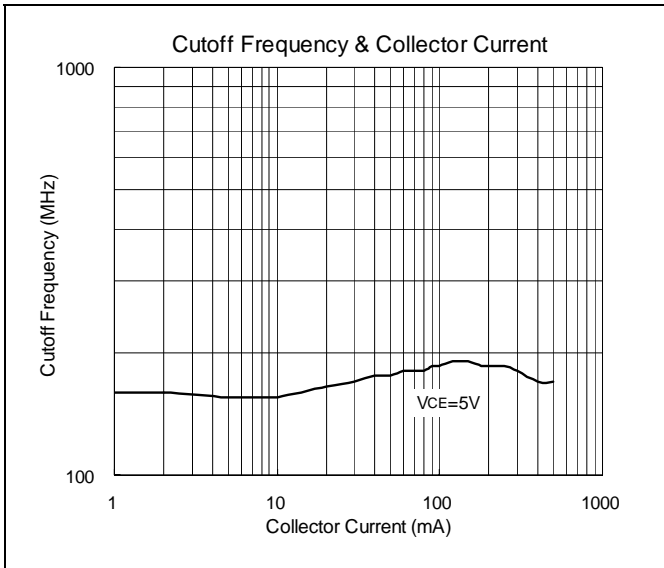
Classification Of h_{FE2}

Rank	Q	P	E
Range	100-200	160-320	250-500



Characteristics Curve







TO-252 Dimension

3-Lead TO-252 Plastic
Surface Mount Package
HSMC Package Code: J

Marking:

Pb Free Mark
 Pb-Free: "●" (Note)
 Normal: None

Date Code Control Code

Note: Green label is used for pb-free packing

Pin Style: 1.Base 2.Collector 3.Emitter

Material:

- Lead solder plating: Sn60/Pb40 (Normal), Sn/3.0Ag/0.5Cu or Pure-Tin (Pb-free)
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

DIM	Min.	Max.
A	6.35	6.80
C	4.80	5.50
F	1.30	1.70
G	5.40	6.25
H	2.20	3.00
L	0.40	0.90
M	2.20	2.40
N	0.90	1.50
a1	0.40	0.65
a2	-	*2.30
a5	0.65	1.05

*: Typical, Unit: mm

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- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

DIM	Min.	Max.
A	6.40	6.80
B	-	6.00
C	5.04	5.64
D	-	*4.34
E	0.40	0.80
F	0.50	0.90
G	5.90	6.30
H	2.50	2.90
I	9.20	9.80
J	0.60	1.00
K	-	0.96
L	0.66	0.86
M	2.20	2.40
N	0.70	1.10
O	0.82	1.22
a1	0.40	0.60
a2	2.10	2.50
y1	-	5°
y2	-	3°

*: Typical, Unit: mm

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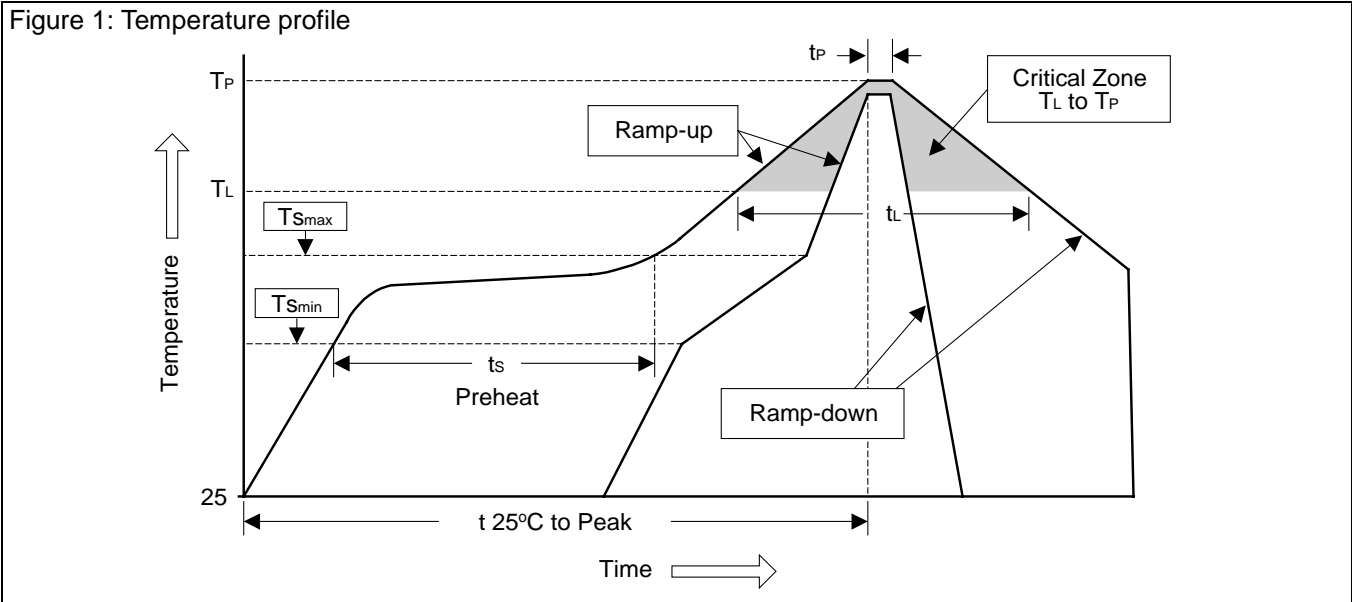
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 Tel: 886-3-5983621~5 Fax: 886-3-5982931



Soldering Methods for HSMC's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	$<3^\circ\text{C}/\text{sec}$	$<3^\circ\text{C}/\text{sec}$
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60~120 sec	60~180 sec
T_{Smax} to T_L		
- Ramp-up Rate	$<3^\circ\text{C}/\text{sec}$	$<3^\circ\text{C}/\text{sec}$
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60~150 sec	60~150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_p)	10~30 sec	20~40 sec
Ramp-down Rate	$<6^\circ\text{C}/\text{sec}$	$<6^\circ\text{C}/\text{sec}$
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec