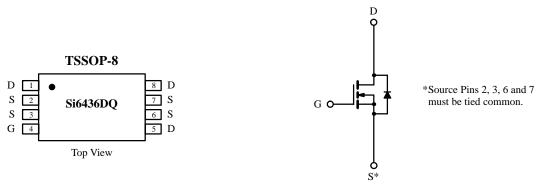
N-Channel 30-V (D-S) Rated MOSFET

Product Summary

V _{DS} (V)	$\mathbf{r_{DS(on)}}(\Omega)$	I _D (A)	
30	$0.045 @ V_{GS} = 10 V$	±4.4	
30	0.070 @ V _{GS} = 4.5 V	±3.5	



N-Channel MOSFET

Absolute Maximum Ratings ($T_A = 25^{\circ}C$ Unless Otherwise Noted)

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V_{DS}	30	37		
Gate-Source Voltage		V_{GS}	±20	V	
Continues Durin Comment (T. 1500 C)	$T_A = 25^{\circ}C$	I _D	±4.4	A	
Continuous Drain Current $(T_J = 150^{\circ}C)^a$	$T_A = 70^{\circ}C$		±3.5		
Pulsed Drain Current		I_{DM}	±30	^	
Continuous Source Current (Diode Conduction) ^a		I_{S}	1.7	1	
Maximum Power Dissipation ^a	$T_A = 25^{\circ}C$	P _D	1.5	w	
waxiiidiii I owei Dissipatioii-	$T_A = 70^{\circ}C$	1 D	1.0	**	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	–55 to 150	°C	

Thermal Resistance Ratings

Parameter	Symbol	Limit	Unit
Maximum Junction-to-Ambient ^a	R_{thJA}	83	°C/W

Notes

Updates to this data sheet may be obtained via facsimile by calling Siliconix FaxBack, 1-408-970-5600. Please request FaxBack document #70169. A SPICE Model data sheet is available for this product (FaxBack document #70535).

a. Surface Mounted on FR4 Board, $t \le 10$ sec.

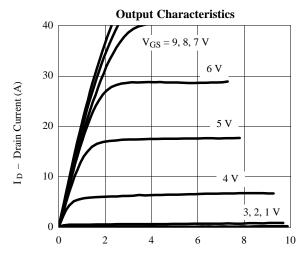
Si6436DQ

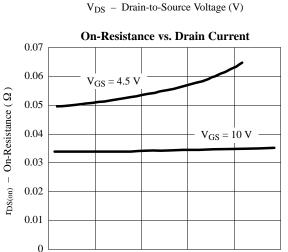
Specifications ($T_J = 25^{\circ}C$ Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static			•	•		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1			V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zone Cote Walterer Dunin Comment	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	1		1	μΑ
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			20	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			A
D'G OGA D' G	_	$V_{GS} = 10 \text{ V}, I_D = 4.4 \text{ A}$		0.034	0.045	Ω
Drain-Source On-State Resistance ^a	r _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 3.0 \text{ A}$		0.052	0.070	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 4.4 \text{ A}$		8		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.75	1.2	V
Dynamic ^b						
Total Gate Charge	Qg			13	30	
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, \ V_{GS} = 10 \text{ V}, \ I_D = 4.4 \text{ A}$		1.7		пC
Gate-Drain Charge	Q_{gd}			3.7		
Turn-On Delay Time	t _{d(on)}			12	30	
Rise Time	t _r	$V_{DD} = 15 \text{ V}, R_L = 15 \Omega$ $I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_G = 6 \Omega$		10	25	ns
Turn-Off Delay Time	t _{d(off)}			25	30	
Fall Time	t _f			10	50	
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 1.7 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		120	160	

<sup>a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.</sup>

Typical Characteristics (25°C Unless Otherwise Noted)





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I_D - Drain Current (A)

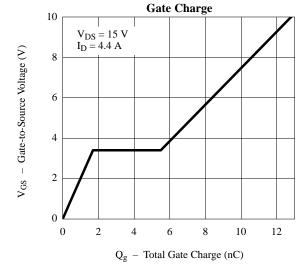
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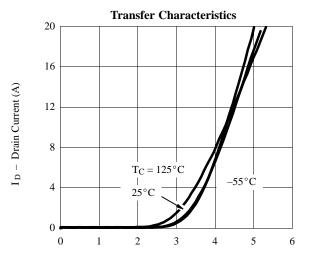
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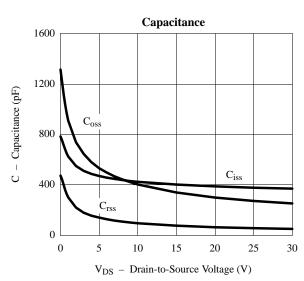
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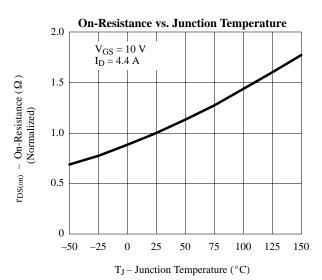
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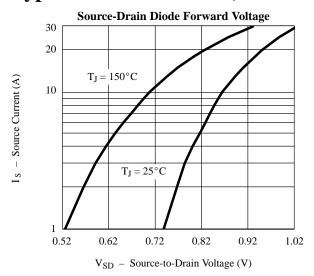


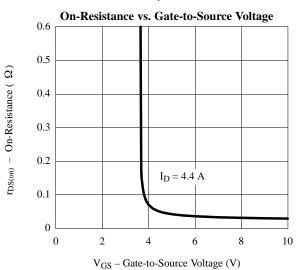
 V_{GS} – Gate-to-Source Voltage (V)

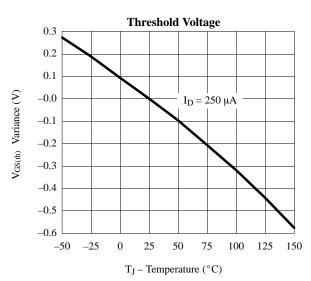


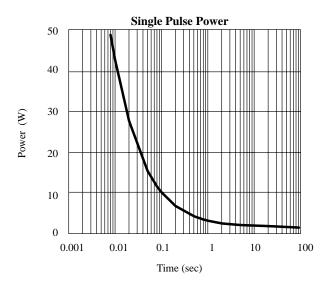


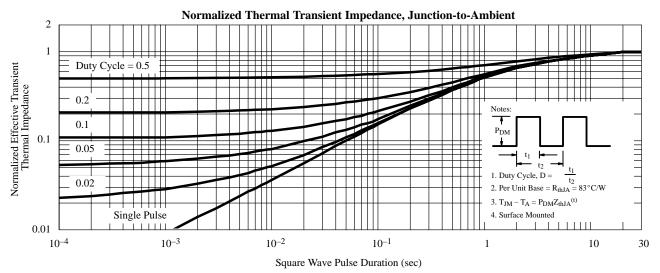
Typical Characteristics (25°C Unless Otherwise Noted)











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