

Low Power Operational Amplifiers — Single, Dual, Triple

**SG1250/SG2250/SG3250 — Single
SG1252/SG2252/SG3252 — Dual
SG1253/SG2253/SG3253 — Triple
SG4250/SG4250C — Single**

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DESCRIPTION

SG1250/1252/1253 operational amplifiers are single, dual, and triple operational amplifiers which have been designed to offer exceptional performance under conditions of extremely low internal power consumption. Quiescent current is determined by a single external resistor which permits operation over a wide range of currents and voltages.

FEATURES

- Adjustable power consumption to less than 20 microwatts
- Supply voltages from +0.75 to +18 volts
- Less than 15 nA bias currents
- Complete short-circuit protection
- Internally compensated

DESCRIPTION

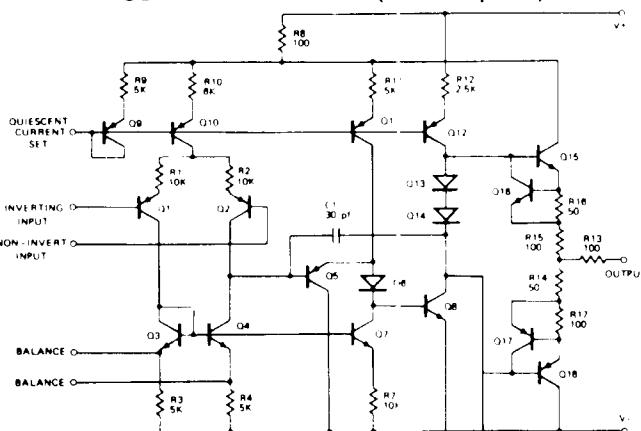
SG4250/4250C

The SG4250/4250C are intended for applications requiring extremely low internal power consumption. The device is pin compatible with the 741 type operational amplifiers and is an exact replacement for the industry standard 4250/4250C.

FEATURES

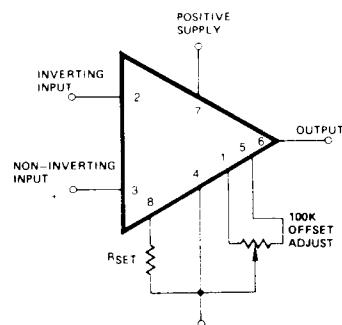
- +1V to +18V power supply operation
- W standby power consumption
- 5nA input bias current
- 35nV $\sqrt{\text{Hz}}$ input noise voltage (typ)
- Internally compensated

SCHEMATIC DIAGRAM (Each Amplifier)



NOTE: Balance adjust not available in triple op amp

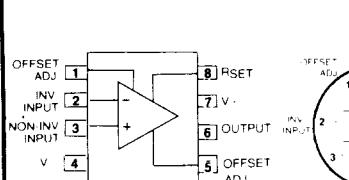
CONNECTION DIAGRAM



NOTE: RSET is required to establish the internal operating currents. Its value may be determined on the table given on page 2.

CONNECTION DIAGRAMS (Top Views)

SINGLE

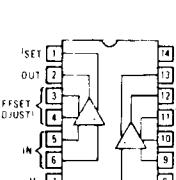


TOP

VIEW

1250/2250/3250/4250/4250C

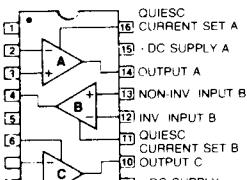
DUAL



TOP VIEW

1252/2252/3252

TRIPLE



TOP VIEW

1253/2253/3253

**SG1250/SG2250/SG3250 — Single
SG1252/SG2252/SG3252 — Dual
SG1253/SG2253/SG3253 — Triple
SG4250/SG4250C — Single**

ABSOLUTE MAXIMUM RATINGS

Supply Voltage	$\pm 1\text{ EV}$
Differential Input Voltage (Note 1)	$\pm 1\text{ EV}$
Common Mode Input Voltage (Note 2)	$\pm 1\text{ EV}$
Output Short Circuit Duration	Indefinite (Note 3)
Power Dissipation (Pkg. Limitation)	
T-Package	680mW
Derate above 25°C	5.4mW/°C
M-Package	400mW
Derate above 25°C	4.0mW/°C
Storage Temperature Range	
T,Y Package	-65°C to +150°C
M-Package	-55°C to +125°C

Note 1. This rating applies to maximum voltage differential between input terminals. The maximum input voltage on either input terminal is limited to supply voltage up to a limit of $\pm 15\text{ V}$.

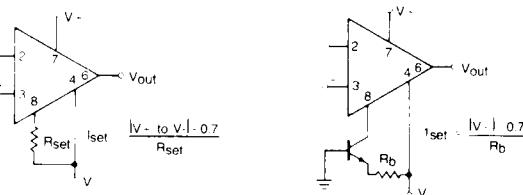
Note 2. This rating limited to \pm supply voltage to a maximum of $\pm 15\text{ V}$.

Note 3. With the output shorted to ground or either supply. Rating applies to -125°C case temperature or -75°C ambient temperature.

SETTING QUIESCENT CURRENT

RESISTOR BIASING				
VS	10 μA	30 μA	100 μA	300 μA
± 1.5	1.5M Ω	470K Ω	150K Ω	-
± 3	3.3M Ω	1.1M Ω	330K Ω	100K Ω
± 6	7.5M Ω	2.7M Ω	750K Ω	220K Ω
± 9	13M Ω	4M Ω	1.3M Ω	350K Ω
± 12	18M Ω	5.6M Ω	1.5M Ω	510K Ω
± 15	22M Ω	7.5M Ω	2.2M Ω	620K Ω

CURRENT SOURCE BIASING				
IQ	10 μA	30 μA	100 μA	300 μA
I _{set}	1.3 μA	4 μA	15 μA	50 μA



ELECTRICAL CHARACTERISTICS

PARAMETERS/CONDITIONS	1250/1252/ 1253 ¹	2250/2252/ 2253 ¹	3250/3252/ 3253 ¹	4250 ²	4250C ²	UNITS
Operating Temperature Range	-5 to +125	0 to +70	0 to +70	55 to +125	0 to +70	°C
Supply Voltage				± 18		
Differential Input Voltage ³				± 15		
Common Mode Range ³				± 15		
Package Types	T Y	T.Y.M	T.Y.M	T.Y	T.Y.M	
Input Offset Voltage	$R_S < 100\text{K}\Omega$ $R_S < 10\text{K}\Omega$	- 3(4)	- 3(4)	- 6.0(7.5)	3(4) -	mV
Input Bias Current	$V_S = \pm 3\text{V}$ $V_S = \pm 15\text{V}$	18(20) 12(15)	18(20) 12(15)	40(50) 25(30)	(15) ² 30(50) ²	nA
Input Offset Current		5(8)	5(8)	10(15)	(5) 10(15)	nA
Input Resistance	3	3	3	3	3	M
Large Signal Voltage Gain	$R_L = 10\text{K}$, $V_S = \pm 3\text{V}$ $R_L = 10\text{K}$, $V_S = \pm 15\text{V}$	40(25) 400(50)	40(25) 100(50)	40(25) 75(50)	100(50) ² 75(50) ²	V/mV
Output Voltage Swing	$V_S = \pm 3\text{V}$, $R_L = 10\text{K}\Omega$ $V_S = \pm 15\text{V}$, $R_L = 10\text{K}\Omega$		$\pm 1.5(\pm 1.0)$ $\pm 11(\pm 10)$		$\pm 11(\pm 10)$ ± 11	V
CMRR $R_S < 10\text{K}\Omega$		(70)	(70)	(70)	(70)	dB
PSRR $R_S < 10\text{K}\Omega$	$V_S = 3\text{V}$ $V_S = \pm 15\text{V}$	(200) (150)	(200) (150)	(200) (150)	(150) ² (150) ²	$\mu\text{V/V}$
Power Consumption	$V_S = \pm 3\text{V}$ $V_S = \pm 15\text{V}$, $R_L = 0$	(240) (1200)	(240) (1200)	(240) (1200)	(480) ² (600) ²	μW
Average TC of Offset Voltage	$R_S = 10\text{K}$, ($\pm 15\text{V}$ for 1250)	4(typ)	4(typ)	6(typ)	5(typ)	$\mu\text{V/}^{\circ}\text{C}$
Average TC of Offset Current	$R_S = 20\text{K}$, ($\pm 15\text{V}$ for 1250)	2(typ)	2(typ)	1(typ)	1.7(typ)	pA/°C
Equiv. Input Noise Voltage	f=10Hz ($\pm 15\text{V}$ for 1250)	35(typ)	35(typ)	35(typ)	35(typ)	nV/ $\sqrt{\text{Hz}}$
Equiv. Input Noise Current	f=10Hz ($\pm 15\text{V}$ for 1250)	0.5(typ)	0.5(typ)	0.5(typ)	0.5(typ)	pA/ $\sqrt{\text{Hz}}$
Slew Rate	$R_L = 10\text{K}$, $C_L = 100\text{pF}$	0.2(typ)	0.2(typ)	0.2(typ)	0.16(typ)	V/ μs
Small Signal Unity Gain-Bandwidth	$R_f = 0$ $V_{in} = 20\text{mV}$, $R_L = 20\text{K}\Omega$	-	-	-	250(typ)	kHz

Parameters for 1250/1252/1253 are min/max limits either at $T_A = 25^\circ\text{C}$ (if over operating temperature range if enclosed in parentheses), for supply voltage of -3V to +15V and for a quiescent current of 30 A established by an R_{set} of 1.

Parameters for 4250/4250C are min/max limits either at $T = 25^\circ\text{C}$ (for over operating temperature range if enclosed in parentheses), for supply voltage of -6 and quiescent current of 30.

Not to exceed either supply voltage.