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# HD74LVC02

Quad. 2-input NOR Gates

REJ03D0342-0300Z (Previous ADE-205-061B (Z)) Rev.3.00 Jul. 22, 2004

### **Description**

The HD74LVC02 has four 2-input NOR gates in a 14 pin package. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

#### **Features**

- $V_{CC} = 2.0 \text{ V to } 5.5 \text{ V}$
- All inputs  $V_{IH}$  (Max.) = 5.5 V (@ $V_{CC}$  = 0 V to 5.5 V)
- Typical  $V_{OL}$  ground bounce < 0.8 V (@ $V_{CC}$  = 3.3 V, Ta = 25°C)
- Typical  $V_{OH}$  undershoot > 2.0 V (@ $V_{CC}$  = 3.3 V, Ta = 25°C)
- High output current  $\pm 24$  mA (@V<sub>CC</sub> = 3.0 V to 5.5 V)
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LVC02FPEL	SOP-14 pin (JEITA)	FP-14DAV	FP	EL (2,000 pcs/reel)
HD74LVC02TELL	TSSOP-14 pin	TTP-14DV	T	ELL (2,000 pcs/reel)

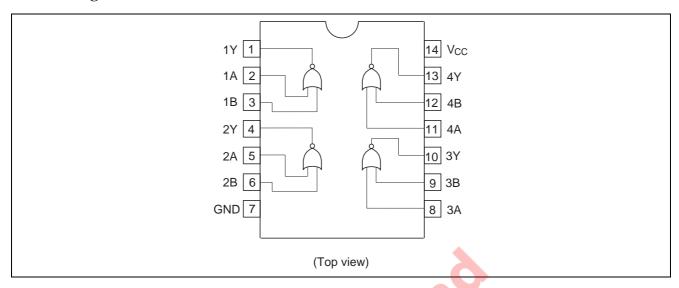
Note: Please consult the sales office for the above package availability.

#### **Function Table**

	inputs		
Α	В	Output Y	
L		Н	
L	Н	L	
Н	L	L	
Н	Н	L	

H: High level
L: Low level

### **Pin Arrangement**



### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	Vcc	-0.5 to 6.0	V	
Input diode current	I <sub>IK</sub>	-50	mA	V₁ = −0.5 V
Input voltage	VI	-0.5 to 6.0	٧	_
Output diode current	I <sub>OK</sub>	-50	mA	$V_{O} = -0.5 \text{ V}$
		50	_	$V_O = V_{CC} + 0.5 \text{ V}$
Output voltage	Vo	-0.5 to V <sub>CC</sub> +0.5	V	_
Output current	I <sub>O</sub>	±50	mA	
V <sub>CC</sub> , GND current / pin	I <sub>CC</sub> or I <sub>GND</sub>	100	mA	
Storage temperature	Tstg	-65 to +150	°C	_

Note: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

# **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	Vcc	1.5 to 5.5	V	Data retention
		2.0 to 5.5		At operation
Input / Output voltage	Vı	0 to 5.5	V	A, B
	Vo	0 to V <sub>CC</sub>		Y
Operating temperature	Та	-40 to 85	°C	
Output current	l <sub>OH</sub>	<b>–12</b>	mA	$V_{CC} = 2.7 \text{ V}$
		-24 <sup>*2</sup>		V <sub>CC</sub> = 3.0 V to 5.5 V
	I <sub>OL</sub>	12		V <sub>CC</sub> = 2.7 V
		24 <sup>*2</sup>		V <sub>CC</sub> = 3.0 V to 5.5 V
Input rise / fall time*1	t <sub>r</sub> , t <sub>f</sub>	10	ns/V	

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

2. Duty cycle ≤ 50%

### **Electrical Characteristics**

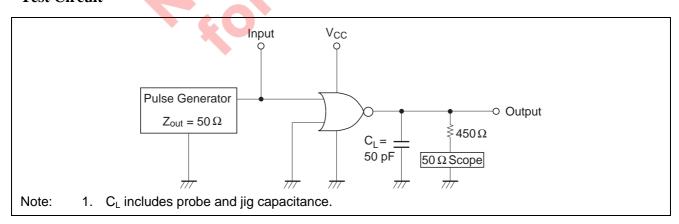
 $Ta = -40 \text{ to } 85^{\circ}\text{C}$ 

Item	Symbol	V <sub>cc</sub> (V)	Min	Max	Unit	Test Conditions
Input voltage	V <sub>IH</sub>	2.7 to 3.6	2.0	_	V	
		4.5 to 5.5	V <sub>CC</sub> ×0.7	_	_	
	V <sub>IL</sub>	2.7 to 3.6	_	0.8	_	
		4.5 to 5.5	_	V <sub>CC</sub> ×0.3	_	
Output voltage	V <sub>OH</sub>	2.7 to 5.5	V <sub>CC</sub> -0.2	_	V	I <sub>OH</sub> = -100 μA
		2.7	2.2	_	_	I <sub>OH</sub> = -12 mA
		3.0	2.4	_	_	I <sub>OH</sub> = -12 mA
		3.0	2.0	_	_	$I_{OH} = -24 \text{ mA}$
		4.5	3.8	_	_	$I_{OH} = -24 \text{ mA}$
	V <sub>OL</sub>	2.7 to 5.5	_	0.2	V	I <sub>OL</sub> = 100 μA
		2.7	_	0.4	_	I <sub>OL</sub> = 12 mA
		3.0	_	0.55	_	I <sub>OL</sub> = 24 mA
		4.5	_	0.55	_	I <sub>OL</sub> = 24 mA
Input current	I <sub>IN</sub>	0 to 5.5	_	±5.0	μΑ	V <sub>IN</sub> = 5.5 V or GND
Quiescent supply current	I <sub>CC</sub>	5.5	_	20	μA	V <sub>IN</sub> = V <sub>CC</sub> or GND
	$\Delta I_{CC}$	3.0 to 3.6	_	500	μА	$V_{IN}$ = one input at ( $V_{CC}$ -0.6)V, other inputs at $V_{CC}$ or GND

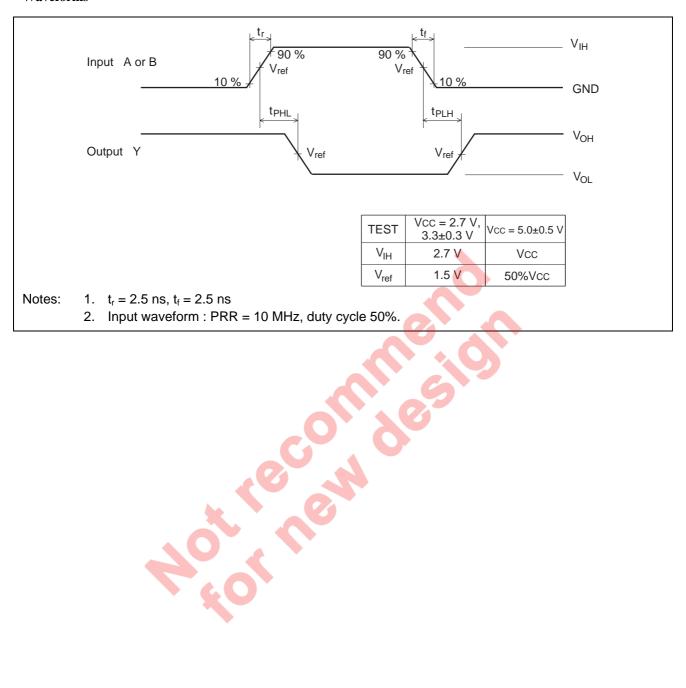
# **Switching Characteristics**

			T	a = -40 t	o 85°C		From	То
Item	Symbol	V <sub>CC</sub> (V)	Min	Тур	Max	Unit	(Input)	(Output)
Propagation delay time	t <sub>PLH</sub>	2.7	(+)	4.5	7.0	ns	A or B	Υ
	$t_{PHL}$	3.3±0.3	1.5	3.5	6.0			
		5.0±0.5		2.5	5.0			
Input capacitance	C <sub>IN</sub>	2.7		3.0	_	pF		
Output capacitance	Co	2.7		15.0	_	pF		

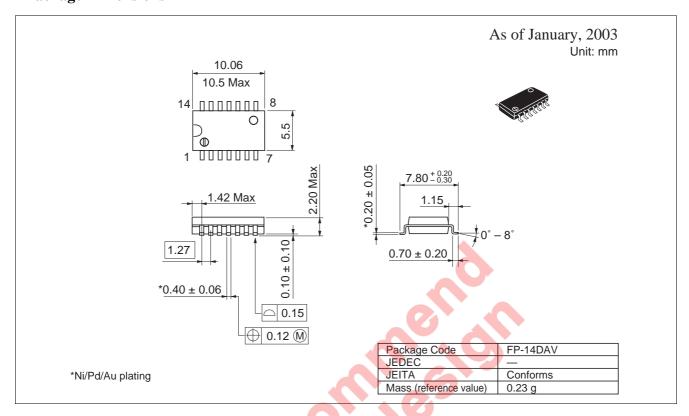
### **Test Circuit**

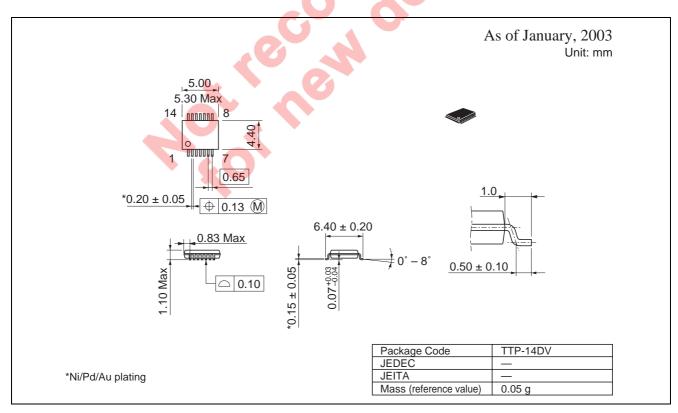


#### Waveforms



### **Package Dimensions**





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