

## EEPROM

### ●Serial 3-wire

Capacity (Bit)	Part No.	Organization (Word × Bit)	Power supply(V)	Operating voltage		Current consumption (Max)		Write cycle time(Max) (ms)	Operating temp (°C)	Rewritability (×10 <sup>4</sup> )	Data retention (Year)	Package	
				Read(V)	Write(V)	Active (mA)	Stand-by (μA)						
								V <sub>CC</sub>					
1K	BR93LL46F	64 × 16	2	1.8~4.0	1.8~4.0	2	1	25	2V	-40~85	10	10	SOP8
	BR93LC46/F		3	2.0~5.5	2.7~5.5	2	3	25	3V				
	BR93LC46A/AF		5	2.0~5.5	4.5~5.5	3	5	10	5V				
2K	BR93LL56/F	128 × 16	2	1.8~4.0	1.8~4.0	2	1	25	2V				
	BR93LC56/F		3	2.0~5.5	2.7~5.5	2	3	25	3V				
	BR93LC56A/AF		5	2.0~5.5	4.5~5.5	3	5	10	5V				
4K	BR93LC66/RF	256 × 16	3	2.0~5.5	2.7~5.5	2	3	25	3V				
	BR93LC66A/ARF		5	2.0~5.5	4.5~5.5	3	5	10	5V				

Note: ☆: Under development

### ●Serial 3-wire(Serial port direct connection)

Capacity (Bit)	Part No.	Organization (Word × Bit)	Power supply(V)	Operating voltage		Current consumption (Max)		Write cycle time(Max) (ms)	Operating temp (°C)	Rewritability (×10 <sup>4</sup> )	Data retention (Year)	Package	
				Read(V)	Write(V)	Active (mA)	Stand-by (μA)						
								V <sub>CC</sub>					
2K <small>New</small>	BR9020/F	128 × 16	3~5	2.0~5.5	2.7~5.5	1.5	2	15	3V	-40~85	10	10	DIP8 SOP8
						2	3	10	5V				
4K	BR9040/F	256 × 16	3~5	2.0~5.5	2.7~5.5	1.5	2	15	3V				
						2	3	10	5V				

### ●Serial 2-wire(IC<sup>2</sup>BUS compatible)

Capacity (Bit)	Part No.	Organization (Word × Bit)	Power supply(V)	Operating voltage		Current consumption (Max)		Write cycle time(Max) (ms)	Operating temp (°C)	Rewritability (×10 <sup>4</sup> )	Data retention (Year)	Package	
				Read(V)	Write(V)	Active (mA)	Stand-by (μA)						
								V <sub>CC</sub>					
1K	BR24C01A/AF	128 × 8	3~5	2.7~5.5	2.7~5.5	1	2	25	3V	-40~85	10	10	DIP8 SOP8
2K	BR24C02/F	256 × 8	3~5	2.7~5.5	2.7~5.5	1	2	10	5V				
								25	3V				
4K	BR24C04/F	512 × 8	3~5	2.7~5.5	2.7~5.5	1	2	10	5V				
								25	3V				

Note: 1. I<sup>2</sup>C BUS is a registered trademark of Philips

### ●Parallel(CMOS Type)

Capacity (Bit)	Part No.	Organization (Word × Bit)	Access time Max(ns)	Power supply(V)	Operating voltage		Current consumption (Max)		Operating temp (°C)	Rewritability (×10 <sup>4</sup> )	Data retention (Year)	Package
					Read(V)	Write(V)	Active (mA)	Stand-by (μA)				
									V <sub>CC</sub>			
16K	BR28C16A-150	2K × 8	150	5	4.5~5.5	4.5~5.5	30	0.1	0~70	1	10	DIP24

## SRAM

Capacity (Bit)	Part No.	Organization (Word × Bit)	Access time Max(ns)	Power supply(V)	Current consumption(Max)			Package
					Active(mA)	Stand-by(μA)		
						V <sub>CC</sub> = 5V	V <sub>CC</sub> = 3V	
16K	BR6216B-10LL	2K × 8	100	5	40	20	10	SK-DIP24(300mil)
64K <small>New</small>	BR6265A-10LL	8K × 8	100	5	40	50	20	DIP28 SOP-W28(450mil)
	BR6265AF-10LL					20	10	
	BR6265A-10SL					20	10	

## Memory backup IC

Part No.	Power supply(V)	Output voltage (backup)(V)	Current (backup)(μA)	Detect voltage (V)	Switching voltage (V)	Feature	Package
BA6129F	5	4.95	0.5	3.5	3.3	Outputs CS, CSB and reset signals below detect signal	SOP8
BA6162/F	5	4.95	0.5	4.2	3.3	Same as BA6129F but different detect voltage	DIP8/SOP8