

### FEATURES

- **SMALL PACKAGE STYLE:**  
2 NE688 Die in a 2 mm x 1.25 mm package
- **LOW NOISE FIGURE:**  
NF = 1.5 dB TYP at 2 GHz
- **HIGH GAIN BANDWIDTH:**  $f_T = 9$  GHz
- **HIGH COLLECTOR CURRENT:** 100 mA

### DESCRIPTION

The UPA814T is two NPN high frequency silicon epitaxial transistors encapsulated in an ultra small 6 pin SMT package. Each transistor is independently mounted and easily configured for either dual transistor or cascode operation. The high  $f_T$ , low voltage bias and small size make this device suited for various hand-held wireless applications.

### ABSOLUTE MAXIMUM RATINGS<sup>1</sup> ( $T_A = 25^\circ\text{C}$ )

SYMBOLS	PARAMETERS	UNITS	RATINGS
V <sub>CB0</sub>	Collector to Base Voltage	V	9
V <sub>CEO</sub>	Collector to Emitter Voltage	V	6
V <sub>EBO</sub>	Emitter to Base Voltage	V	2
I <sub>c</sub>	Collector Current	mA	100
P <sub>T</sub>	Total Power Dissipation 1 Die 2 Die	mW mW	110 200
T <sub>J</sub>	Junction Temperature	°C	150
T <sub>STG</sub>	Storage Temperature	°C	-65 to +150

Note: 1. Operation in excess of any one of these parameters may result in permanent damage.

### ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ )

PART NUMBER PACKAGE OUTLINE			UPA814T S06		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
I <sub>CBO</sub>	Collector Cutoff Current at V <sub>CB</sub> = 5V, I <sub>E</sub> = 0	μA			0.1
I <sub>EBO</sub>	Emitter Cutoff Current at V <sub>EB</sub> = 1V, I <sub>C</sub> = 0	μA			0.1
h <sub>FE</sub> <sup>1</sup>	Forward Current Gain at V <sub>CE</sub> = 1V, I <sub>C</sub> = 3 mA		80	110	160
f <sub>T</sub>	Gain Bandwidth at V <sub>CE</sub> = 3V, I <sub>C</sub> = 20 mA, f = 2 GHz	GHz		9.0	
C <sub>re</sub> <sup>2</sup>	Feedback Capacitance at V <sub>CB</sub> = 1V, I <sub>E</sub> = 0, f = 1 MHz	pF		0.75	0.85
S <sub>21E</sub>   <sup>2</sup>	Insertion Power Gain at V <sub>CE</sub> = 3V, I <sub>C</sub> = 20 mA, f = 2 GHz	dB		6.5	
NF	Noise Figure at V <sub>CE</sub> = 3V, I <sub>C</sub> = 7 mA, f = 2 GHz	dB		1.5	
h <sub>FE1</sub> /h <sub>FE2</sub>	h <sub>FE</sub> Ratio: h <sub>FE1</sub> = Smaller Value of Q <sub>1</sub> , or Q <sub>2</sub> h <sub>FE2</sub> = Larger Value of Q <sub>1</sub> or Q <sub>2</sub>		0.85		

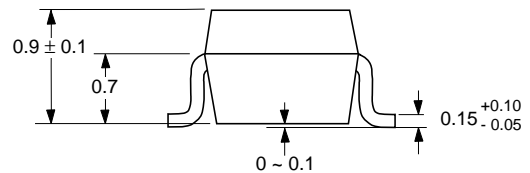
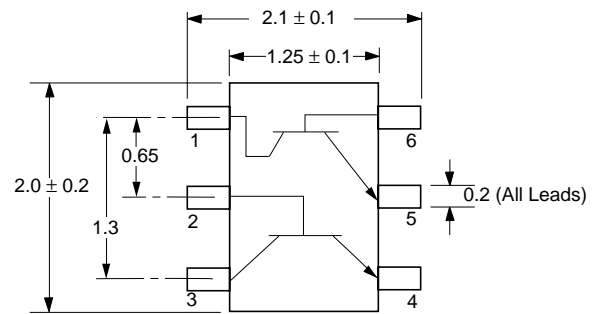
Notes: 1. Pulsed measurement, pulse width ≤ 350 μs, duty cycle ≤ 2 %.

2. The emitter terminal should be connected to the ground terminal of the 3 terminal capacitance bridge.

For Tape and Reel version use part number UPA814T-T1, 3K per reel.

### OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE S06



#### PIN OUT

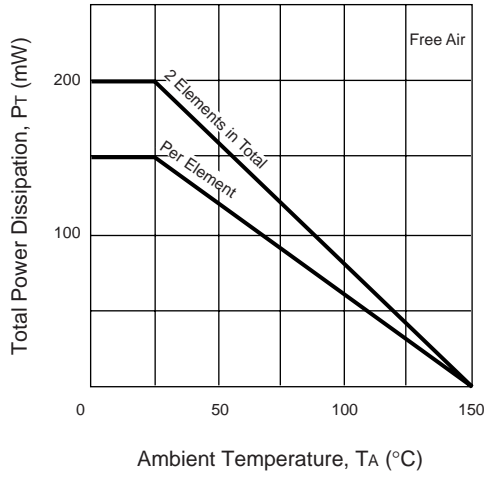
1. Collector Transistor 1
2. Base Transistor 2
3. Collector Transistor 2
4. Emitter Transistor 2
5. Emitter Transistor 1
6. Base Transistor 1

Note:

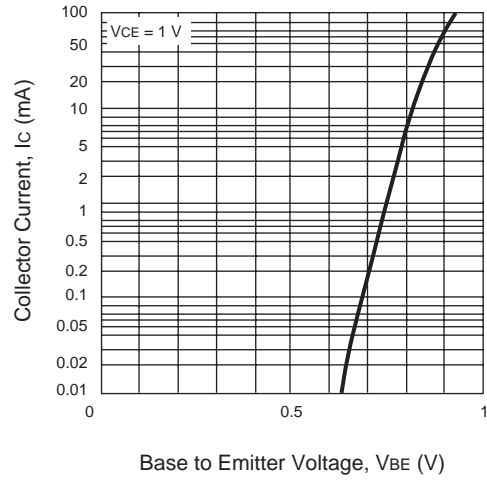
Pin 3 is identified with a circle on the bottom of the package.

TYPICAL PERFORMANCE CURVES (T<sub>A</sub> = 25°C)

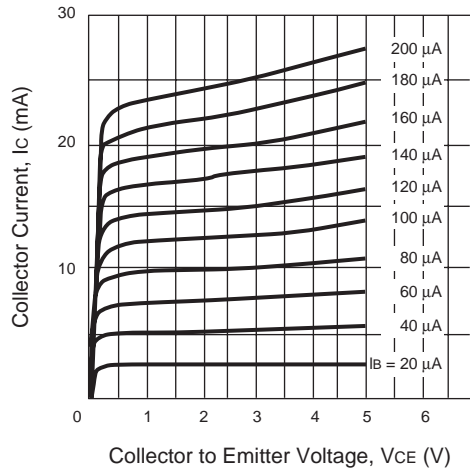
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



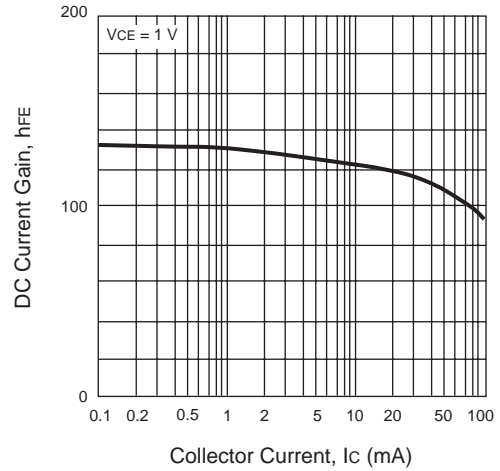
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



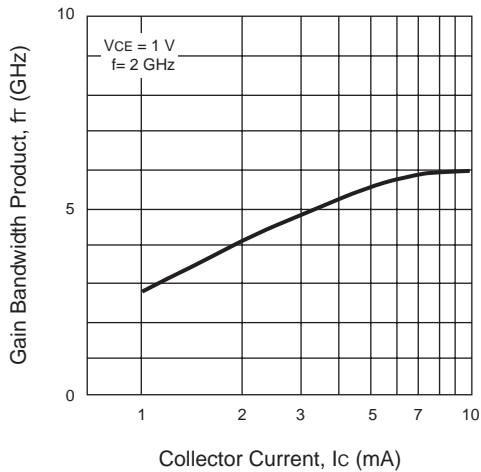
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



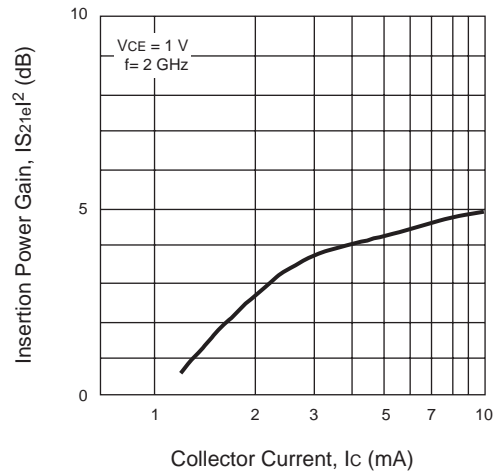
DC CURRENT GAIN vs. COLLECTOR CURRENT



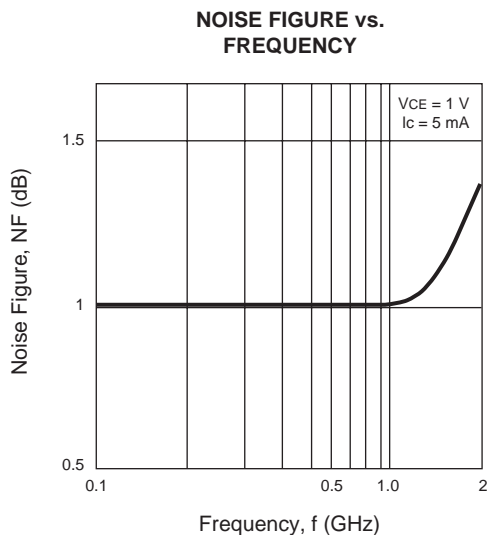
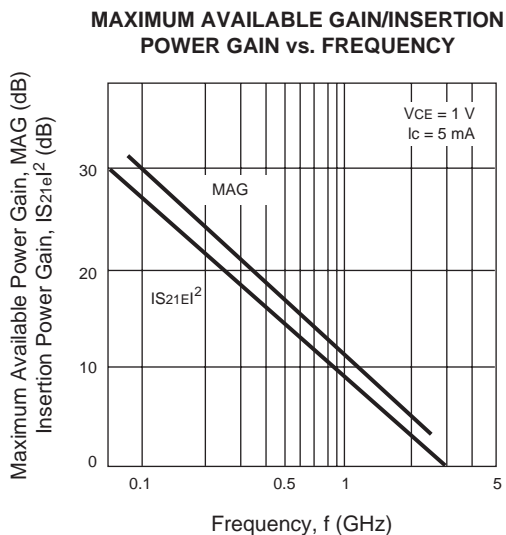
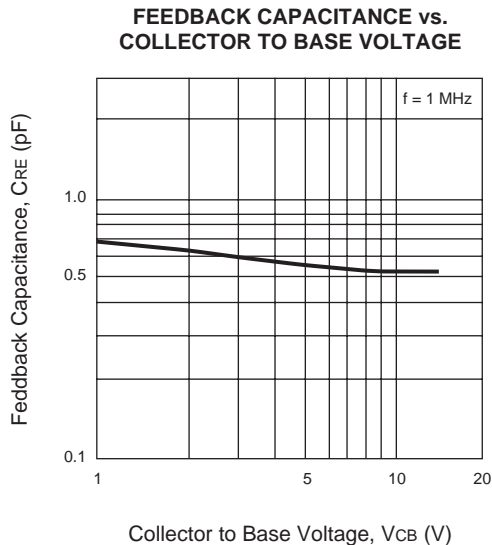
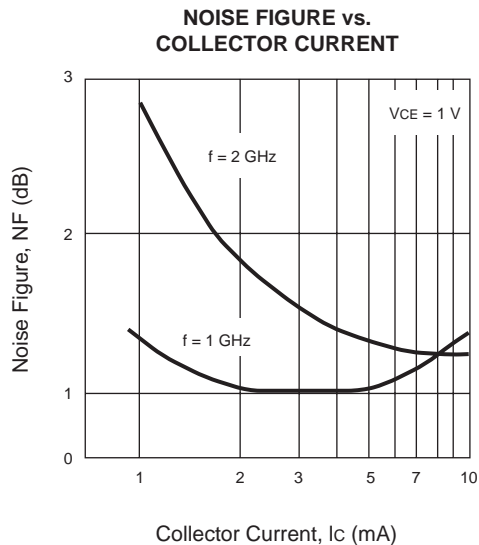
GAIN BANDWIDTH PRODUCT vs. COLLECTOR CURRENT



INSERTION POWER GAIN vs. COLLECTOR CURRENT



**TYPICAL PERFORMANCE CURVES** ( $T_A = 25^\circ\text{C}$ )



**ORDERING INFORMATION**

PART NUMBER	QUANTITY	PACKAGING
UPA814T-T1	3000	Tape & Reel

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