



New Product

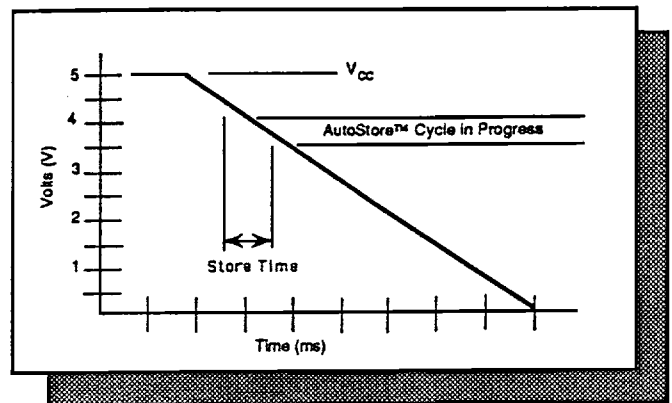
What a Memory!!!

STK15C88

32K x 8 AutoStore™ nvSRAM

FEATURES

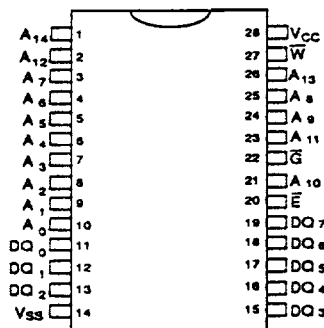
- 25, 35 and 45ns Access Times
- 28 pin 600-mil DIP package (standard SRAM pinout)
- Unlimited Reads and Writes to SRAM
- 10 year Data Retention in EEPROM
- 100,000 Store Cycles to EEPROM
- Store to EEPROM Initiated by AutoStore™ on Power-down
- Recall to SRAM Initiated by Power-up Recall
- Low Active Power
- Write Protect Below 4.3V
- Commercial and Industrial Temperatures



The STK15C88 automatically stores data to EEPROM cells when system power level falls below 4.3V.

The AutoStore™ feature of the STK15C88 32K x 8 high speed nvSRAM prevents loss of important data during power-down periods without requiring additional circuitry or components. When the system voltage falls below 4.3 Volts, the STK15C88 will automatically capture the data currently in its SRAM and store it in EEPROM. The Store operation is complete in a maximum of 10ms. Once the data is stored in this manner *it cannot be forgotten*. Data retention is guaranteed to be in excess of 10 years, even at high temperatures. On power-up, the device will recall data from the EEPROM into the SRAM. In this way, any data that is written into the STK15C88 will remain valid until altered by the system, regardless of the status of the system supply voltage. The STK15C88's fast access time (25ns) allows it to be interfaced directly to high speed buses, eliminating the need for external buffering circuitry or wait states. Initiation of Store and Recall cycles can also be controlled by entering software sequences on the SRAM inputs.

PIN CONFIGURATIONS



28 - 600 PDIP

STK15C88 PIN NAMES

$A_0 - A_{14}$	Address Inputs
\bar{W}	Write Enable
$DQ_0 - DQ_7$	Data In/Out
\bar{E}	Chip Enable
\bar{G}	Output Enable
V_{cc}	Power (+5V)
V_{ss}	Ground

*SIMT S003X