AN6500, AN6500S, AN6501

Built-in Reference Voltage Operational Amplifiers

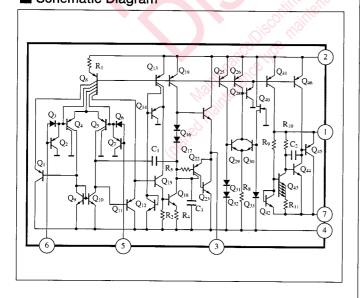
Overview

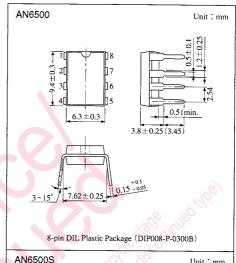
The AN6500, the AN6500S, and the AN6501 are highperformance operational amplifiers with reference voltage built-in, allowing single power supply voltage operation and wide application with reference voltage.

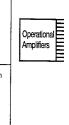
Features

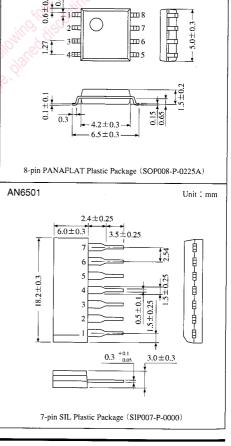
- Wide range of operating voltage: 3 to 24V
- · Single power supply voltage operation
- Large output current : $I_0 = +120 \text{mA}$ typ. -110 mA typ.
- Low reference voltage: V_{REF}=1.33V typ.
- Easy to compose variable regulator with reference voltage
- 3 types of packages are available
- Little cross-over distortion in operational amplifier circuit

Schematic Diagram









■ Pin Descriptions

Pin No.	Pin name		
1	Ref. voltage(+)		
2	Supply voltage		
3	OP. amp. output		
4	GND		
5	OP. amp. input(+)		
6	OP. amp. input(-)		
7	Ref. voltage(-)		
8	NC		

■ Absolute Maximum Ratings (Ta=25°C)

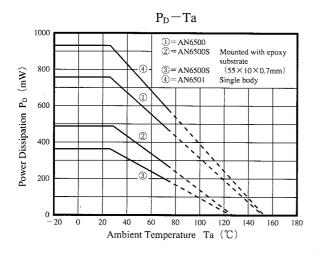
Parameter		Symbol	Rating	Unit
Supply voltage		V _{cc}	24	V
Supply current		I _{CC}	160	mA
Reference voltage outflow current		(V _{REF}) - I * !	-100	μA
Reference voltage inflow current		(V _{REF}) + I *2	500	μΑ
Common-mode input voltage range		V _{ICM}	-0.3 to +24	V O
Differential input voltage		V _{ID}	24	N A
Output sink current		V _{SINK}	150	mA
Power dissipation	AN6500		750	mW
	AN6500S	P _D *3	360	mW
	AN6501		925	mW
Operating ambient temperature		Topr	-20 to +75	$^{\circ}$
Storage temperature	AN6500, AN6501	т	-55 to +150	$^{\circ}$
	AN6500S	T _{stg}	-40 to + 125	$^{\circ}$

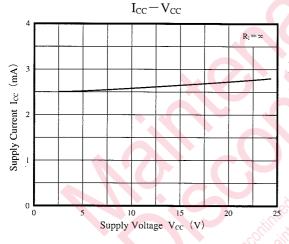
^{*1} Current flowed out from Pin①. *2 Current flowed into Pin①. *3 When enlarging output current, watch power consumption.

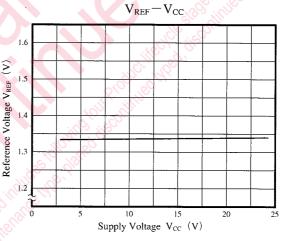
■ Electrical Characteristics $(V_{CC} = 5V, T_a = 25 \% \pm 2 \%)$

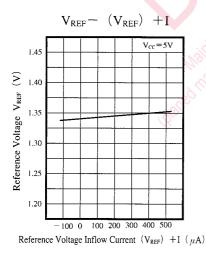
Parameter	Symbol	Condition	min	typ	max	Unit
Reference voltage	V _{REF}	fill rolls	1.25	1.33	1.45	V
Reference voltage temperature variation characteristics	⊿V _{REF} /Ta	Ta=0 to 50℃		-30		ppm/℃
Input offset voltage	V _{I (offset)}	$R_S = 50 \Omega$	_	2	7	mV
Input bias current	I_{Bias}		_	100	500	nA
Input offset current	I_{l0}		_	5	300	nA
Common-mode input voltage range	V _{CM}		_		3.5	V
Supply current	I_{CC}	$R_L = \infty$		2.5	3.5	mA
Voltage gain	G_V	$R_L \ge 2k\Omega$	80	108		dB
Maximum output voltage (1)	$V_{O(max)1}$	$R_L \ge 2k\Omega$	3.5			V
Maximum output voltage (2)	$V_{O(max)2}$	$V_{CC}=5V$, $I_O=70$ mA	3	4.1		V
Common-mode rejection ratio	CMR		_	85		dB
Supply voltage rejection ratio	SVR		I —	90		dB
Output source current	I _O (source)	$V_{IN}^{+} = 1V, V_{IN}^{-} = 0V$	70	110		mA
Output sink current	I _{SINK}	$V_{IN}^{+} = 0V, V_{IN}^{-} = 1V$	70	120		mA
Zero-cross frequency	f (T)			1		MHz

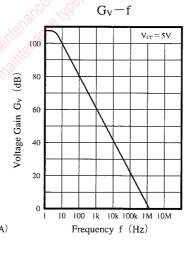
■ Characteristics Curve

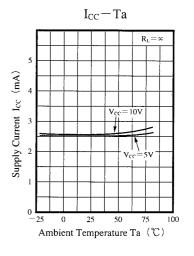












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