

# MGFC45V5053

**PRELIMINARY**

Notice: This is not a final specification.  
Some parametric limits are subject to change.

## 5.05~5.25GHz BAND 30W INTERNALLY MATCHED GaAs FET

### DESCRIPTION

The MGFC45V5053 is an internally impedance-matched GaAs power FET especially designed for use in 5.05~5.25 GHz band amplifiers. The hermetically sealed metal-ceramic package guarantees high reliability.

### FEATURES

- Class A operation
- Internally matched to 50Ω system
- High output power  
 $P_{1dB} = 30W(TYP) @ 5.05 \sim 5.25GHz$
- High power gain  
 $G_{LP} = 10 \text{ dB (TYP) } @ 5.05 \sim 5.25GHz$
- High power added efficiency  
 $\eta_{add} = 40\%(TYP) @ 5.05 \sim 5.25GHz, P_{1dB}$
- Hermetically sealed metal-ceramic package
- Low distortion (Item: -51)  
 $IM3 = -45dBc (TYP) @ P_o = 34.5(dBm) \text{ S.C.L.}$

### APPLICATION

- Item-01: 5.05~5.25GHz band power amplifiers.
- Item-51: Digital radio communication

### QUALITY GRADE

- IG

### ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Symbol	Parameter	Rating	Unit
V <sub>GD0</sub>	Gate to drain voltage	-15	V
V <sub>GS0</sub>	Gate to source voltage	-15	V
I <sub>D</sub>	Drain current	20	A
I <sub>GR</sub>	Reverse gate current	-60	mA
I <sub>GF</sub>	Forward gate current	126	mA
P <sub>T</sub>	Total power dissipation *1	93	W
T <sub>ch</sub>	Channel temperature	175	°C
T <sub>stg</sub>	Storage temperature	-65 ~ +175	°C

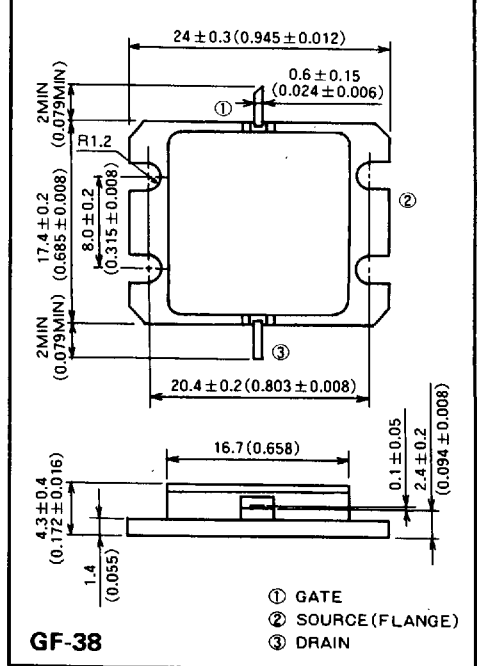
\*1: T<sub>c</sub> = 25°C

### ELECTRICAL CHARACTERISTICS (Ta=25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
I <sub>DSS</sub>	Saturated drain current	V <sub>DS</sub> =3V, V <sub>GS</sub> =0V	—	18	—	A
g <sub>m</sub>	Transconductance	V <sub>DS</sub> =3V, I <sub>D</sub> =6.2A	—	6.5	—	S
V <sub>GS(off)</sub>	Gate to source cut-off voltage	V <sub>DS</sub> =3V, I <sub>D</sub> =120mA	-2	—	-5	V
P <sub>1dB</sub>	Output power at 1dB gain compression	V <sub>DS</sub> =10V, I <sub>D</sub> =6.2A, f=5.05~5.25GHz	44	44.7	—	dBm
G <sub>LP</sub>	Linear power gain		9	10	—	dB
η <sub>add</sub>	Power added efficiency		—	40	—	%
IM <sub>3</sub>	3rd order IM distortion *1		-42	-45	—	dBc
R <sub>th(ch-c)</sub>	Thermal resistance *2	ΔV <sub>f</sub> method	—	—	1.6	°C/W

\*1: Item-51, 2-tone test P<sub>o</sub>=34.5dBm Single Carrier Level f=5.25 Δf=5MHz. \*2: Channel to case

### OUTLINE DRAWING Unit: millimeters (inches)



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### RECOMMENDED BIAS CONDITIONS

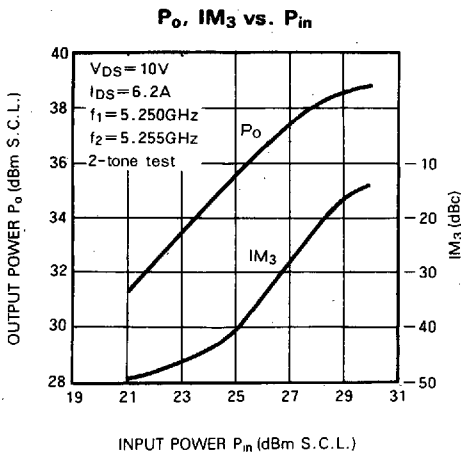
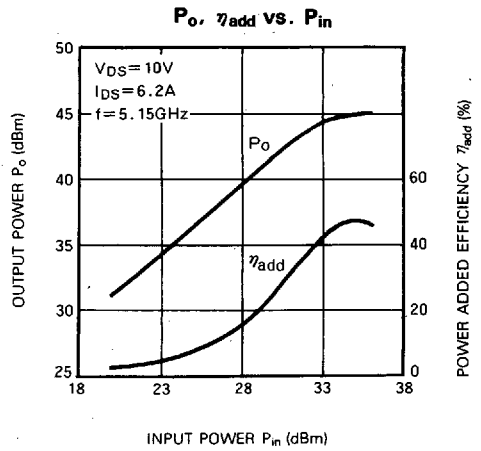
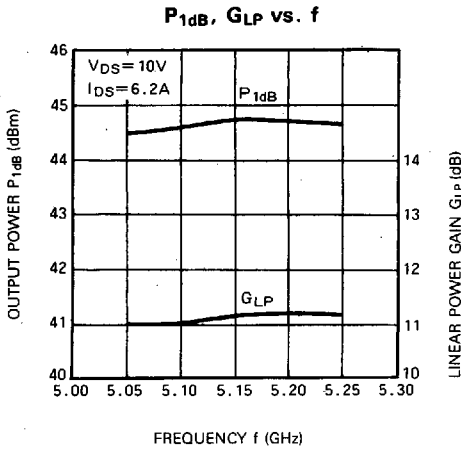
- V<sub>DS</sub> = 10V
- I<sub>D</sub> = 6.2A
- R<sub>g</sub> = 25Ω
- Refer to Bias Procedure

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**TYPICAL CHARACTERISTICS (Ta=25°C)**



**S PARAMETERS (Ta=25°C, V<sub>DS</sub>=10V, I<sub>DS</sub>=6.2A)**

f (GHz)	S Parameters (TYP.)							
	S <sub>11</sub>		S <sub>21</sub>		S <sub>12</sub>		S <sub>22</sub>	
	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)
5.05	0.49	36	3.48	- 72	0.054	- 131	0.17	- 148
5.10	0.45	24	3.48	- 83	0.056	- 139	0.16	- 168
5.15	0.41	10	3.47	- 94	0.061	- 149	0.17	- 180
5.20	0.36	- 6	3.46	- 105	0.063	- 163	0.17	164
5.25	0.33	- 26	3.43	- 116	0.061	- 172	0.16	148