ON Semiconductor

Is Now



To learn more about onsemi™, please visit our website at www.onsemi.com

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application,

POWERTAP II Switch-mode Power Rectifier

This state-of-the-art Switch-mode Power Rectifier uses the Schottky Barrier principle with a platinum barrier metal.

Features

- Dual Diode Construction; May Be Paralleled for Higher Current Output
- Guard-Ring for Stress Protection
- Low Forward Voltage Drop
- 150°C Operating Junction Temperature
- Recyclable Epoxy
- Guaranteed Reverse Avalanche Energy Capability
- Improved Mechanical Ratings
- Pb-Free Package is Available*

Mechanical Characteristics

- Case: Epoxy, Molded with Metal Heatsink Base
- Weight: 80 grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant
- Top Terminal Torque: 25-40 lb-in Max
- Base Plate Torques: See Procedure Given in the Package Outline Section

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	35	V
Average Rectified Forward Current (At Rated V _R , T _C = 100°C) Per Leg Per Device	I _{F(AV)}	300 600	Α
Peak Repetitive Forward Current, (At Rated V_R , Square Wave, 20 kHz, $T_C = 100^{\circ}C$)	I _{FRM}	300	Α
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	4000	Α
Peak Repetitive Reverse Surge Current (2.0 μs, 1.0 kHz)	I _{RRM}	2.0	Α
Storage Temperature Range	T _{stg}	-55 to +150	°C
Operating Junction Temperature	TJ	-55 to +150	°C
Voltage Rate of Change (Rated V _R)	dv/dt	10,000	V/μs



ON Semiconductor®

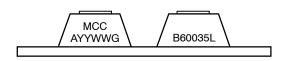
www.onsemi.com

LOW V_F SCHOTTKY
BARRIER RECTIFIER
600 AMPERES, 35 VOLTS





MARKING DIAGRAM



B60035L = Specific Device Code
MCC = Mold Compound Code
A = Assembly Location
YY = Year

WW = Work Week
G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MBRP60035CTL	POWERTAP II	25 Units/Tray
MBRP60035CTLG	POWERTAP II (Pb-Free)	25 Units/Tray

Preferred devices are recommended choices for future use and best overall value.

THERMAL CHARACTERISTICS

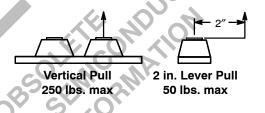
Rating	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	0.4	°C/W
ELECTRICAL CHARACTERISTICS			
Maximum Instantaneous Forward Voltage (Note 1) (iF = 300 Amps, $T_C = +25^{\circ}C$) (iF = 300 Amps, $T_C = +100^{\circ}C$)	V _F	0.57 0.50	V
Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_C = +25^{\circ}C$) (Rated dc Voltage, $T_C = +100^{\circ}C$)	I _R	3.0 250	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

MAXIMUM MECHANICAL RATINGS

Terminal Penetration:	0.235 max	
Terminal Torque:	25-40 in-lb max	
Mounting Torque — Outside Holes:	30-40 in-lb max	
Mounting Torque — Center Hole:	8–10 in-lb max	
Seating Plane Flatness	1 mil per in. (between mounting holes)	

POWERTAP MECHANICAL DATA APPLIES OVER OPERATING TEMPERATURE



Note: While the POWERTAP is capable of sustaining these vertical and levered tensions, the intimate contact between POWERTAP and heat sink may be lost. This could lead to thermal runaway. The use of very flexible leads is recommended for the anode connections. Use of thermal grease is highly recommended.

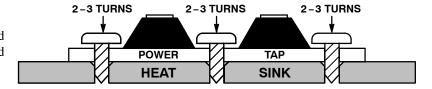
^{1.} Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2%.

MOUNTING PROCEDURE

The POWERTAP package requires special mounting considerations because of the long longitudinal axis of the copper heat sink. It is important to follow the proper tightening sequence to avoid warping the heat sink, which can reduce thermal contact between the POWERTAP and heat sink.

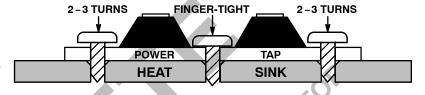
STEP 1:

Locate the POWERTAP on the heat sink and start mounting bolts into the threads by hand (2 or 3 turns).



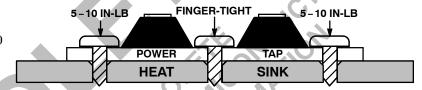
STEP 2:

Finger tighten the center bolt. The bolt may catch on the threads of the heat sink so it is important to make sure the face of the bolt or washer is in contact with the surface of the POWERTAP.



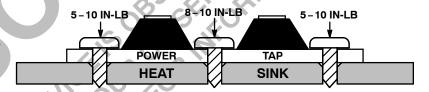
STEP 3:

Tighten each of the end bolts between 5 to 10 in-lb.



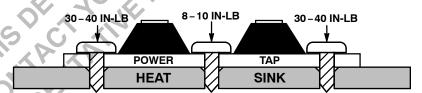
STEP 4:

Tighten the center bolt between 8 to 10 in-lb.



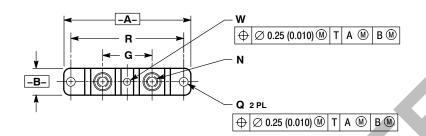
STEP 5:

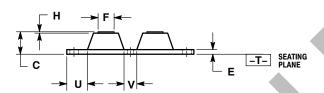
Finally, tighten the end bolts between 30 to 40 in-lb.



PACKAGE DIMENSIONS

CASE 357C-03 POWERTAP PLASTIC PACKAGE ISSUE E





NOTES

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
- 3. TERMINAL PENETRATION: 5.97 (0.235) MAXIMUM.

	INCHES		MILLIMETERS	
MIC	MIN	MAX	MIN	MAX
Α	3.450	3.635	87.63	92.33
В	0.700	0.810	17.78	20.57
С	0.615	0.640	15.63	16.26
Е	0.120	0.130	3.05	3.30
F	0.435	0.445	11.05	11.30
G	1.370	1.380	34.80	35.05
Н	0.007	0.030	0.18	0.76
N	1/4-20UNC-2B		1/4-20UNC-2B	
Q	0.270	0.285	6.86	7.23
R	3.150 BSC		80.01 BSC	
U	0.600	0.630	15.24	16.00
٧	0.330	0.375	8.39	9.52
W	0.170	0.190	4.32	4.82

1 trademarks of € trademarks,
SCILCC

1/8 for

1/8 fo ON Semiconductor and the III) are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative