

LM108/LM208/LM308 Operational Amplifiers

General Description

The LM108 series are precision operational amplifiers having specifications a factor of ten better than FET amplifiers over a -55° C to $+125^{\circ}$ C temperature range.

The devices operate with supply voltages from $\pm 2V$ to $\pm 20V$ and have sufficient supply rejection to use unregulated supplies. Although the circuit is interchangeable with and uses the same compensation as the LM101A, an alternate compensation scheme can be used to make it particularly insensitive to power supply noise and to make supply bypass capacitors unnecessary.

The low current error of the LM108 series makes possible many designs that are not practical with conventional amplifiers. In fact, it operates from 10 M Ω source resistances,

introducing less error than devices like the 709 with 10 k Ω sources. Integrators with drifts less than 500 μ V/sec and analog time delays in excess of one hour can be made using capacitors no larger than 1 μ F.

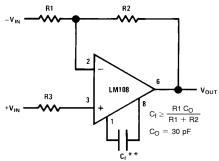
The LM108 is guaranteed from -55° C to $+125^{\circ}$ C, the LM208 from -25° C to $+85^{\circ}$ C, and the LM308 from 0° C to $+70^{\circ}$ C

Features

- Maximum input bias current of 3.0 nA over temperature
- Offset current less than 400 pA over temperature
- Supply current of only 300 µA, even in saturation
- Guaranteed drift characteristics

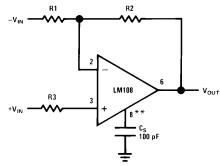
Compensation Circuits

Standard Compensation Circuit



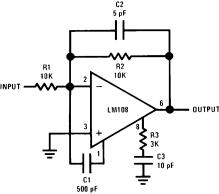
TL/H/7758-1

Alternate* Frequency Compensation



TL/H/7758-2

Feedforward Compensation



^{**}Bandwidth and slew rate are proportional to $1/C_{\mbox{\scriptsize f}}$

^{*}Improves rejection of power supply noise by a factor of ten.

 $^{^{**}\}mbox{Bandwidth}$ and slew rate are proportional to $\mbox{1/C}_{\mbox{S}}$

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications. (Note 5)

	LM108/LM208	LM308
Supply Voltage	±20V	± 18V
Power Dissipation (Note 1)	500 mW	500 mW
Differential Input Current (Note 2)	\pm 10 mA	\pm 10 mA
Input Voltage (Note 3)	± 15V	± 15V
Output Short-Circuit Duration	Continuous	Continuous
Operating Temperature Range (LM108)	-55°C to +125°C	0°C to +70°C
(LM208)	-25°C to $+85$ °C	
Storage Temperature Range	-65°C to $+150$ °C	-65°C to $+150$ °C
Lead Temperature (Soldering, 10 sec)		

DIP 260°C 260°C

 H Package Lead Temp
 300°C
 300°C

 (Soldering 10 seconds)
 300°C
 300°C

Soldering Information

Dual-In-Line Package
Soldering (10 seconds) 260°C

Small Outline Package
Vapor Phase (60 seconds) 215°C
Infrared (15 seconds) 220°C

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

ESD Tolerance (Note 6) 2000V

Electrical Characteristics (Note 4)

Parameter	Condition	LM108/LM208			LM308			Units
	Condition	Min	Тур	Max	Min	Тур	Max	Omis
Input Offset Voltage	$T_A = 25^{\circ}C$		0.7	2.0		2.0	7.5	mV
Input Offset Current	$T_A = 25^{\circ}C$		0.05	0.2		0.2	1	nA
Input Bias Current	$T_A = 25^{\circ}C$		0.8	2.0		1.5	7	nA
Input Resistance	T _A = 25°C	30	70		10	40		MΩ
Supply Current	$T_A = 25^{\circ}C$		0.3	0.6		0.3	0.8	mA
Large Signal Voltage Gain	$T_A = 25^{\circ}\text{C}, V_S = \pm 15\text{V}$ $V_{OUT} = \pm 10\text{V}, R_L \ge 10\text{ k}\Omega$	50	300		25	300		V/mV
Input Offset Voltage				3.0			10	mV
Average Temperature Coefficient of Input Offset Voltage			3.0	15		6.0	30	μV/°C
Input Offset Current				0.4			1.5	nA
Average Temperature Coefficient of Input Offset Current			0.5	2.5		2.0	10	pA/°C
Input Bias Current				3.0			10	nA
Supply Current	$T_A = +125^{\circ}C$		0.15	0.4				mA
Large Signal Voltage Gain	$V_S = \pm 15V, V_{OUT} = \pm 10V$ $R_L \ge 10 \text{ k}\Omega$	25			15			V/mV
Output Voltage Swing	$V_S = \pm 15V$, $R_L = 10 \text{ k}\Omega$	± 13	±14		±13	±14		V

Electrical Characteristics (Note 4) (Continued)

Parameter	Condition	LM108/LM208			LM308			Units	
- aramotor	Condition	Min	Тур	Max	Min	Тур	Max	20	
Input Voltage Range	$V_S = \pm 15V$	± 13.5			±14			V	
Common Mode Rejection Ratio		85	100		80	100		dB	
Supply Voltage Rejection Ratio		80	96		80	96		dB	

Note 1: The maximum junction temperature of the LM108 is 150°C, for the LM208, 100°C and for the LM308, 85°C. For operating at elevated temperatures, devices in the H08 package must be derated based on a thermal resistance of 160°C/W, junction to ambient, or 20°C/W, junction to case. The thermal resistance of the dual-in-line package is 100°C/W, junction to ambient.

Note 2: The inputs are shunted with back-to-back diodes for overvoltage protection. Therefore, excessive current will flow if a differential input voltage in excess of 1V is applied between the inputs unless some limiting resistance is used.

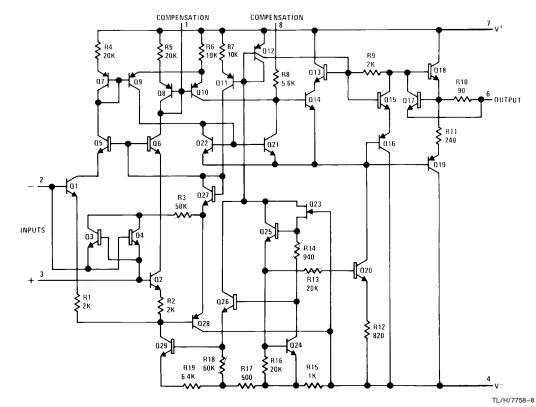
Note 3: For supply voltages less than $\pm 15V$, the absolute maximum input voltage is equal to the supply voltage.

Note 4: These specifications apply for $\pm 5\text{V} \le \text{V}_S \le \pm 20\text{V}$ and $-55^{\circ}\text{C} \le \text{T}_A \le +125^{\circ}\text{C}$, unless otherwise specified. With the LM208, however, all temperature specifications are limited to $-25^{\circ}\text{C} \le \text{T}_A \le 85^{\circ}\text{C}$, and for the LM308 they are limited to $0^{\circ}\text{C} \le \text{T}_A \le 70^{\circ}\text{C}$.

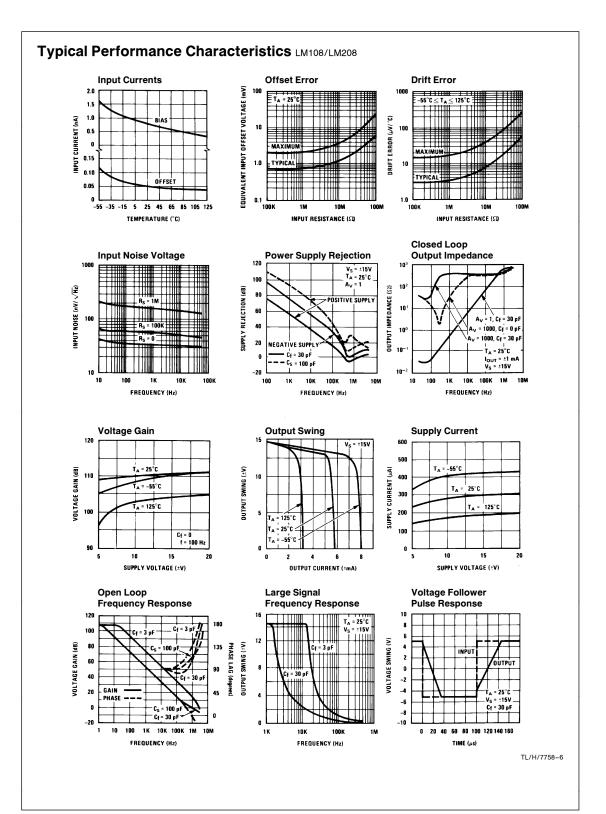
Note 5: Refer to RETS108X for LM108 military specifications and RETs 108AX for LM108A military specifications.

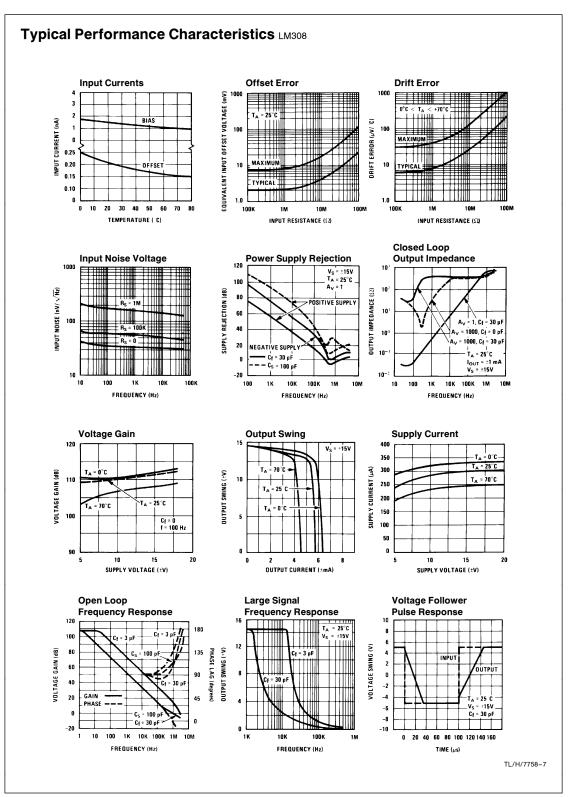
Note 6: Human body model, 1.5 k Ω in series with 100 pF.

Schematic Diagram

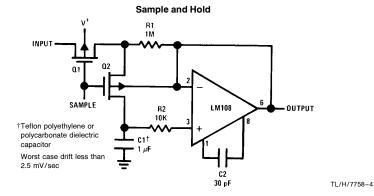


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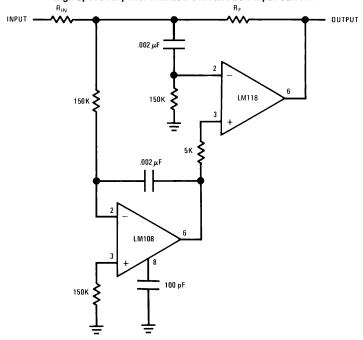




Typical Applications



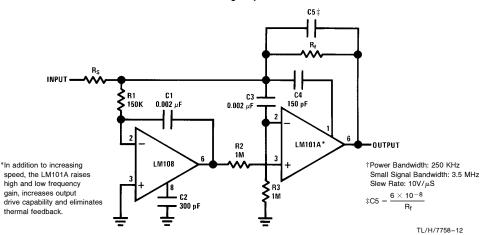
High Speed Amplifier with Low Drift and Low Input Current



TL/H/7758-5

Typical Applications (Continued)

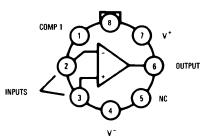
Fast† Summing Amplifier



Connection Diagrams

Metal Can Package

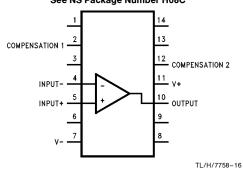
COMP 2



TL/H/7758-13

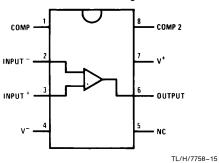
- *Package is connected to Pin 4 (V-)
- **Unused pin (no internal connection) to allow for input anti-leakage guard ring on printed circuit board layout.

Order Number LM108H, LM108H/883, LM308AH or LM308H See NS Package Number H08C



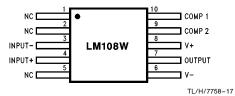
Top View
Order Number LM108J/883
See NS Package Number J14A

Dual-In-Line Package



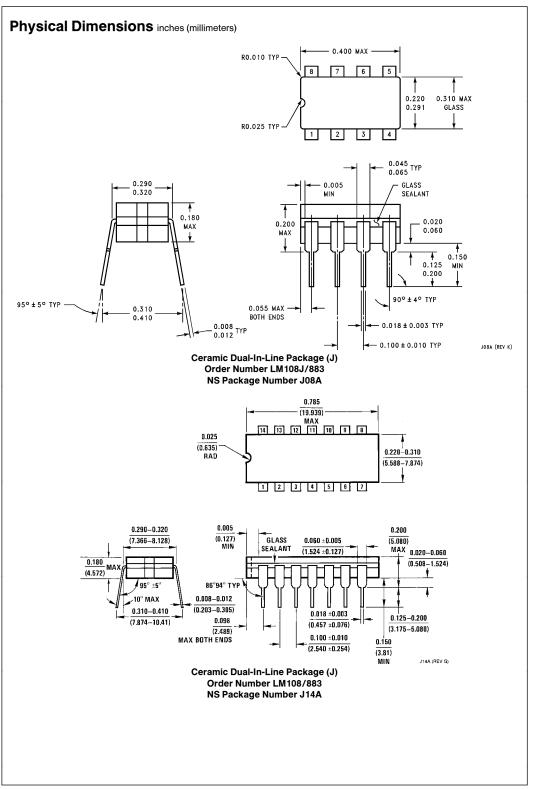
Top View

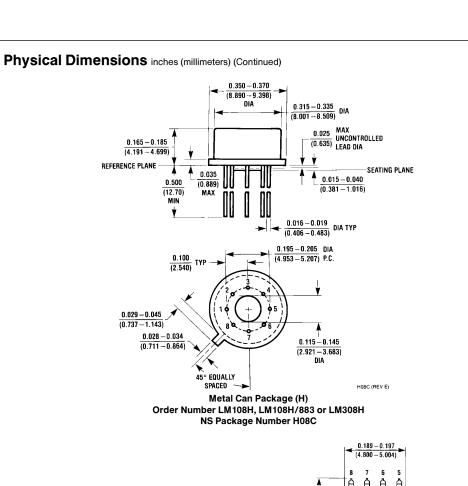
Order Number LM108J-8/883, LM308M or LM308N See NS Package Number J08A, M08A or N08E

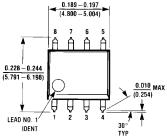


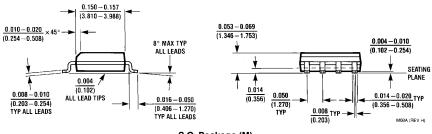
Order Number LM108W/883 See NS Package Number W10A

†Also available per JM38510/10104

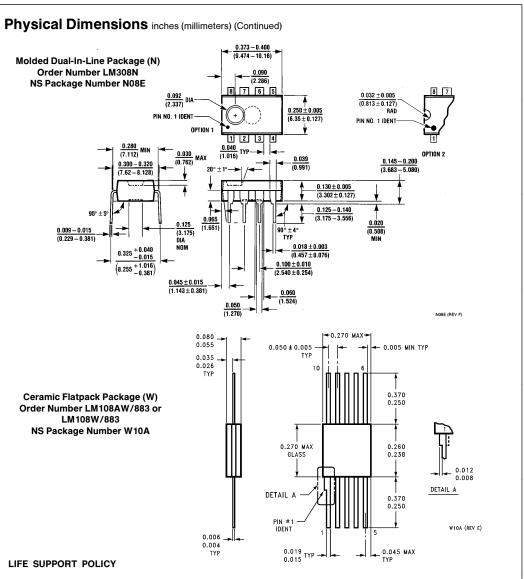








S.O. Package (M) Order Number LM308M NS Package Number M08A



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LM308 Operational Amplifier

Generic P/N 308

LM308

Contents

- General Description
- Features
- Datasheet
- Package Availability, Models, Samples & Pricing
- Design Tools
- Application Notes

Parametric Table							
Channels (Channels)	1						
Input Output Type	Not Rail to Rail						
Bandwidth, typ (MHz)	1						
Slew Rate, typ (Volts/usec)	.30						
Minimum Supply Voltage (Volt)							
Maximum Supply Voltage (Volt)							
Offset Voltage, Max (mV)							
Input Bias Current, Temp Max							
Output Current, typ (mA)							
Voltage Noise, typ (nV/Hz)							

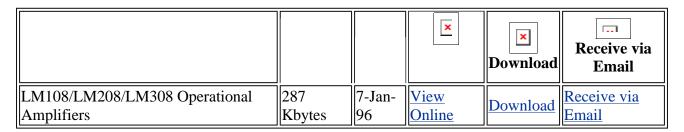
General Description

rejection to use unregulated supplies. Although the circuit is interchangeable with and uses the same compensation as the LM101A, an alternate compensation scheme can be used to make it particularly insensitive to power supply noise and to make supply bypass capacitors

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- Offset current less than 400 pA over temperature
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Datasheet



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Package Availability, Models, Samples & Pricing

Part	Package		Package Status Models Samples &		Budgetary Pricing		Std Pack	Package		
Number	Type	# pins		SPICE	IBIS	Electronic Orders	Quantity	\$US each	Size	Marking
LM308H	TO-5	8	Full production	N/A	N/A	· ×	1K+	\$0.8400	box of 500	[logo]¢Z¢ 2¢T LM308H
LM308 MDC	di	e	Full production	N/A	N/A				N/A	-
LM308 MWC	wa	fer	Full production	N/A	N/A				N/A	-

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Application Notes

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Linear Brief 28 General Purpose Power					Rece via Ema
AN-161: IC Voltage Reference has 1 ppm per Degree					
References for A/D Converters					
Application Note 222 Super Matched Bipolar Transistor Pair Sets New Standards for Drift					
The Phase Locked Loop IC as a Communication System Building Block		- Nov- 95			

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