

**PRELIMINARY**

Notice: This is not a final specification.  
Some parametric limits are subject to change.

MITSUBISHI SEMICONDUCTOR <GaAs FET>

**MGFS45V2527**

**2.5~2.7GHz BAND 28W INTERNALLY MATCHED GaAs FET**

**DESCRIPTION**

The MGFS45V2527 is an internally impedance-matched GaAs power FET especially designed for use in 2.5~2.7 GHz band amplifiers. The hermetically sealed metal-ceramic package guarantees high reliability.

**FEATURES**

- Class A operation
- Internally matched to 25Ω system
- High output power  
P<sub>1dB</sub> = 28W (TYP) @ 2.5~2.7GHz
- High power gain  
G<sub>LP</sub> = 12dB (TYP) @ 2.5~2.7GHz
- High power added efficiency  
η<sub>add</sub> = 40% (TYP) @ 2.5~2.7GHz, P<sub>1dB</sub>
- Hermetically sealed metal-ceramic package
- Low distortion [Item: -51]  
IM<sub>3</sub> = -45dBc (TYP) @ P<sub>o</sub> = 34 (dBm) S.C.L.

**APPLICATION**

Item-01: 2.5~2.7 GHz band power amplifiers.  
Item-51: Digital radio communication.

**QUALITY GRADE**

- IG

**ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub> = 25°C)**

Symbol	Parameter	Rating	Unit
V <sub>GD0</sub>	Gate to drain voltage	-15	V
V <sub>GS0</sub>	Gate to source voltage	-15	V
I <sub>D</sub>	Drain current	24	A
I <sub>GR</sub>	Reverse gate current	-60	mA
I <sub>GF</sub>	Forward gate current	126	mA
P <sub>T</sub>	Total power dissipation *1	100	W
T <sub>ch</sub>	Channel temperature	175	°C
T <sub>stg</sub>	Storage temperature	-65 ~ +175	°C

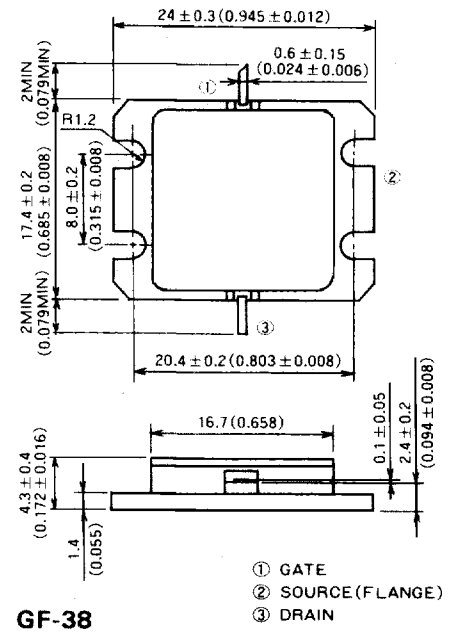
\*1 T<sub>c</sub> = 25°C

**ELECTRICAL CHARACTERISTICS (T<sub>a</sub> = 25°C)**

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
I <sub>DSS</sub>	Saturated drain current	V <sub>DS</sub> = 3V, V <sub>GS</sub> = 0V	—	20	—	A
g <sub>m</sub>	Transconductance	V <sub>DS</sub> = 3V, I <sub>D</sub> = 6.4A	—	6	—	S
V <sub>GS(off)</sub>	Gate to source cut-off voltage	V <sub>DS</sub> = 3V, I <sub>D</sub> = 120mA	—	—	-4.5	V
P <sub>1dB</sub>	Output power at 1dB gain compression	V <sub>DS</sub> = 10V, I <sub>D</sub> = 6.4A, f = 2.5~2.7GHz	44	45	—	dBm
G <sub>LP</sub>	Linear power gain		11	12	—	dB
I <sub>D</sub>	Drain current		—	7	—	A
η <sub>add</sub>	Power added efficiency		—	40	—	%
IM <sub>3</sub>	3rd order IM distortion *1		-42	-45	—	dBc
R <sub>th(ch-o)</sub>	Thermal resistance *2	ΔV <sub>f</sub> method	—	—	1.6	°C/W

\*1: Item-51, 2-tone test P<sub>o</sub> = 34dBm Single Carrier Level f = 2.7GHz Δf = 5MHz, \*2: Channel to case

**OUTLINE DRAWING** Unit: millimeters (inches)



**RECOMMENDED BIAS CONDITIONS**

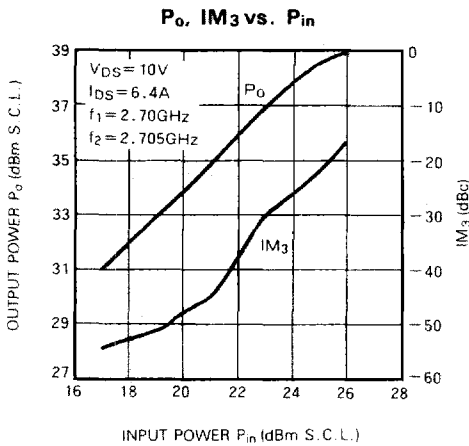
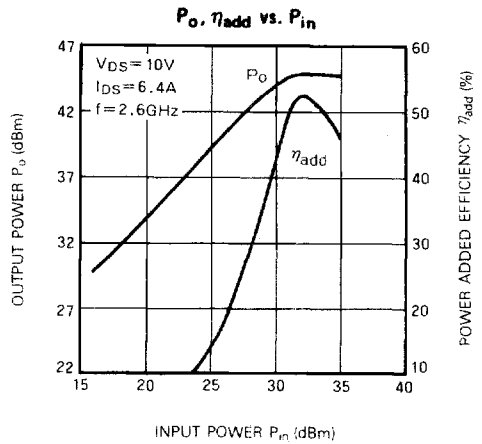
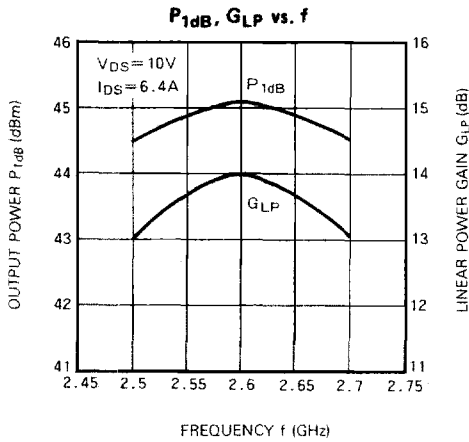
- V<sub>DS</sub> = 10V
- I<sub>D</sub> = 6.4A
- R<sub>g</sub> = 25Ω
- Refer to Bias Procedure

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**TYPICAL CHARACTERISTICS (Ta = 25°C)**



**S PARAMETERS (Ta = 25°C, V<sub>DS</sub> = 10V, I<sub>DS</sub> = 6.4A)**

f (GHz)	S Parameter (TYP.)							
	S <sub>11</sub>		S <sub>21</sub>		S <sub>12</sub>		S <sub>22</sub>	
	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)
2.40	0.580	7	3.69	179	0.026	135	0.61	73
2.45	0.486	-10	4.01	165	0.029	123	0.55	67
2.50	0.380	-37	4.36	148	0.035	103	0.47	60
2.55	0.310	-71	4.49	130	0.037	92	0.41	47
2.60	0.300	-112	4.61	112	0.043	69	0.29	41
2.65	0.320	-150	4.63	95	0.042	52	0.22	32
2.70	0.370	-179	4.56	78	0.039	33	0.14	29
2.75	0.410	157	4.43	62	0.042	15	0.07	21
2.80	0.440	138	4.29	47	0.037	-2	0.02	87