Lednium Series Optimal X (10-watts,120° Viewing Angle)

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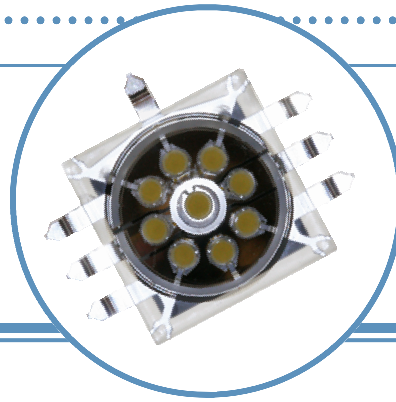
OVTL09LG3x Series

- Revolutionary 3-dimensional packaged LED source
- Robust energy-efficient design with long operating life
- Low thermal resistance (2.5°C/W)
- Exceptional spatial uniformity
- Available in amber, blue, green, red, cool white, daylight white, warm white and multi-colored



The **OVTL09LG3x Series** surface mount provides a 10-Watt energy-efficient 3-dimensional packaged LED source that offers high luminance, low thermal resistance @ 2.5°C/W and a long operating lifespan. Devices offer a 120° viewing angle and are available in amber, blue, green, red, cool white, daylight white, warm white and multi-colored.

Applications

- Automotive exterior and interior lighting
- Architectural lighting
- Electronic signs and signals

Flux Characteristics (I_F = 1.05 A, T_J = 25° C)

Part Number	Viewing Angle	Emitted Color	Typical Luminous Flux (lm)	Lens Color
OVTL09LG3A	120°	Amber	270	Water Clear
OVTL09LG3B		Blue	70	
OVTL09LG3G		Green	250	
OVTL09LG3R		Red	230	
OVTL09LG3W		Cool White	250	
OVTL09LG3WD		Daylight White	275	
OVTL09LG3WW		Warm White	230	
OVTL09LG3M		Red/Green/Blue	221	

RoHS Moisture

ATTENTION
OBSERVE PRECAUTIONS
ELECTROSTATIC
SENSITIVE DEVICES

DO NOT LOOK DIRECTLY AT LED WITH UNSHIELDED EYES OR DAMAGE TO RETINA MAY OCCUR.

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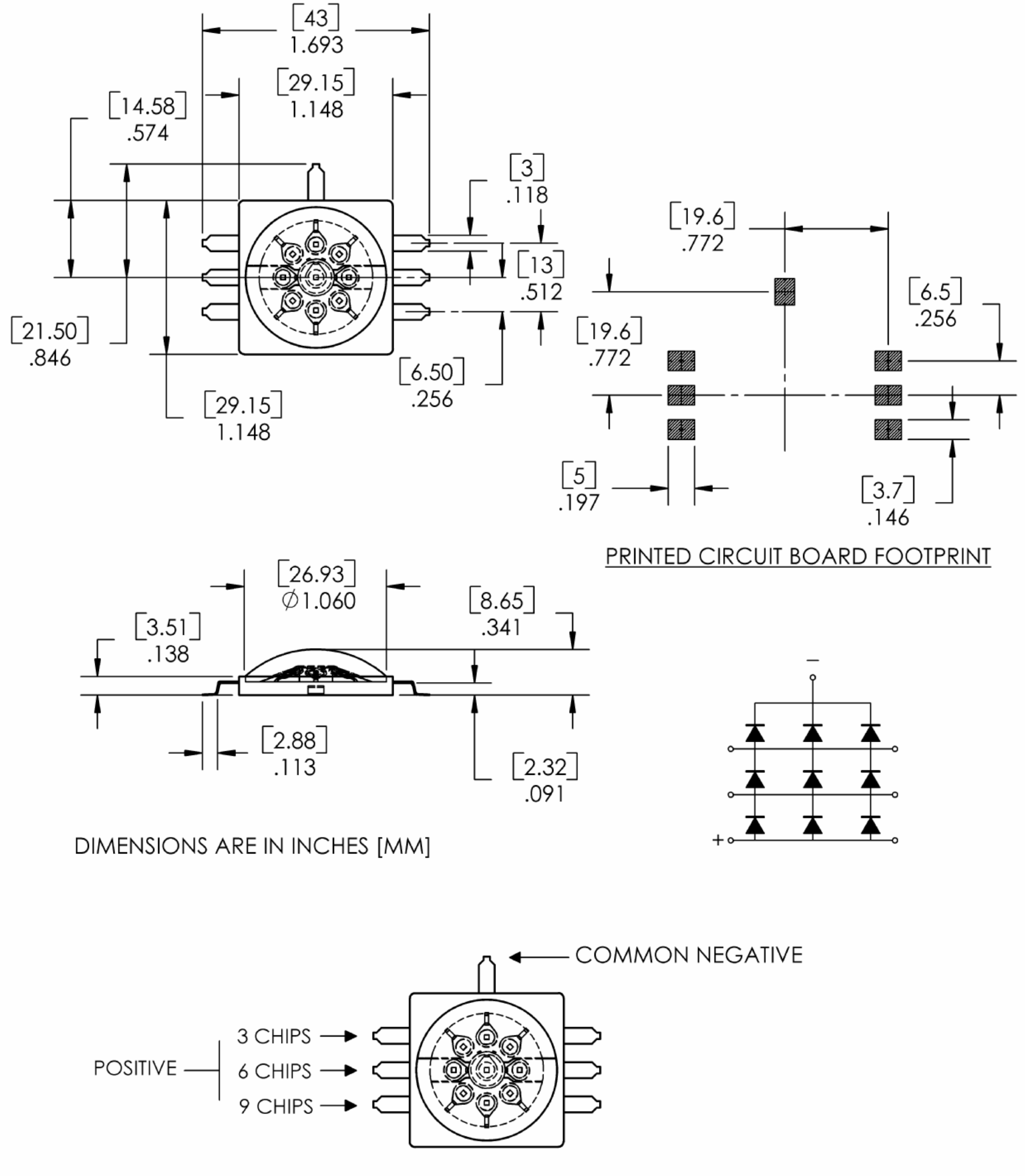
Package Drawing:



OPTEK Technology Inc. — 1645 Wallace Drive, Carrollton, Texas 75006
Phone: (972) 323-2200 or (800) 341-4747 FAX: (972) 323-2396 visibleLED@optekinc.com www.optekinc.com

Absolute Maximum Ratings

DC Forward Current	1.05 A
Peak Pulsed Forward Current[1]	3 A
Reverse Voltage	15 V
Maximum Allowable Junction Temperature[2]	130° C
Storage and Operating Temperature	-50° ~ +85 °C

Notes:
1. Pulse width 1 ms maximum, duty cycle 1/16.
2. Thermal resistance junction to board (T_{JB}) is 2.5° C/W.

Electrical Characteristics (I_F = 1.05 A, T_J = 25° C)

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS
V_F	Forward Voltage **(Amber)**	5.7	6.9	7.8	V
	Forward Voltage **(Blue)**	8.7	10.2	11.1	V
	Forward Voltage **(Green)**	9.6	10.8	12.0	V
	Forward Voltage **(Red)**	5.7	6.9	7.8	V
	Forward Voltage **(Red/Green/Blue)**	8.5	9.2	9.9	V
	Forward Voltage **(White)**	8.7	10.2	11.1	V
	V_F Temperature Co-efficient **(Amber, Red)**	----	-6.0	----	mV/°C
	V_F Temperature Co-efficient **(White, Blue)**	----	-4.8	----	mV/°C
	V_F Temperature Co-efficient **(Green)**	----	-5.0	----	mV/°C
2 Θ½	50% Power Angle	----	120	----	deg

Optical Characteristics (I_F = 1.05 A, T_J = 25° C)

COLOR	DOMINANT WAVELENGTH			SPECTRAL FULL-WIDTH-HALF-MAXIMUM	DOMINANT WAVELENGTH TEMPERATURE DEPENDENCE
	MIN	TYP	MAX		
Amber	590	595	600	16 nm	0.08 nm/° C
Blue	455	460	465	24 nm	0.05 nm/° C
Green	510	515	520	40 nm	0.04 nm/° C
Red	620	625	630	18 nm	0.05 nm/° C

Color	Minimum CCT (°K)	Maximum CCT (°K)	Chromaticity Coordinates				
Cool White	6400	7600	C_x	.298	.306	.313	.317
			C_y	.314	.288	.34	.307
Daylight White	5200	6400	C_x	.313	.318	.334	.338
			C_y	.341	.300	.326	.382
Warm White	3200	3800	C_x	.395	.395	.435	.435
			C_y	.372	.390	.426	.443

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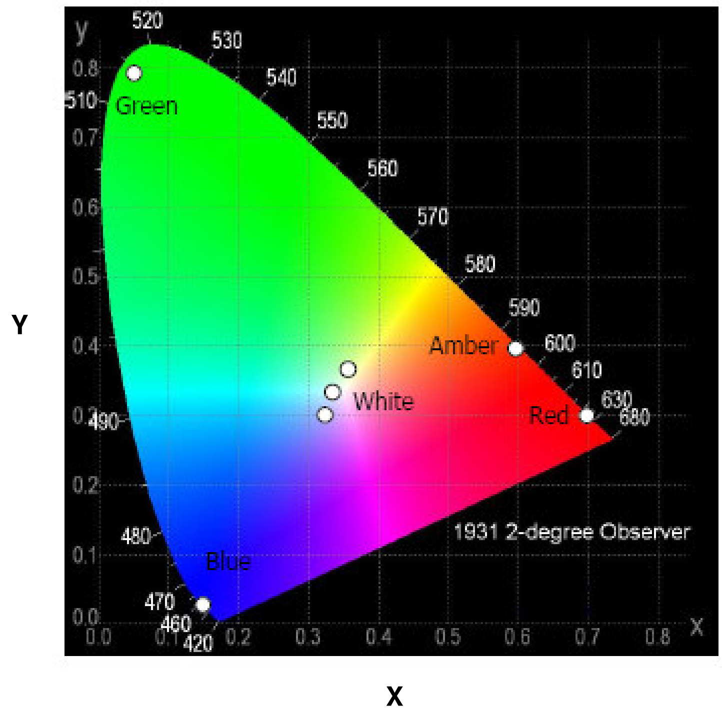
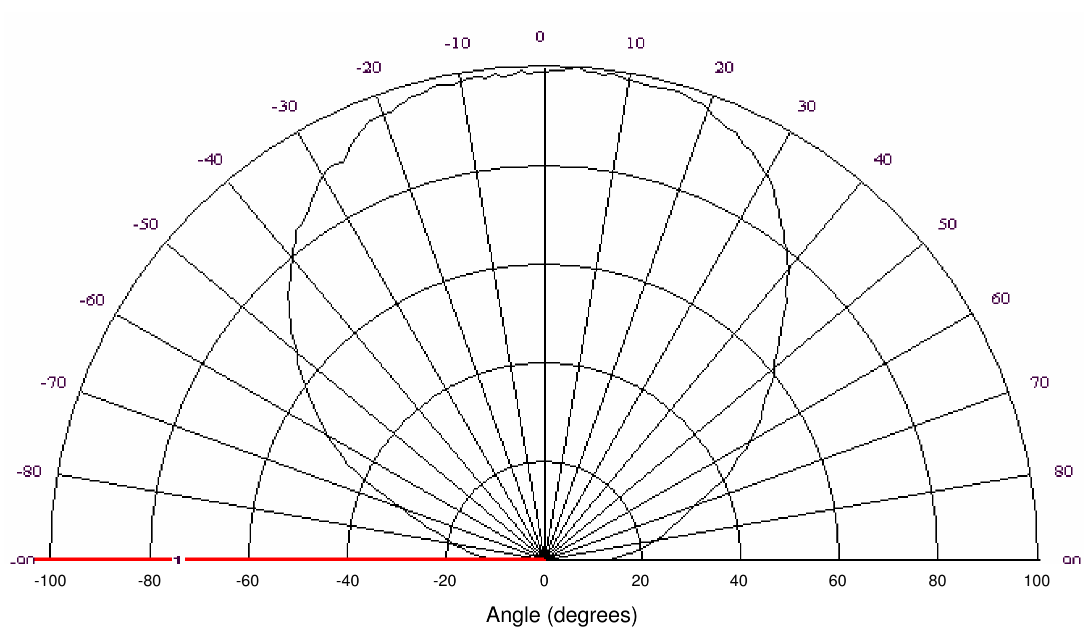
CIE Chromaticity Diagram



Spatial Intensity Distribution



Normalized Spectral Intensity vs Angular Displacement

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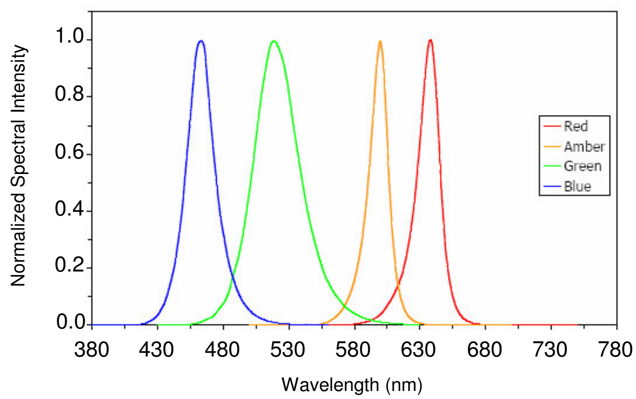
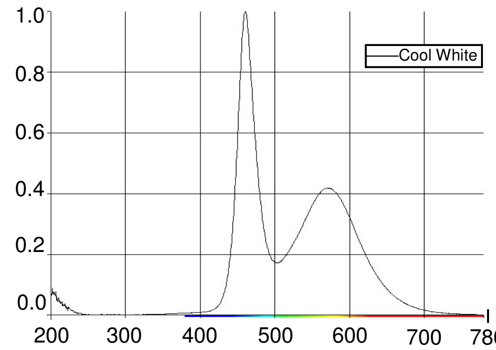
Typical Electro-Optical Characteristics Curves

Input Current = 1.05 A, T_J = 25° C



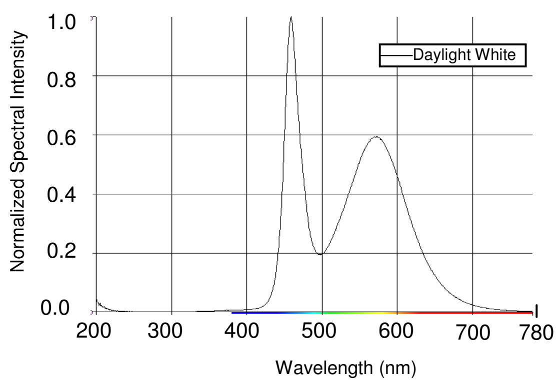
Wavelength Characteristics

Input Current = 1.05A, T_J = 25° C



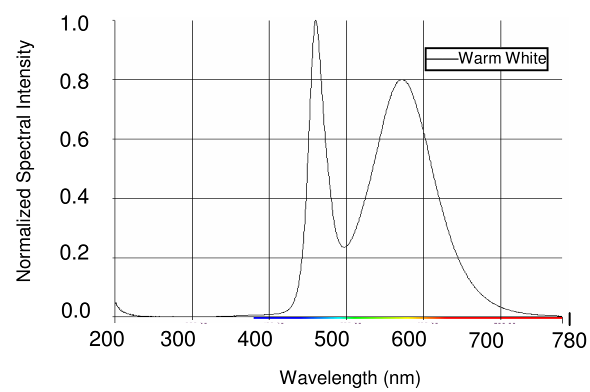
Wavelength Characteristics

Input Current = 1.05A, T_J = 25° C



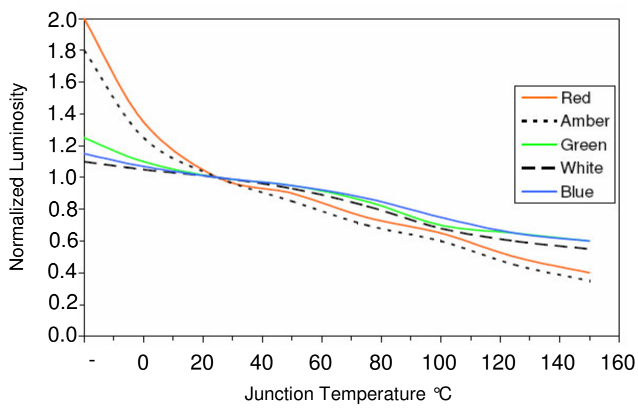
Wavelength Characteristics

Input Current = 1.05A, T_J = 25° C



Wavelength Characteristics

Luminosity normalized to T_J = 25° C



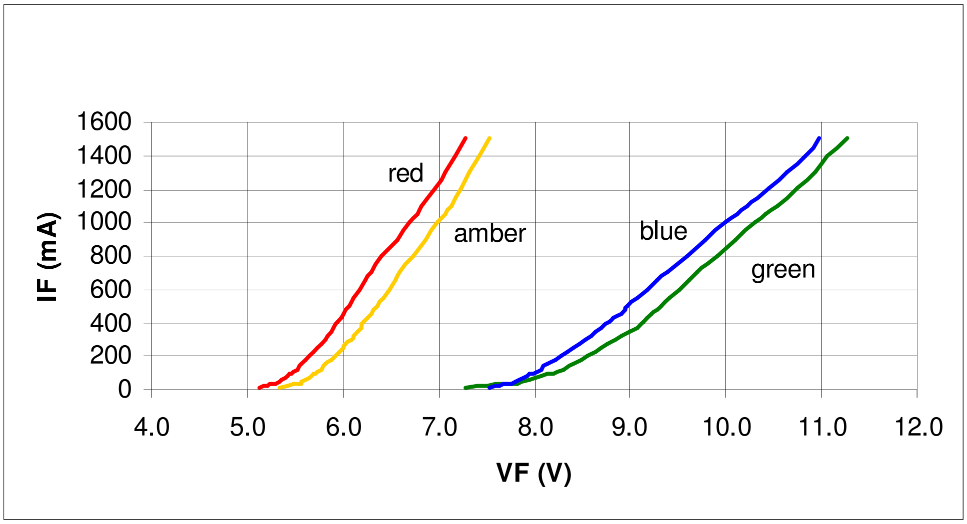
Junction Temperature Characteristics

Luminosity normalized to T_J = 25° C

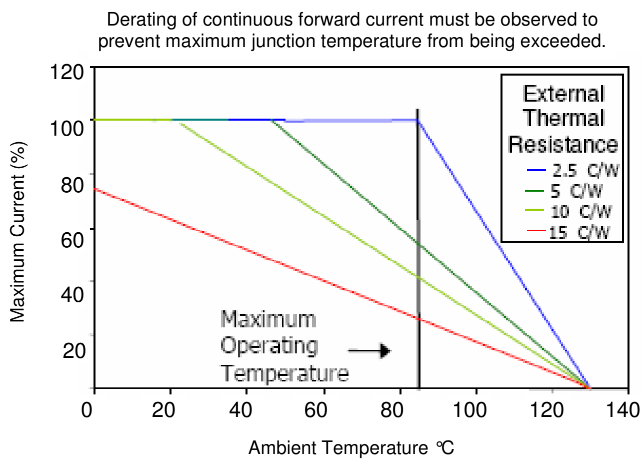
OPTEK Part Number	% Normalized Luminosity at Junction Temperature (° C)					
	0	25	50	75	100	125
OVTLO9LG3A	125	100	85	70	60	45
OVTLO9LG3B	107	100	95	87	75	65
OVTLO9LG3G	110	100	95	85	70	65
OVTLO9LG3R	135	100	90	75	65	50
OVTLO9LG3W	105	100	93	82	68	60
OVTLO9LG3WD	105	100	93	82	68	60
OVTLO9LG3WW	105	100	93	82	68	60

Junction Temperature Characteristics

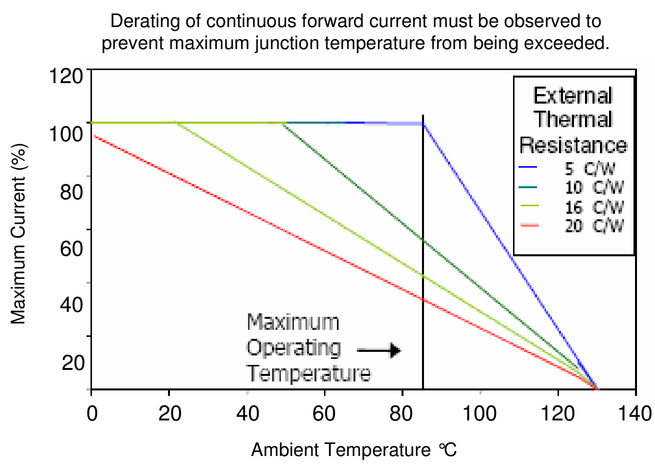
OPTEK reserves the right to make changes at any time in order to improve design and to supply the best product possible.



Forward Current vs. Forward Voltage



Derating Curves - Blue, Green and White LEDs



Derating Curves - Amber and Red LEDs

Critical Thermal Conditions (To maintain junction temperature (T_J) at 85° C)

	WHEN MOUNTED ON:				
	FR4 PC BOARD	IMS	SPREADER PLATE	3x3x1 in. FIN EXTRUSION	ACTIVE HEATSINK
USE SAFE OPERATING CURRENT OF:	200 mA	500 mA	700 mA	800 mA	1000 mA

NOTE: Refer to OPTEK Application Note #228 on thermal management (www.optekinc.com/pdf/AppNote228.pdf).

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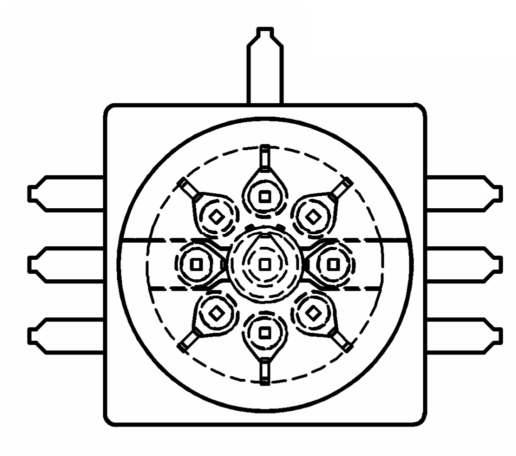
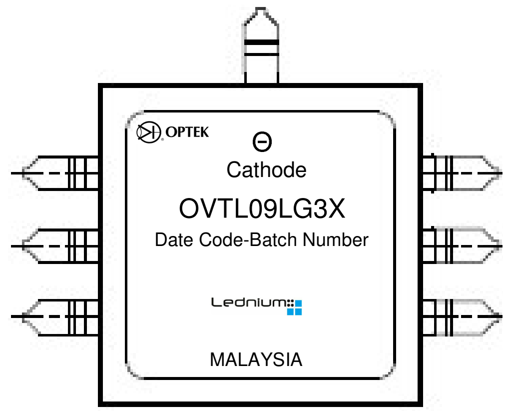
OPTEK 10-watt Lednium Markings



FRONT



BACK

Packaging: 25 pieces per tray

OPTEK's Lednium Series Solid State Lighting products package the highest quality LED chips. Typically, the lumen output of these chips can be as high as 70% after 50,000 hours of operation. This prediction is based on specific test results and on tests on similar materials, and relies on strict observation of the design limits and ratings included in this data sheet.

Thermal Resistance

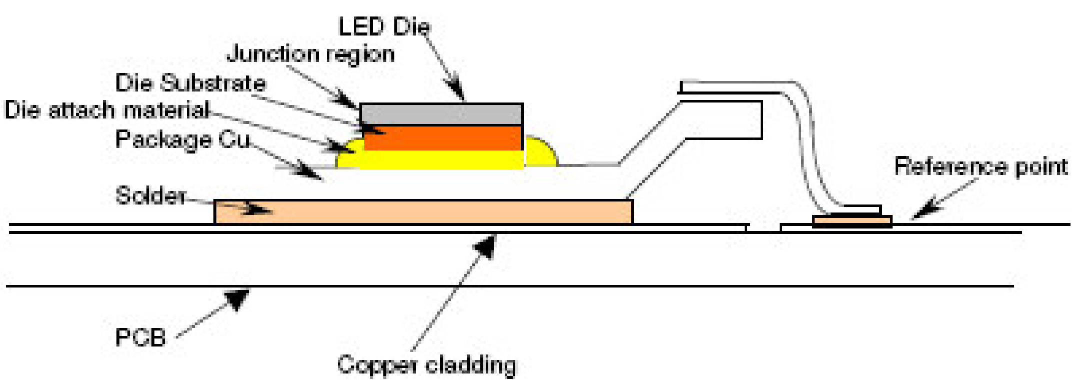
Optek Lednium Series 1-watt Cup – Measured value 2°C/w (OVTL01LGAxx)

Optek Lednium Series 10-watt Matrix – Measured value 2.5°C/w (OVTL09LG3xx)

Theory

In line with industry practice, the thermal resistance (Rth) of our LED packages is stated as $R\theta_{j\text{-}b}$, thermal resistance from the junction region (j) of the die, to the board (b) - PCB or other mounting surface. What this means in a practical sense, is that when operating at rated input (1watt approx.) the junction of a die in a cup product will attain a temperature that is 2°C higher than a reference point on the mounting surface beneath it. In the case of a 10-watt Matrix product, the maximum temperature difference between any junction and the reference point is 25°C (2.5°C/w x 10w). The thermal path thus quantified is a composite of a number of thermally resistive elements in a series and or parallel configuration, but lumped together into a single parameter for convenience.

For an end user of LED products then, this constant allows the junction temperature to be determined by a simple measurement of the temperature of the mounting surface. Optek recommends that the design value of sustained die junction temperature be limited to 80°C. In an ambient temperature of 25°C, the board temperature of a 10-watt device must be constrained below 55°C to comply with this recommendation, and for a 1-watt cup the board can theoretically operate at up to 78°C.



From the diagram above it can be seen that the heat generated in the junction region follows a somewhat serial conductive path through the package to the major radiating surface – which in this example is a single sided PCB. Some additional radiation may occur directly from the upper surface of the package (not shown). This would be conducted upward from the die surface through the transparent encapsulating material to the package surface and be radiated from there. To all practical purposes this is a very minor effect. The polymer encapsulants in normal use are poor conductors of heat.

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Typical elements in the conducting path and corresponding nominal thermal conductivities are:

	Elements	w/mK
Epilayers	GaN/InGaN	150
Substrate	Sapphire	50
Die attach material	Conductive epoxy	10
Package	Silver plated copper	350
Solder	Solder (Sn/Ag/Cu)	35
Copper cladding	Copper	300

Note : Thermal conductivity is a physical constant. For the materials above, the respective contribution each makes to the overall thermal resistance ($R\theta_{j\text{-}b}$) is a function of the thickness of each material layer, and the surface area. Thermal Conductivity (TC) is defined to be the heat conducted in time (t), through thickness (T) in a direction normal to a surface area (A), due to a temperature difference (δT).

Therefore TC= q/t x {T/[A x δT]}

and δT = [Q x T]/[A x TC] where δT = Temp. difference (K)
Q = Power (w)
A = Surface area (m^2)
T = layer thickness (m)
TC = Thermal Conductivity (w/mK)

Theoretical Calculation (for 1 watt dissipated in a cup product via a single 40mil die)

GaN	Thickness approx 10 x 10^{-6} Area 10^{-6}	= 1 x 10x10^{-6}/ 10^{-6} x 150 = 0.07 K
Substrate	T = 60 x 10^{-6}	= 1 x 60x10^{-6}/ 10^{-6} x 50 = 1.2 K
Die attach	T = 20 x 10^{-6} A = 2 x 10^{-6}	= 1 x 20x10^{-6} / 2x10^{-6} x 10 = 1
Package	T = 0.4x10^{-3} A = 6x10^{-6}	= 1 x 0.4x10^{-3}/ 6x10^{-6} x 350 = 0.19
Solder	T = 60x10^{-6} A = 6x10^{-6}	= 1 x 60x10^{-6}/6x10^{-6} x 25 = 0.4

Total Calculated δT = 2.86K

Power input is 1 watt; however, some power is converted into light energy. Assuming this is of the order of 200mw, the adjusted value of δT is 2.29K. The calculation now assumes that all of the dissipation, 800mw of heat, is conducted along the thermal path, thereby ignoring any conduction and subsequent radiation that is not directionally normal to the surfaces considered, ie: conduction through the encapsulant material vertically away from the board, and conduction horizontally away from the heat source. The calculation also assumes that there is no contribution to thermal resistance at the boundaries between material layers. In practice it is improbable that perfect transfer will occur at these transition regions, even though the bonding between layers in this example are of high quality. In general, the calculation indicates that the measurements below are of the order of magnitude that can be expected.

The alternate matrix product range is of a much more complicated thermal design, which does not lend itself to a simple theoretical calculation similar to that shown above. There are multiple incident heat sources, parallel heat conduction paths, and significantly larger surface area for stray radiation, eg. Cup above has a surface area available for stray radiation of approximately, 25mm^2 per watt of input power. A 10-watt matrix product has approximately 92.5mm^2 of exposed surface per input watt.

Measurements

The key to an accurate measurement of thermal resistance is to obtain a reliable value for the junction temperature (Tj). Since the die itself is, and must be, encapsulated during testing, and the junction is contained within the structure of the die, direct measurement of the junction temperature by normal means is not possible.
Two methods of non-contact thermography are available, both of which rely on emitted infrared detection.

Infrared imagery by calibrated radiograph is a possibility; however, in the instance of a cup product only a small value of δT is expected which makes accurate estimation of the actual temperature gradient difficult using colorimetry.

The alternative measurement type is digital infrared thermography. This means there is an inherent uncertainty in the calculation algorithm, which sometimes gives results considered unacceptably inaccurate. In this instance absolute accuracy is of secondary importance because the value to be determined is a temperature difference (δT) which requires only relative values – any error in a first reading will also be present in subsequent readings that are about the same value. The difference between readings is accurate.

The other significant drawback to infrared thermometers is a limitation to minimizing the spot size over which the measurement is made. This poses a difficulty for small assemblies like an LED cup, and in particular the added complication that the calculated temperature is an average value for the area being interrogated further complicates the issue. Another concern is sometimes raised about the ability of this type of instrument to detect a heated surface beyond the closest transparent radiating surface. This is a significant issue for far field measurements; however, it is simple to demonstrate that this does not hold true for the near field, and particularly when the incident beam has a known focal length.

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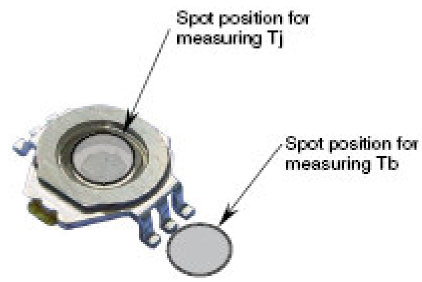
Measurement
Instrument: IR Thermometer
Auto ranging: -100 to 1200°C
Spot size 3mm D.
Focus 25.4mm

Cup Product

Input 350mA at 3.3V(1watt)


Device under test



Averaged Test Results

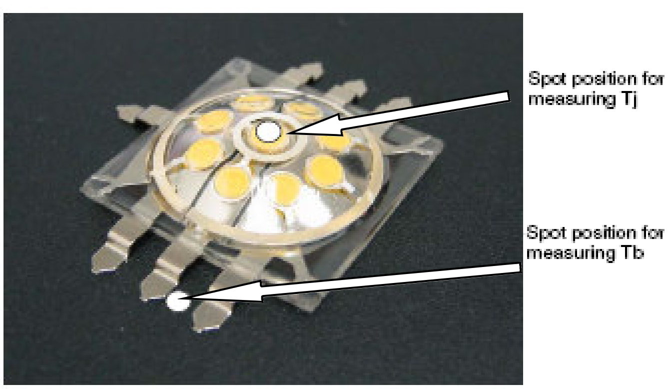
Tj	Tb	δT	**Rth**
32	30.2	1.8	**1.8°C/W**

Matrix Product

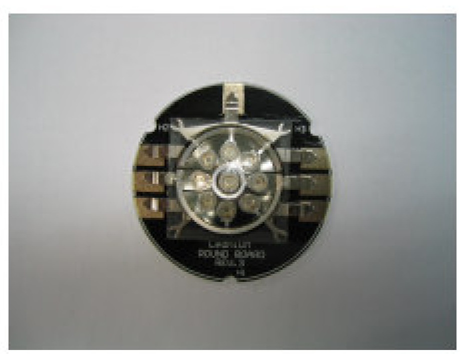
Input 1050mA at 10.2V(10.7watts)

Averaged Test Results

Tj	Tb	δT	**Rth**
89	62	27	**2.52°C/W**



Measurement points


Test set-up on MCPCB

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